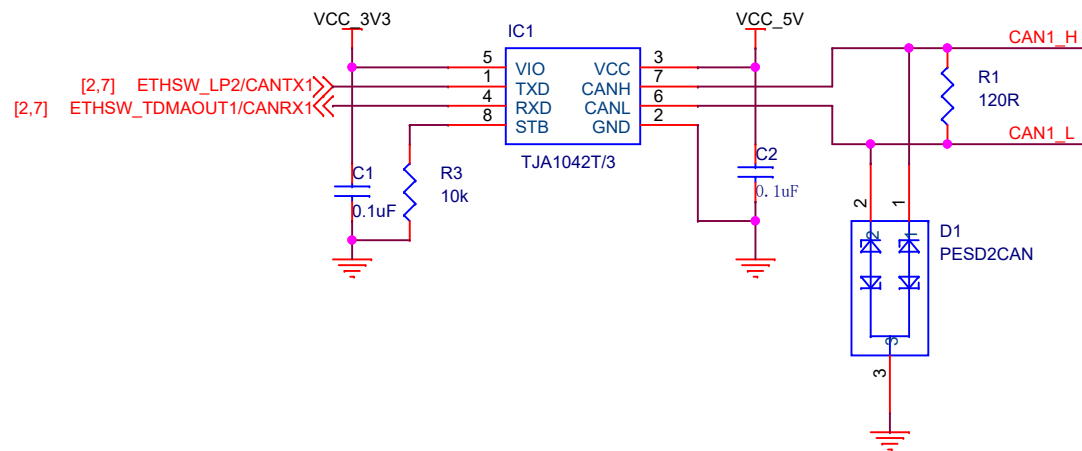
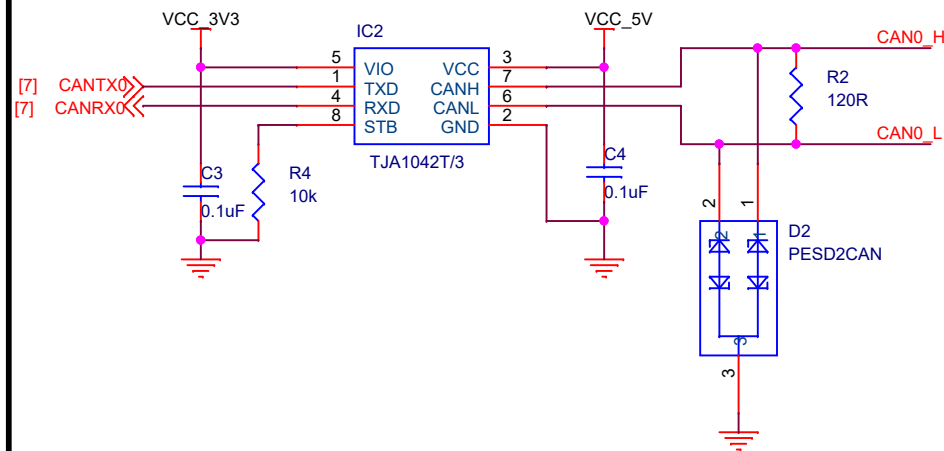


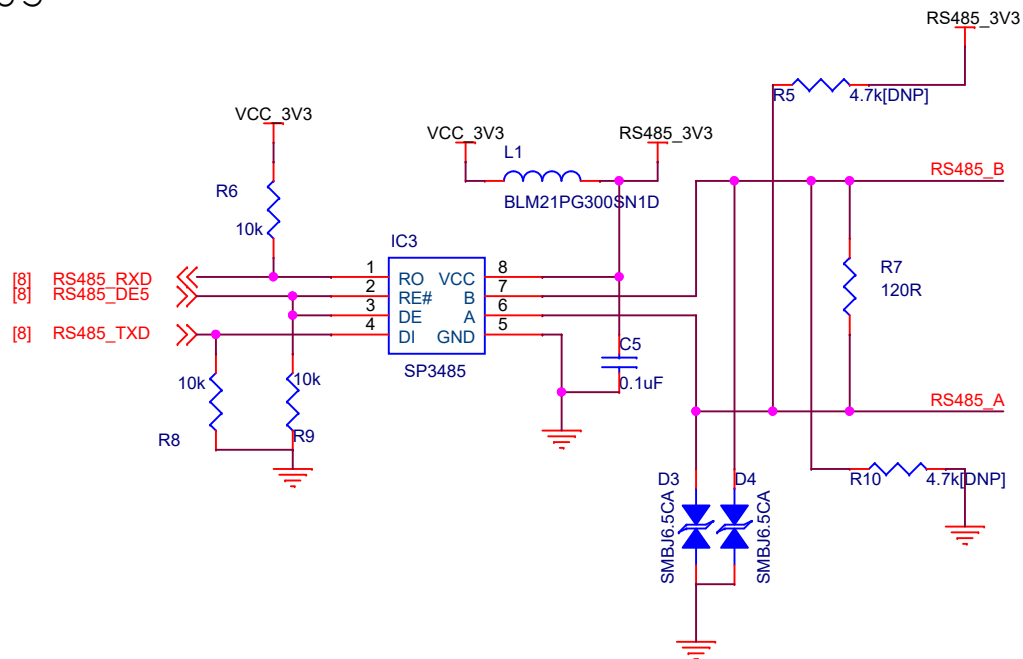
CAN1



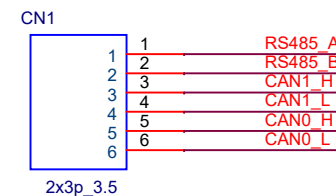
CAN0



RS485



接线端子



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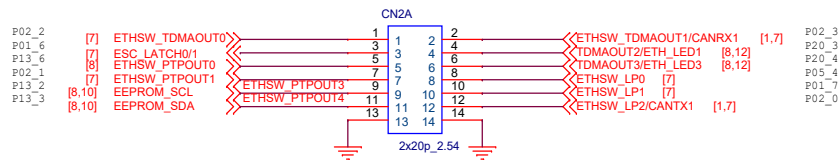
Title RT-EtherKit以太网开发板

Size A4 Document Number <CAN&RS485>

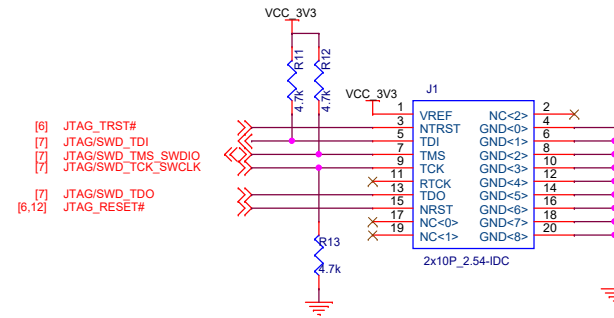
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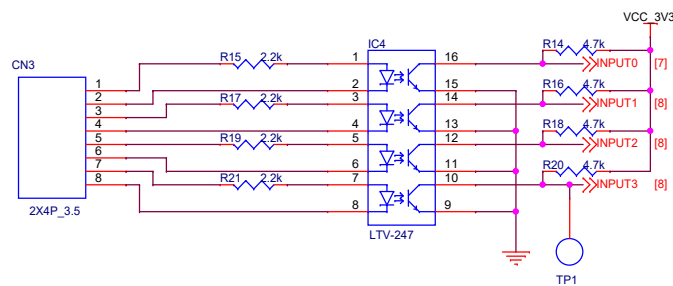
ETH相关IO



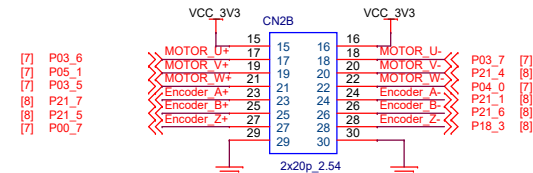
JTAG调试接口



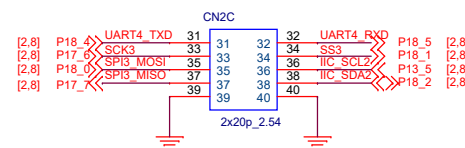
INPUT



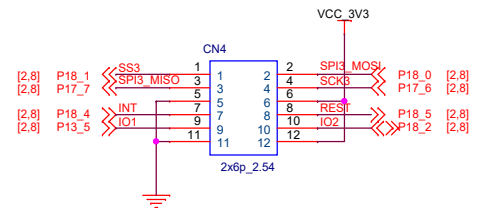
timer



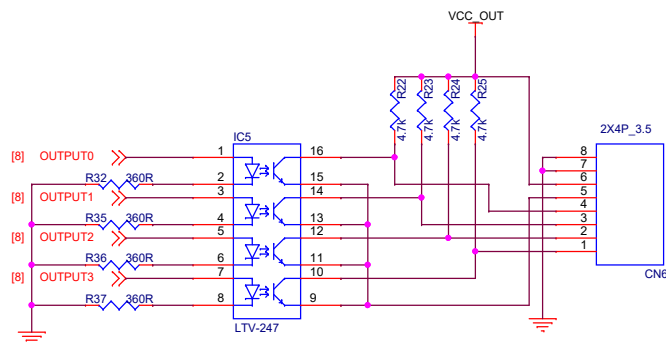
Serial device IO



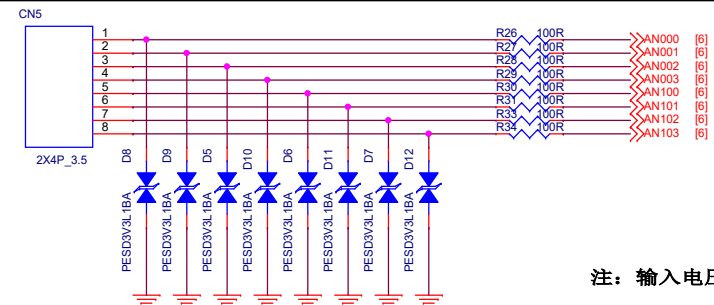
PMOD-SPI



OUTPUT

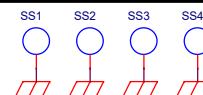


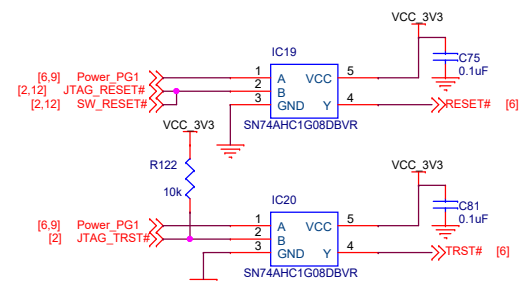
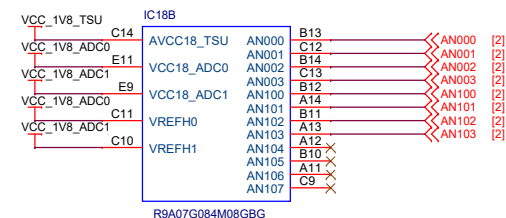
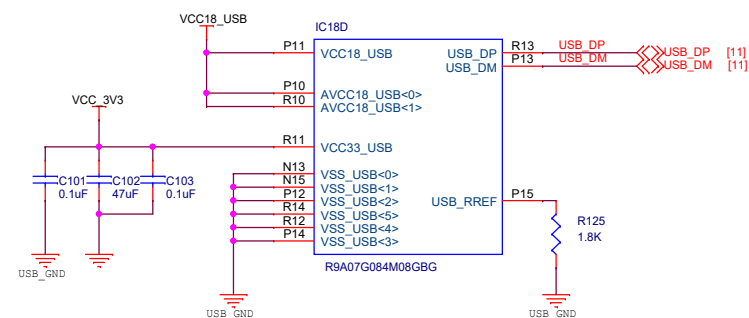
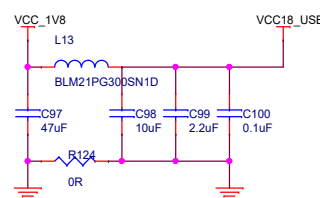
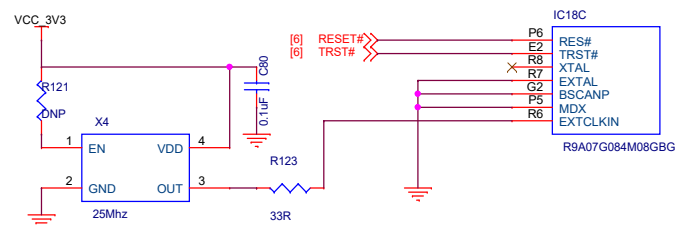
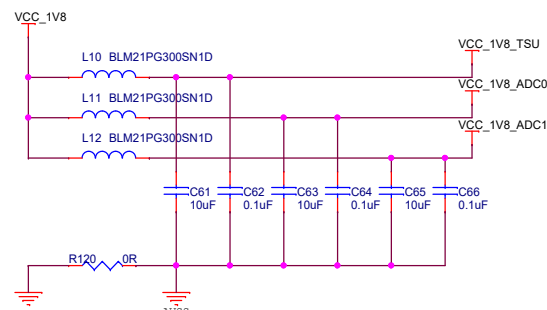
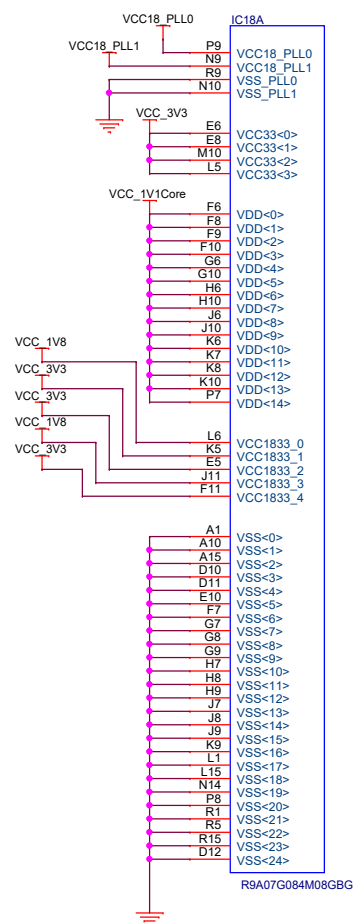
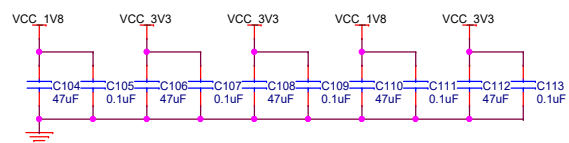
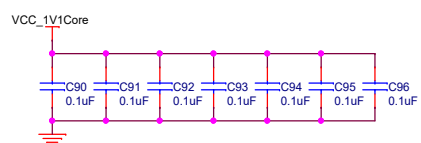
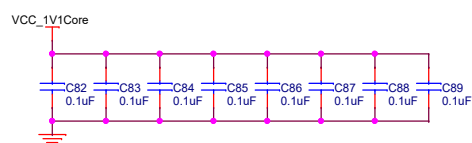
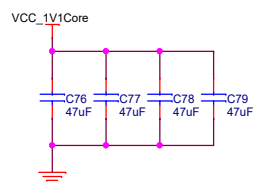
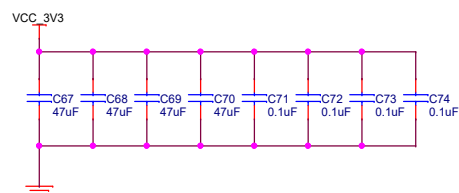
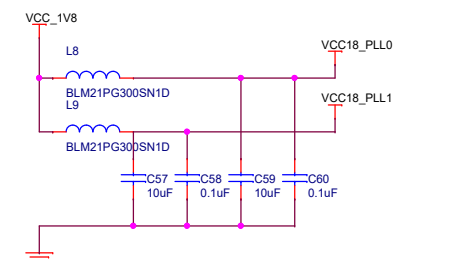
ADC

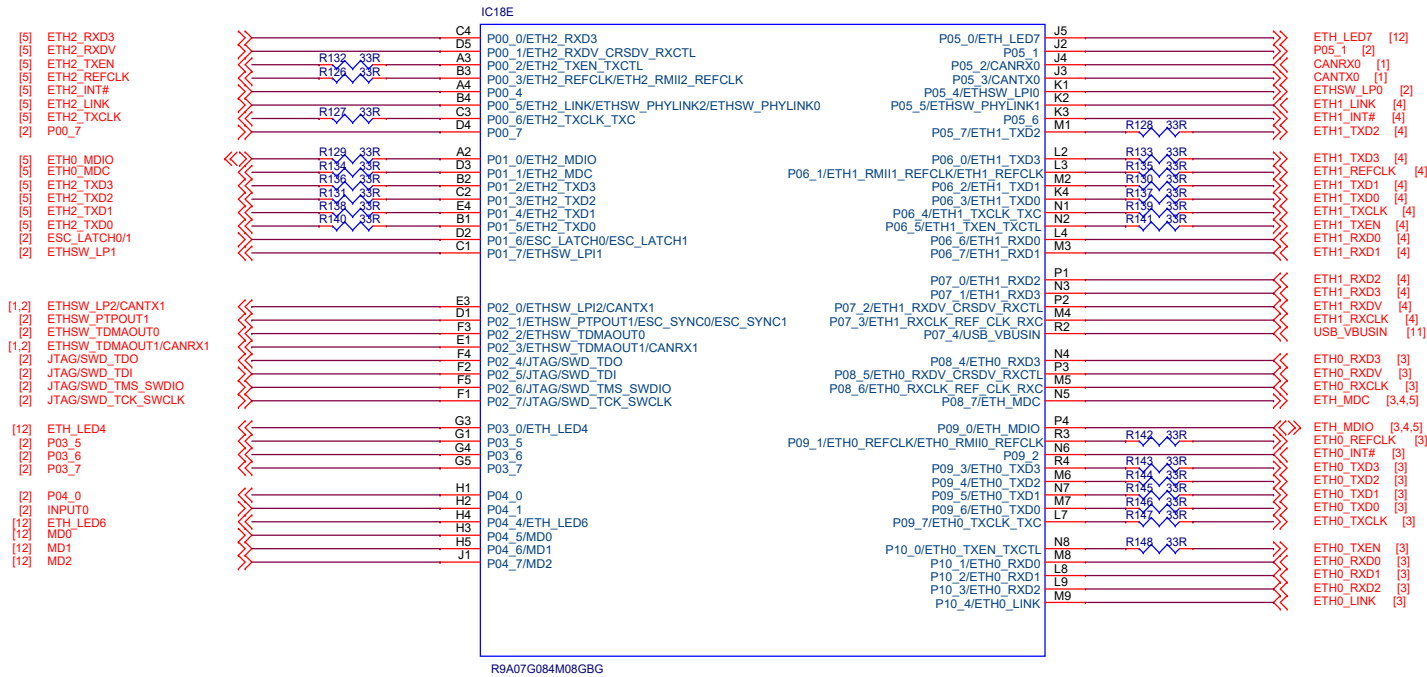


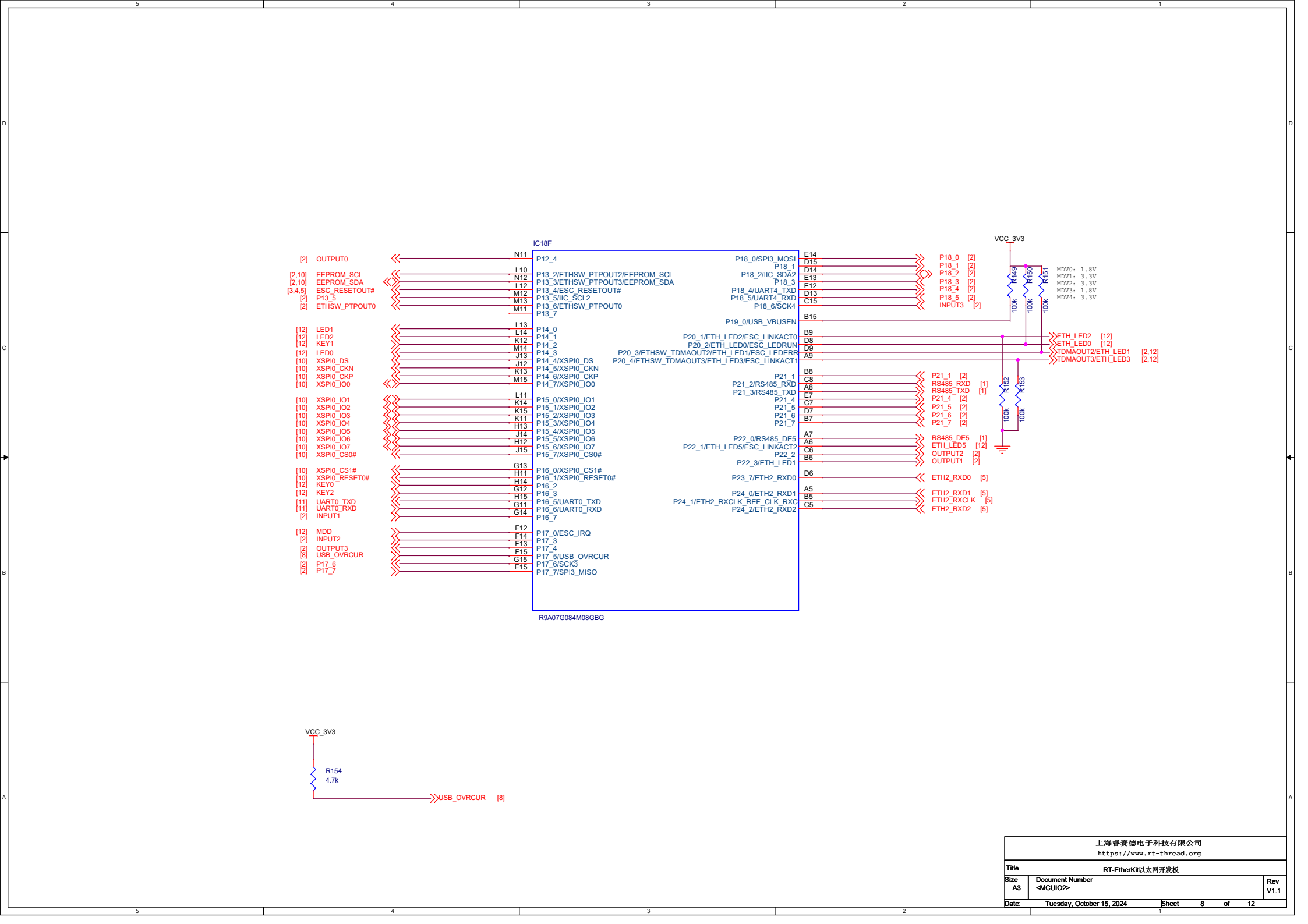
注：输入电压范围0~1.8V

定位孔

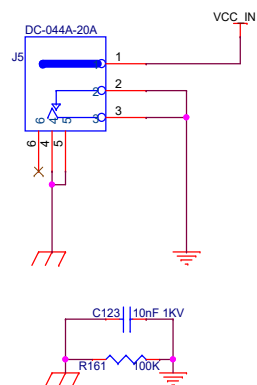




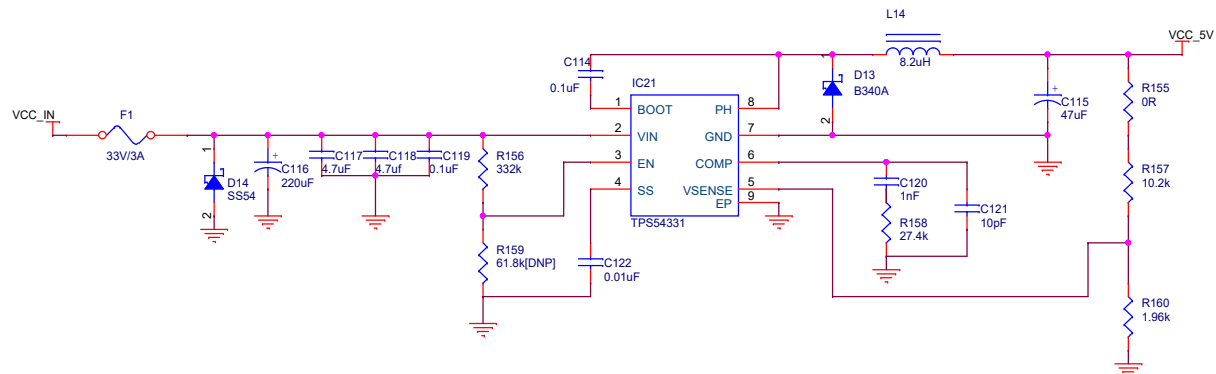




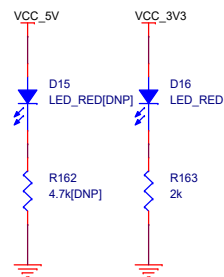
DC IN 9~24V



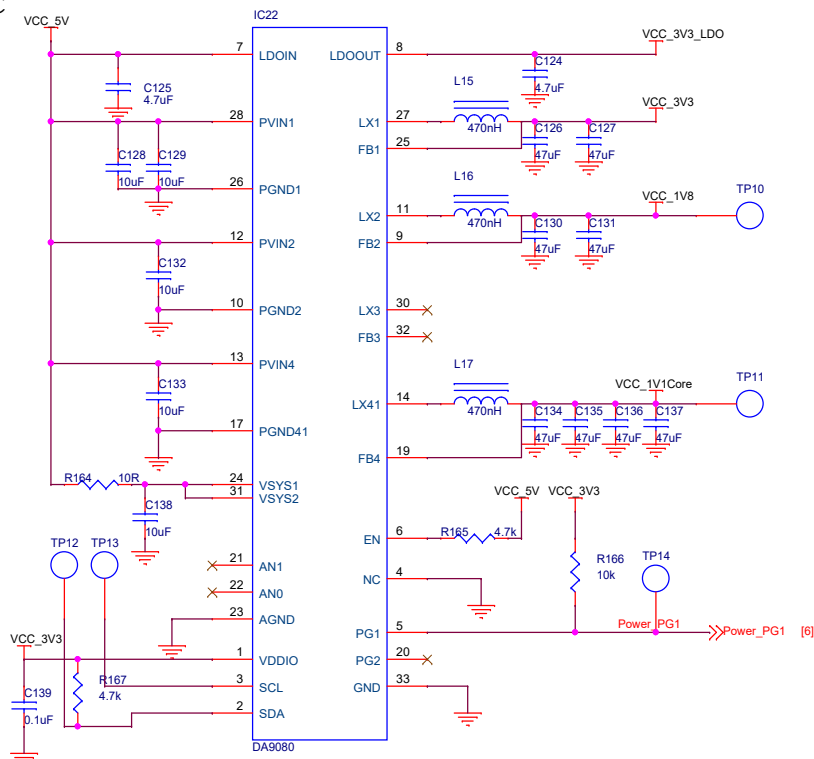
DC: 9~24V--->5V



Power LED



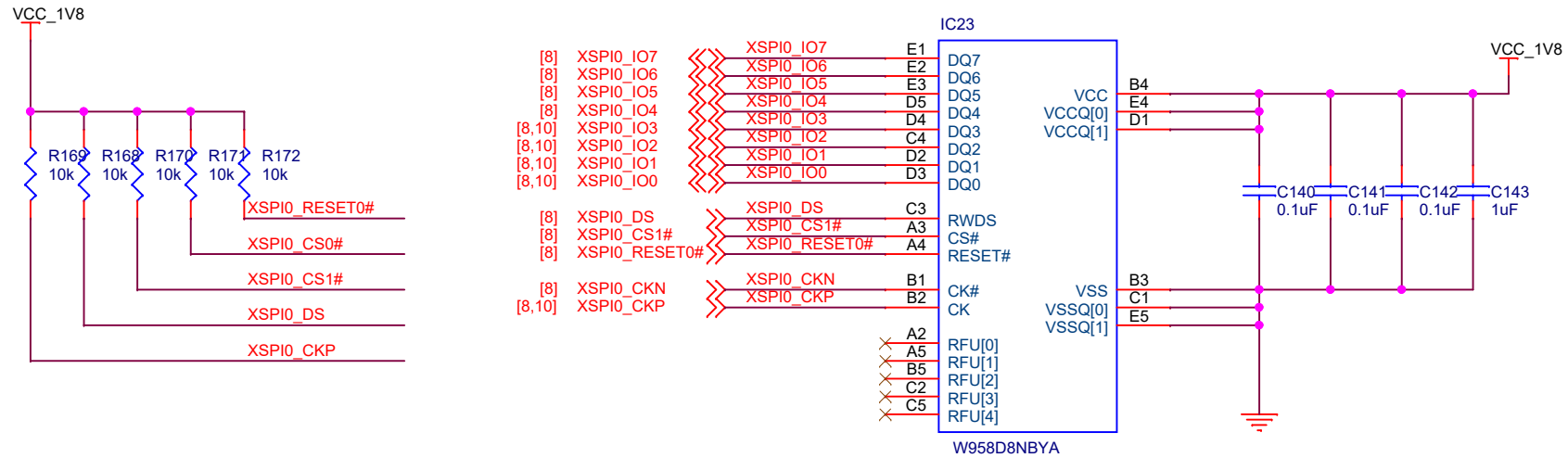
PMIC



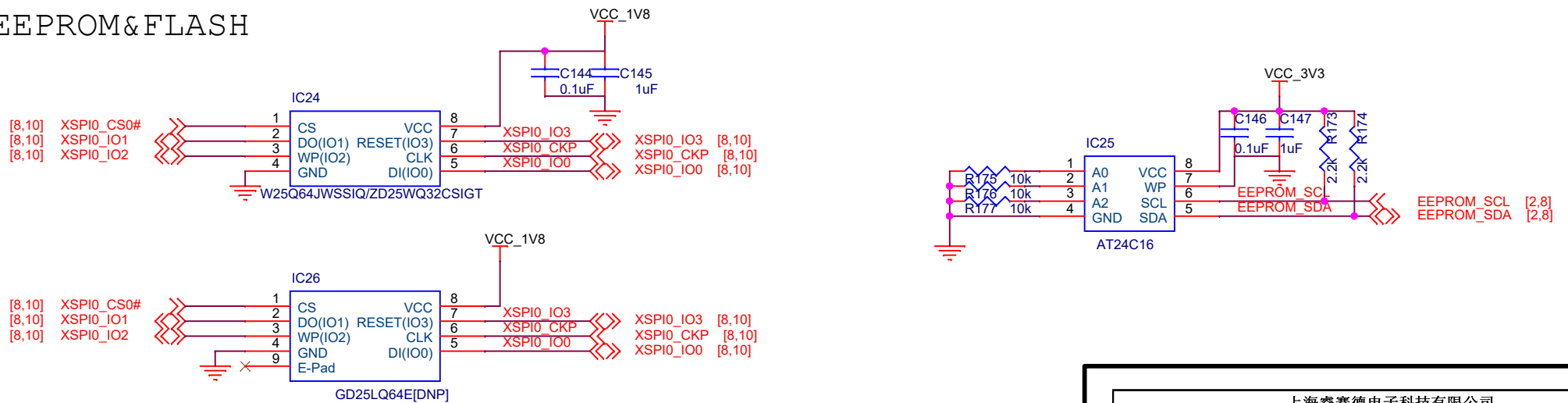
Power-on
sequence: 1.1->1.8->3.3

| | | | |
|---|-----------------------------|----------|-------|
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| RT-EtherKit以太网开发板 | | | |
| Size A3 | Document Number <POWER> | Rev V1.1 | |
| Date: | Wednesday, October 16, 2024 | Sheet 9 | of 12 |

Hyper RAM



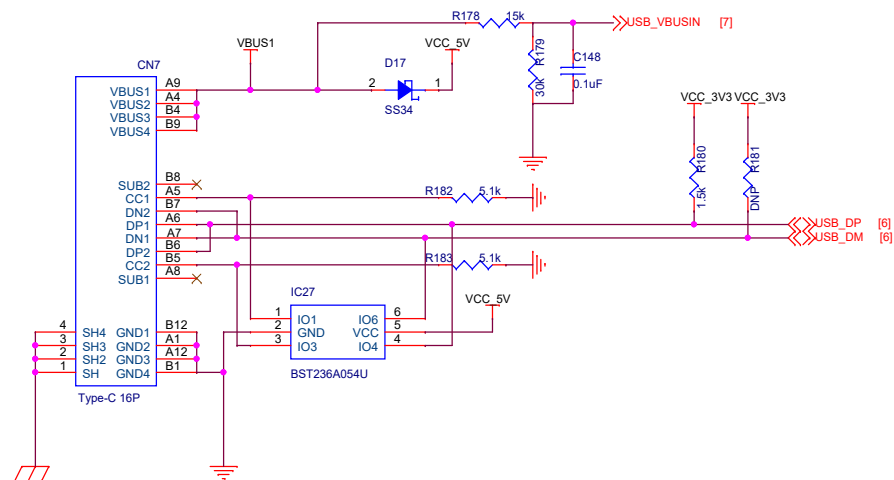
EEPROM&FLASH



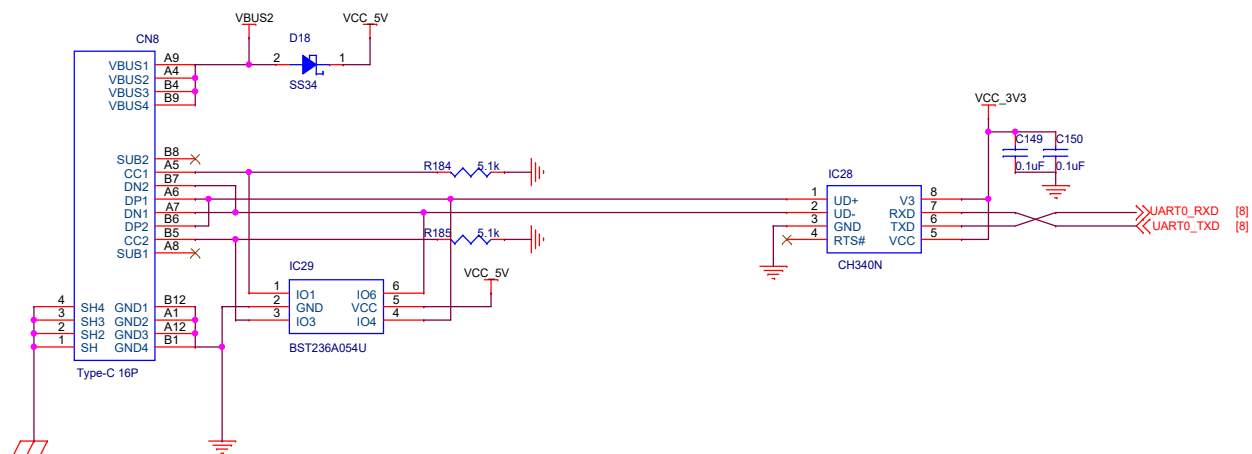
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| | | | |
|-------|-----------------------------|-------------------|--------------------|
| Title | | RT-EtherKit以太网开发板 | |
| Size | A4 | Document Number | <SPI Flash Memory> |
| Date: | Wednesday, October 16, 2024 | Sheet | 10 of 12 |
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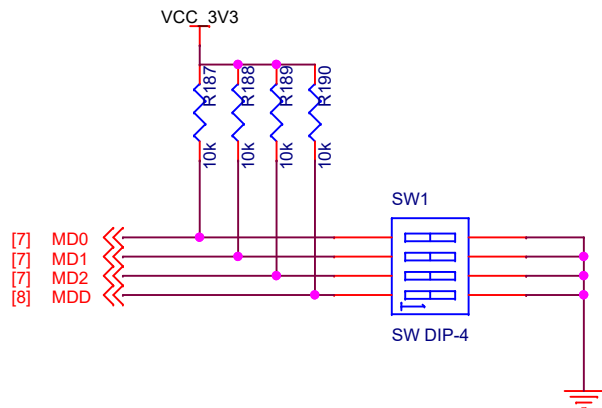
USB-DEVICE



USB-TTL



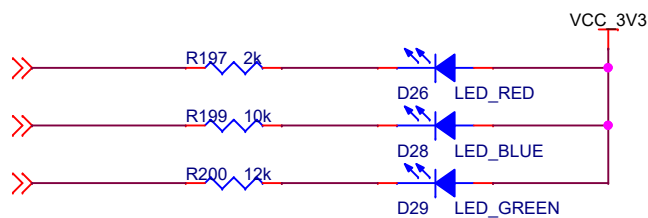
BOOT SWITCH



[8] LED0

[8] LED1

[8] LED2



ETH-LED

[8] ETH_LED0

[2,8] TDMAOUT2/ETH_LED

[8] ETH_LED2

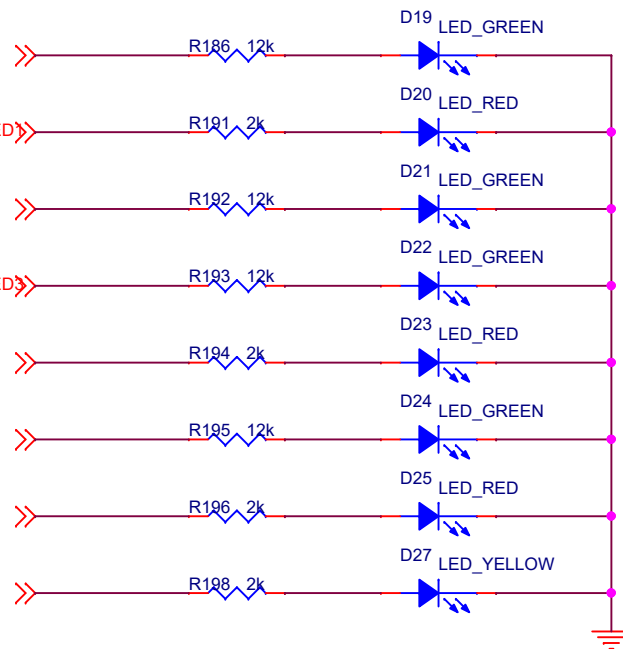
[2,8] TDMAOUT3/ETH_LED

[7] ETH_LED4

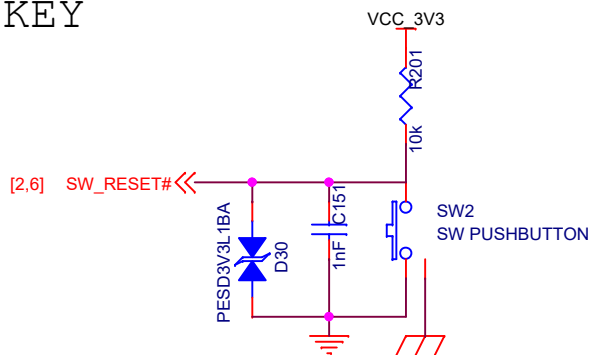
[8] ETH_LED5

[7] ETH_LED6

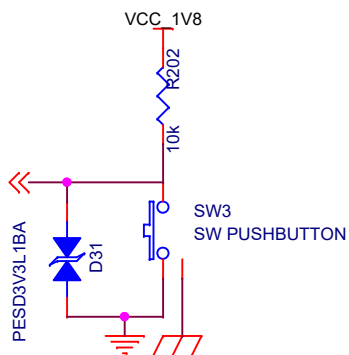
[7] ETH_LED7



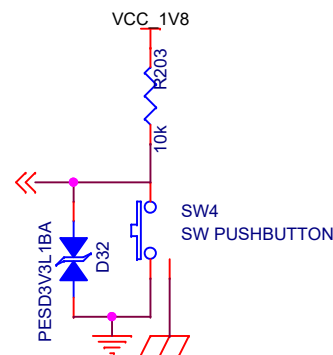
KEY



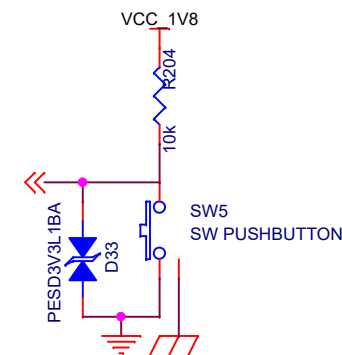
[8] KEY0



[8] KEY1



[8] KEY2



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| Title | | |
|---------------------------|------------------|-------|
| RT-EtherKit以太网开发板 | | |
| Size | Document Number | |
| A4 | <User Interface> | |
| Date: | Rev | |
| Tuesday, October 15, 2024 | V1.1 | |
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