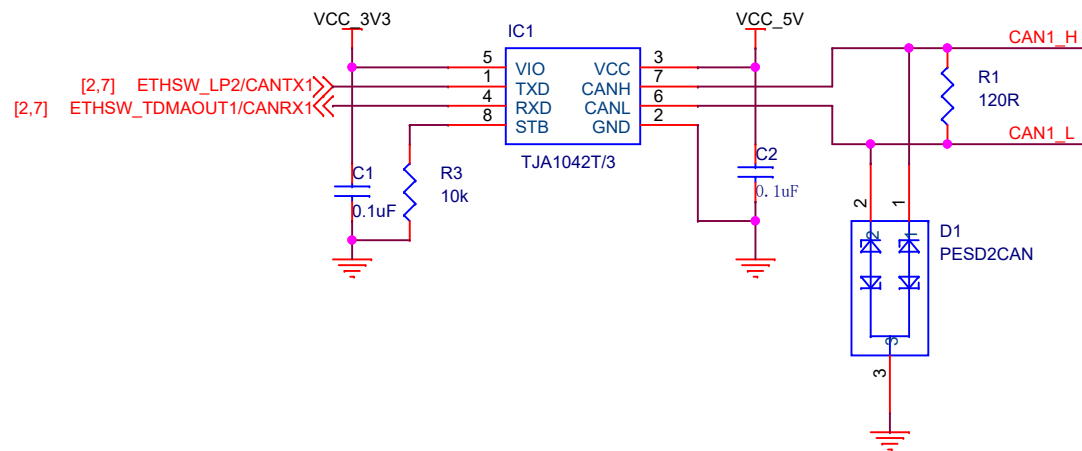
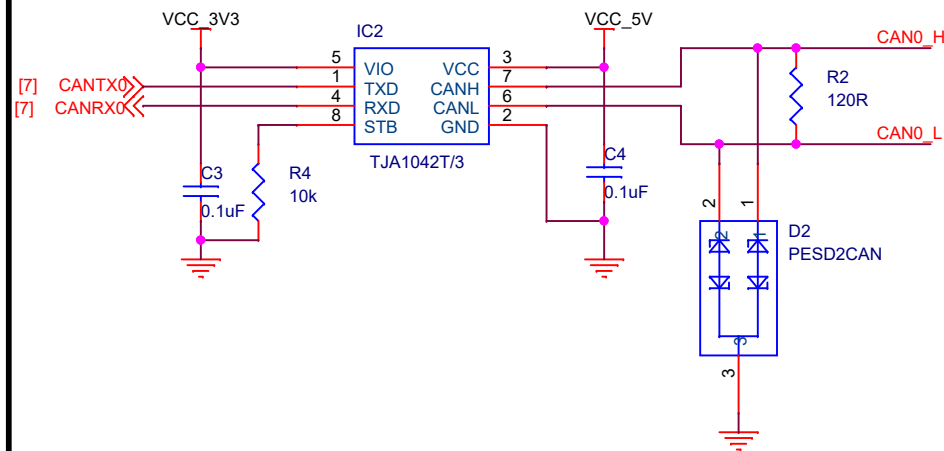


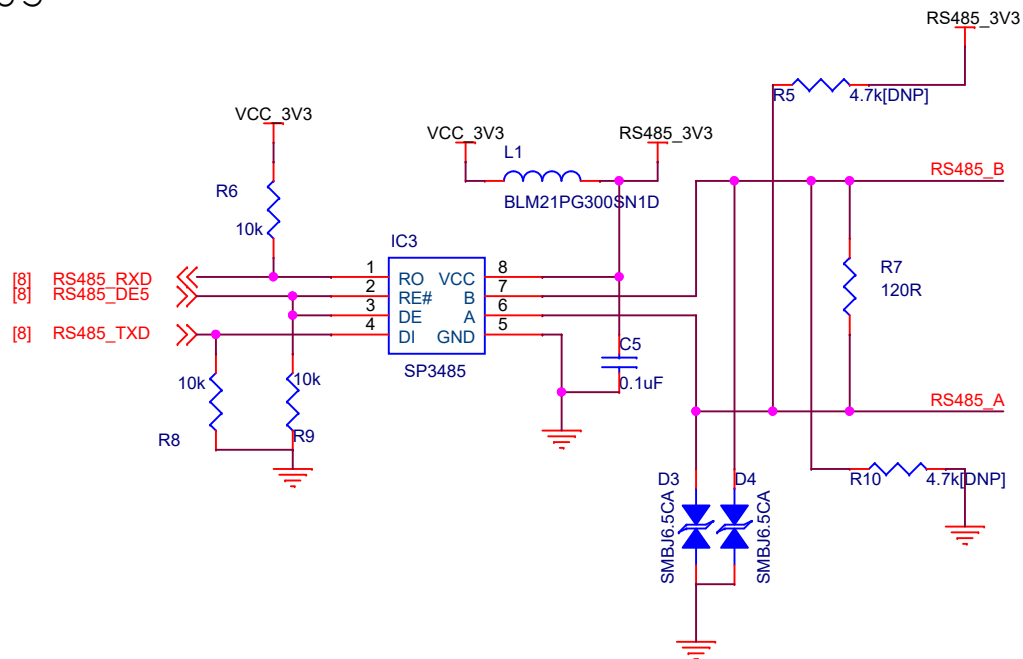
CAN1



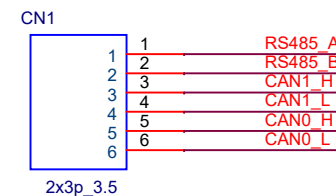
CAN0



RS485

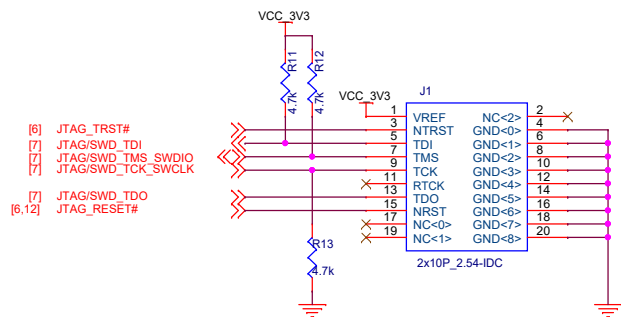


接线端子

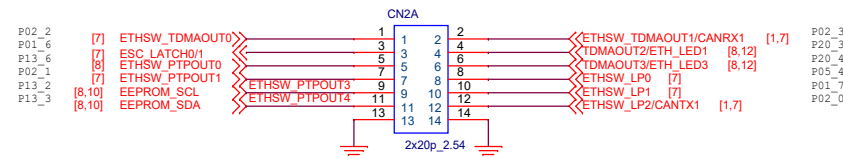


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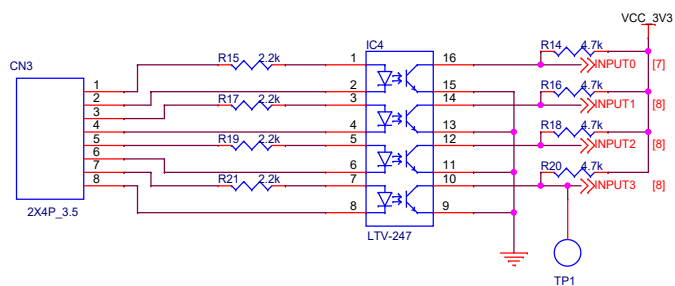
JTAG调试接口



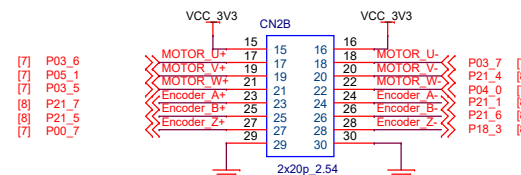
ETH相关IO



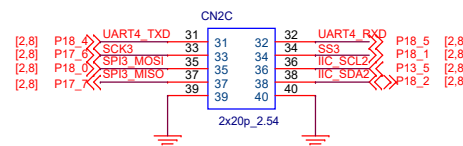
INPUT



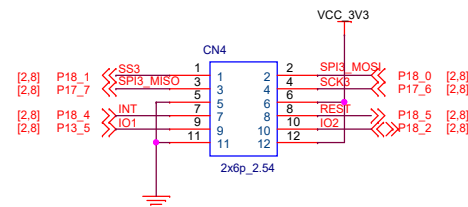
timer



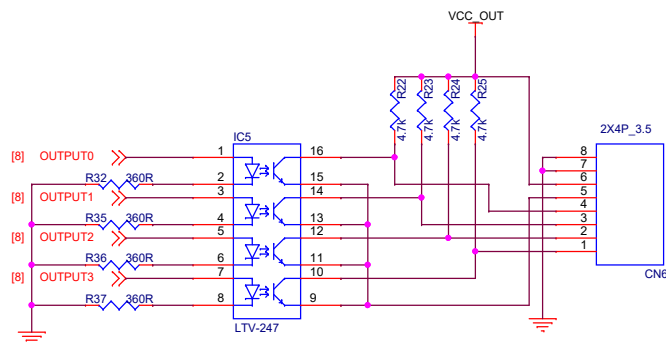
Serial device IO



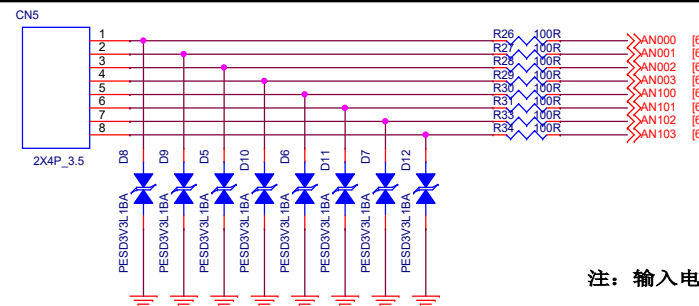
PMOD-SPI



OUTPUT

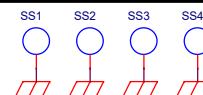


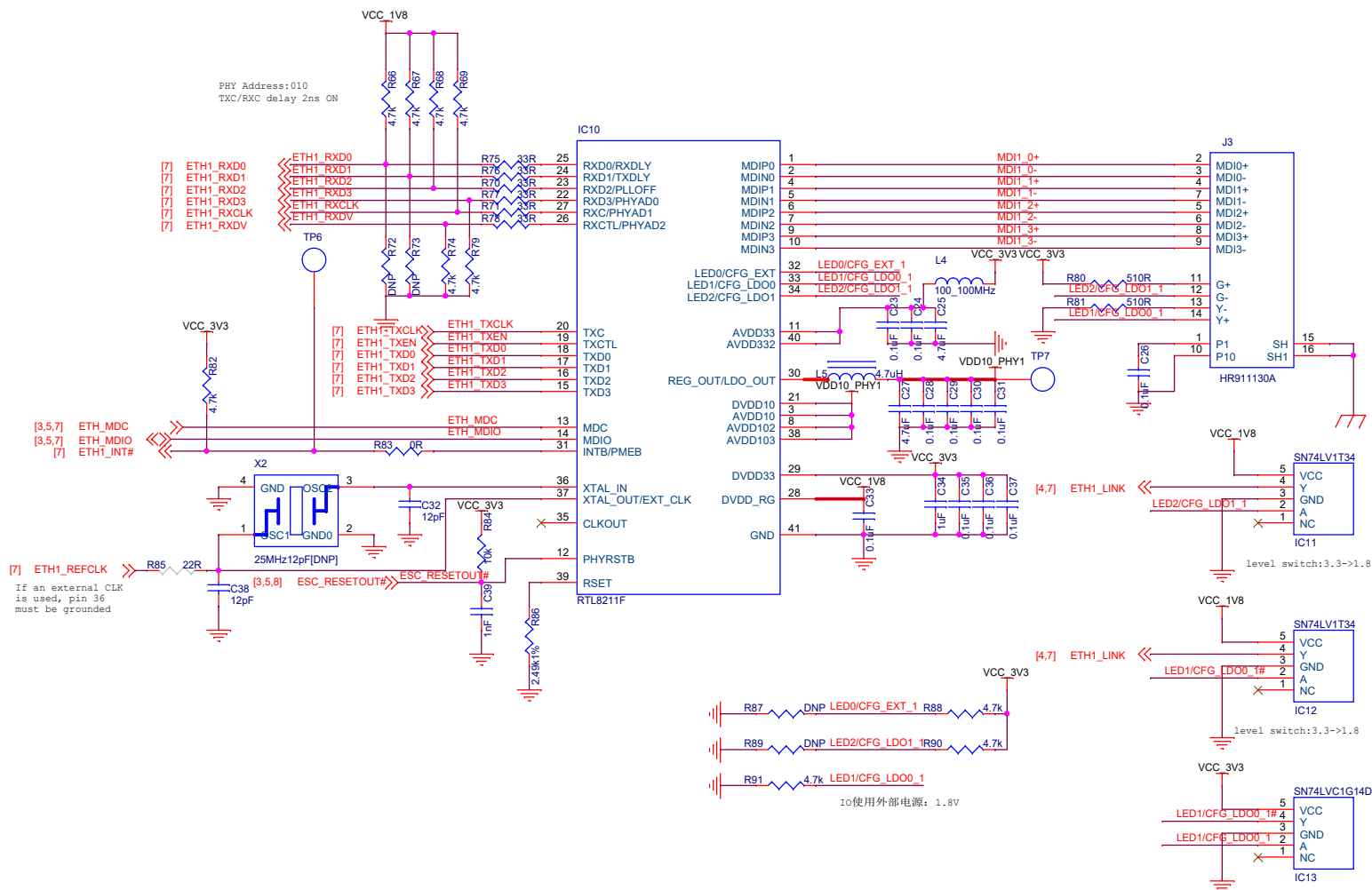
ADC

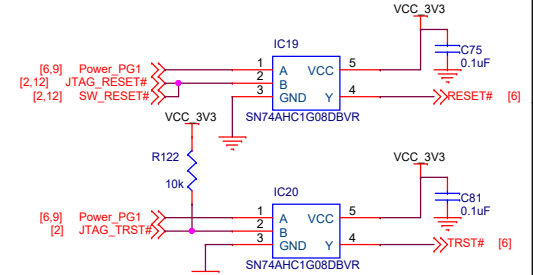
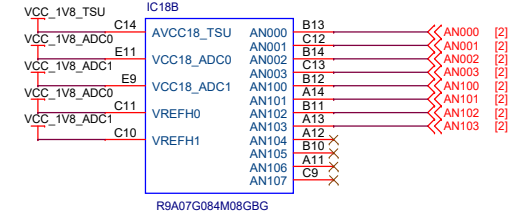
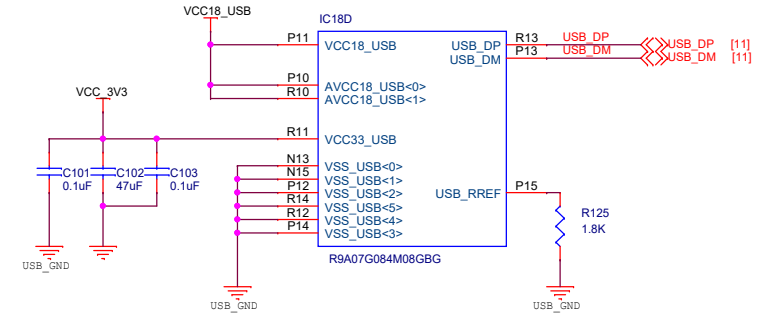
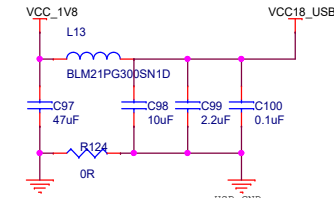
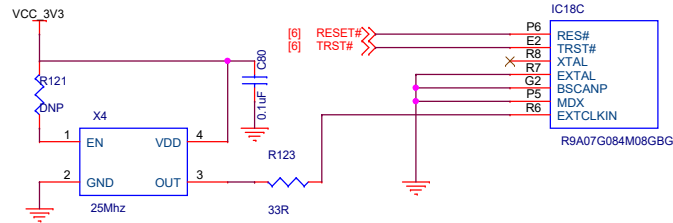
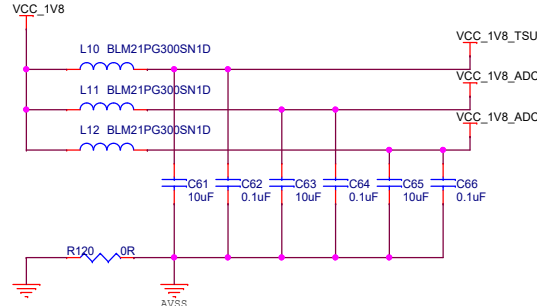
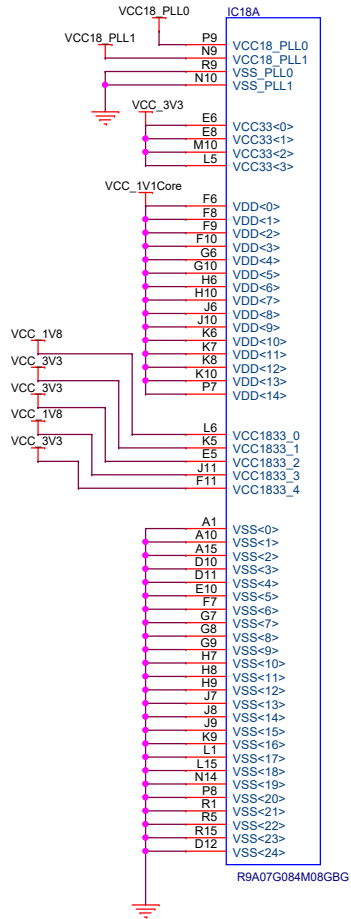
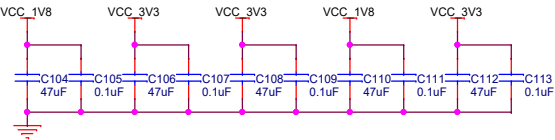
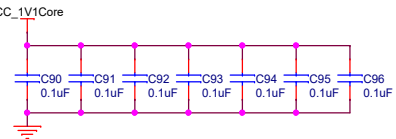
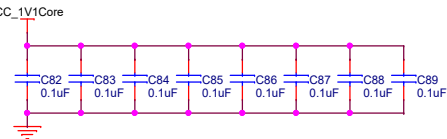
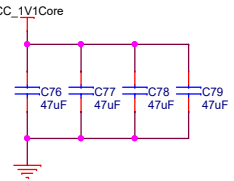
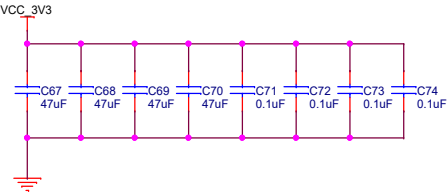
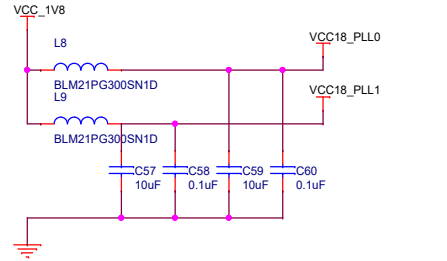


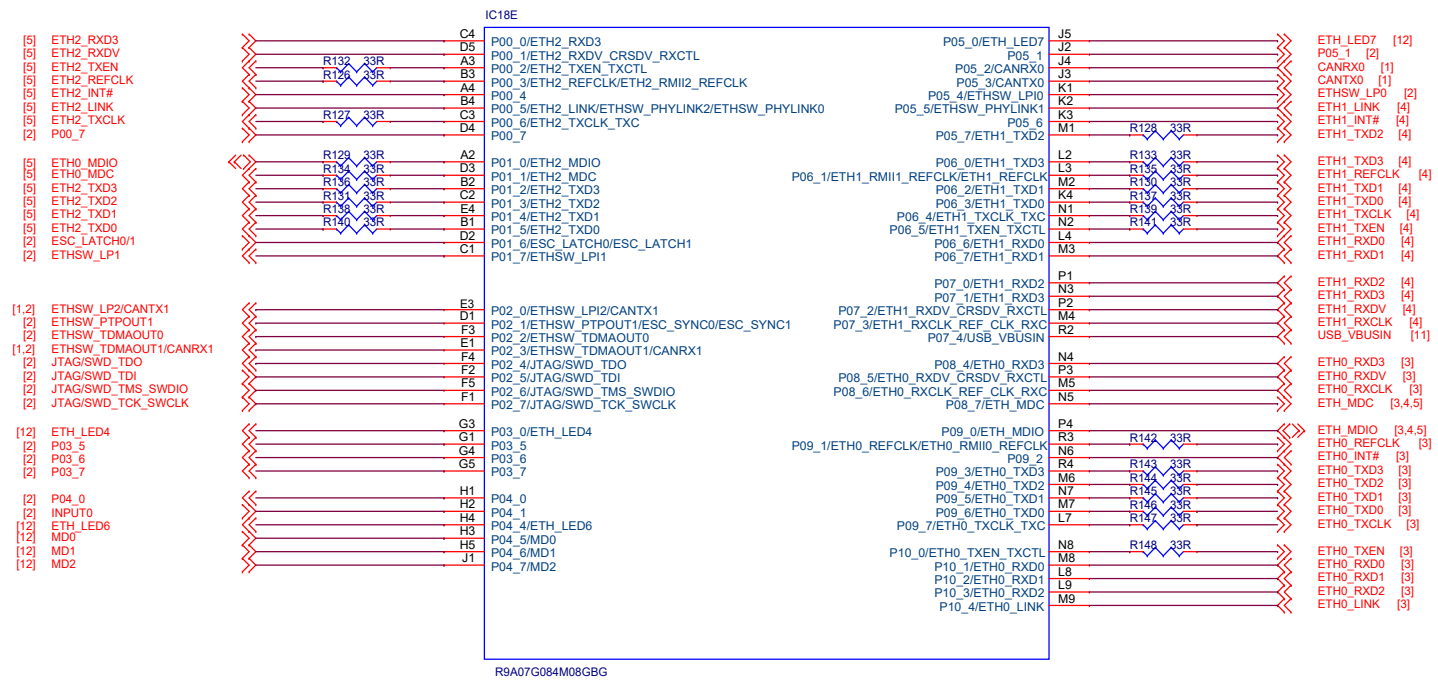
注：输入电压范围0~1.8V

定位孔



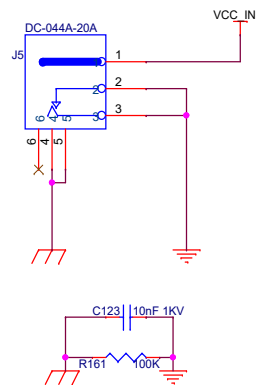




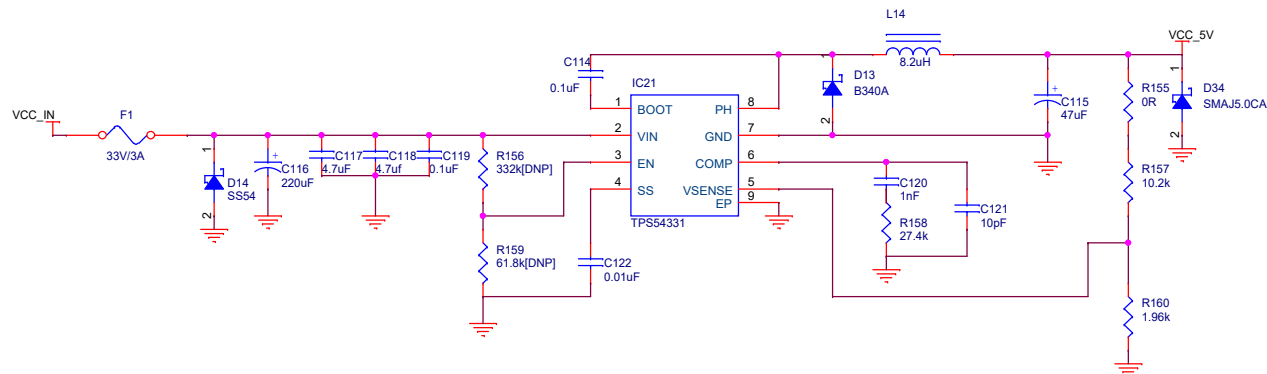




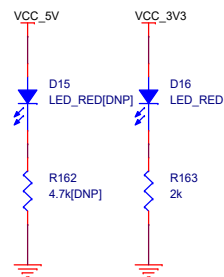
DC IN 9~24V



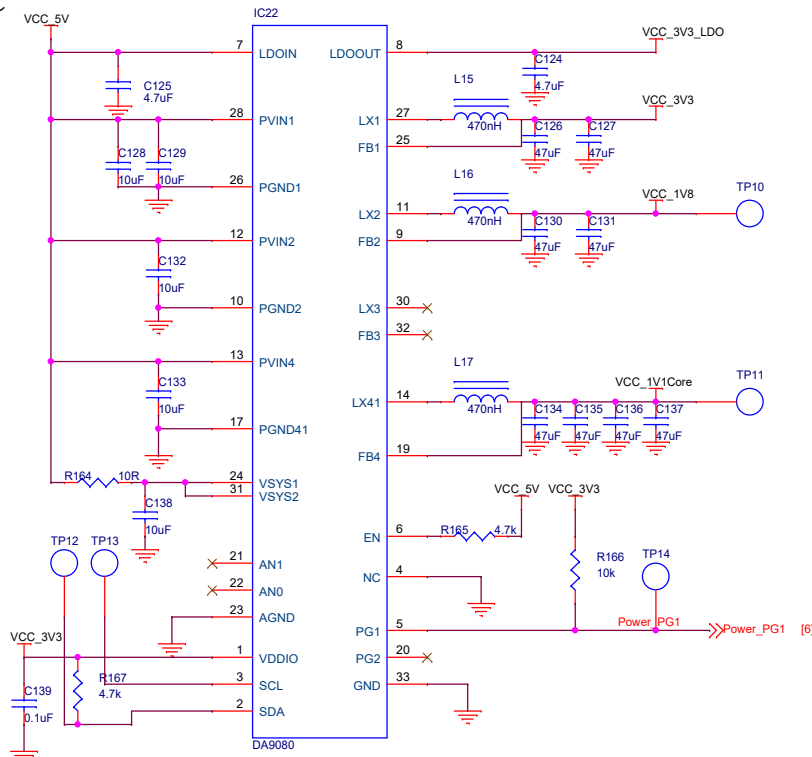
DC: 9~24V--->5V



Power LED



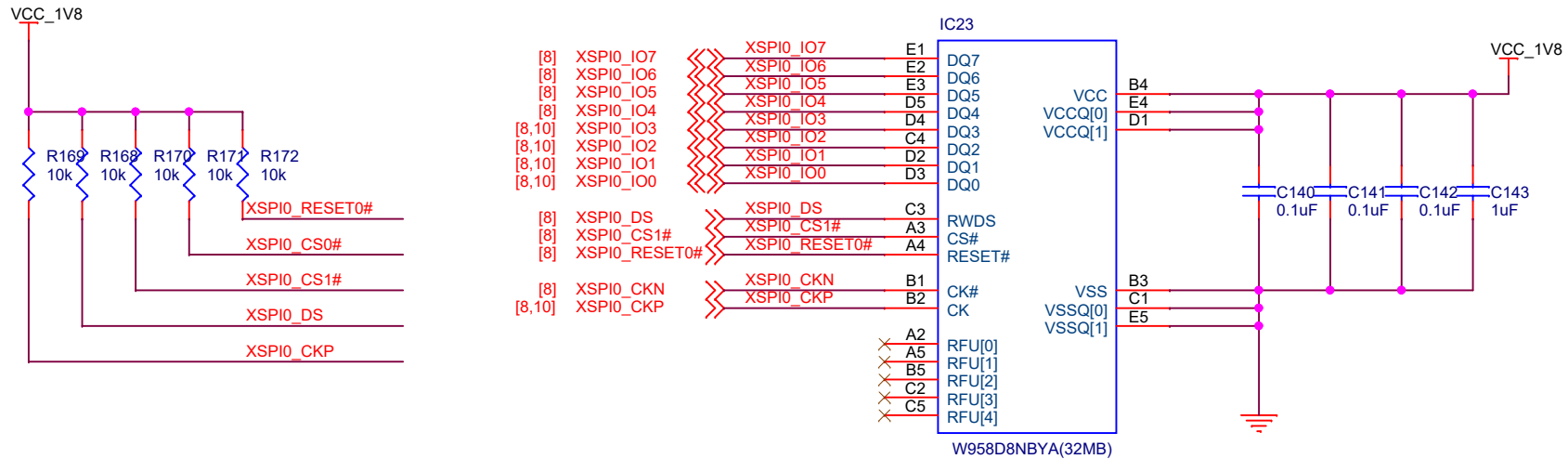
PMIC



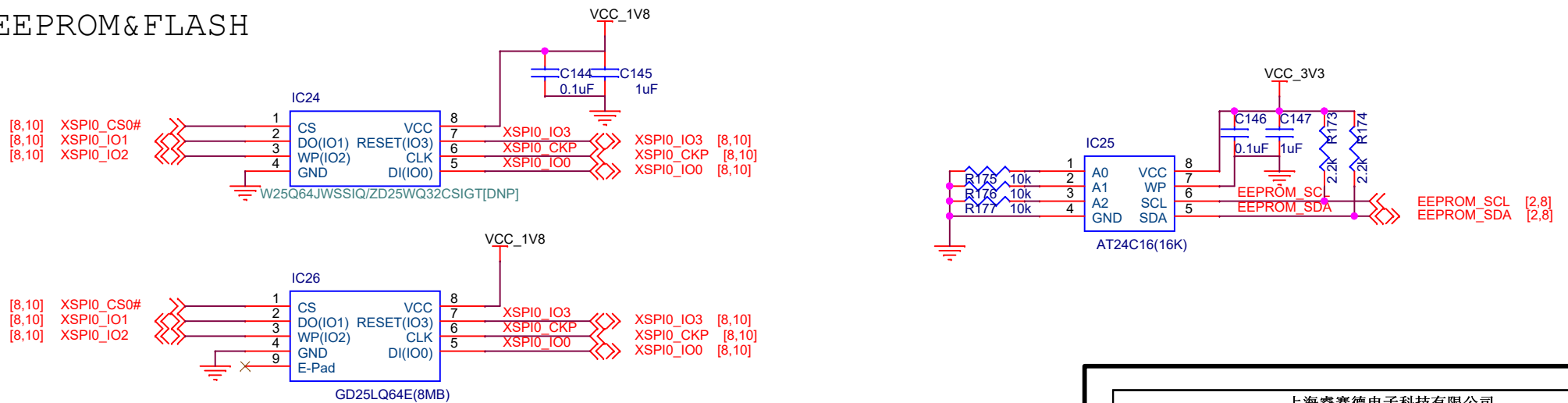
Power-on
sequence: 1.1->1.8->3.3

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| Size A3 | Document Number <POWER> | Rev V1.1 | |
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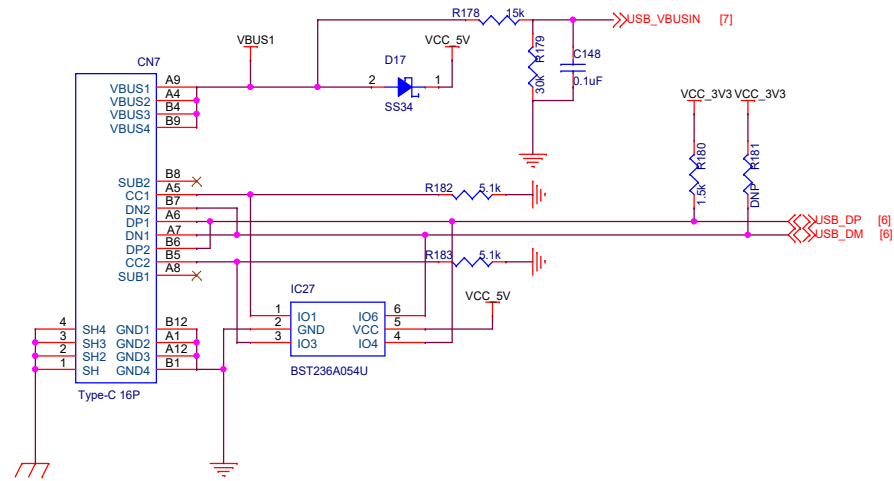
Hyper RAM



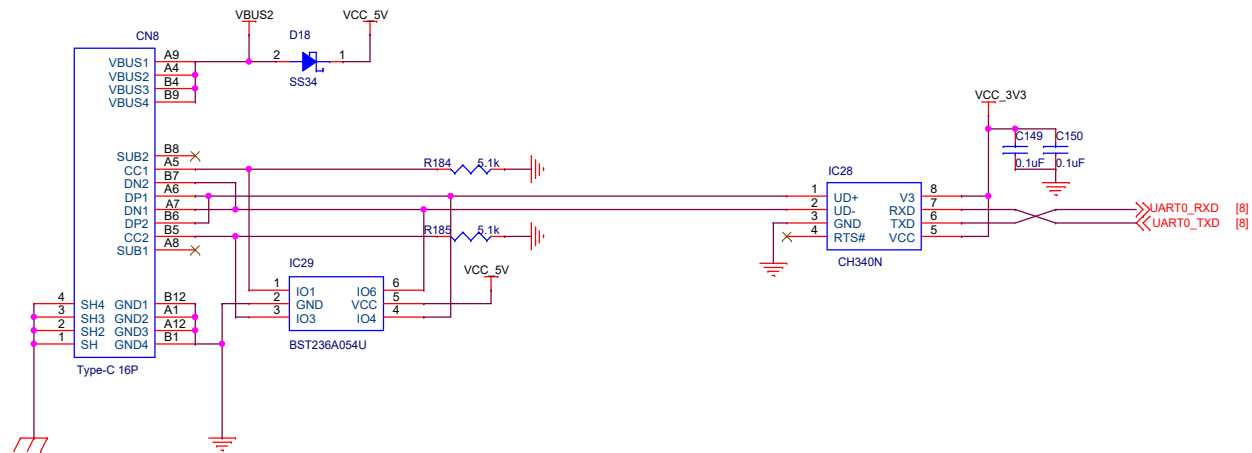
EEPROM&FLASH



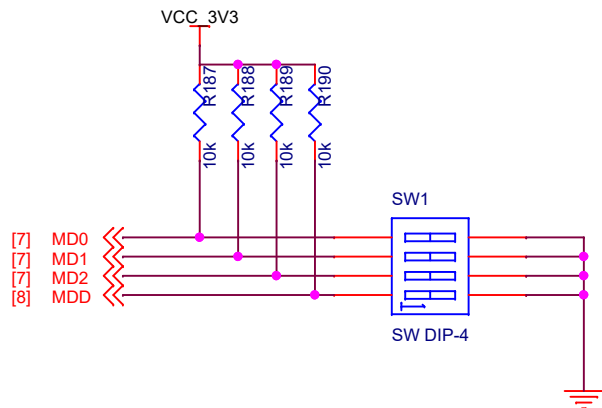
USB-DEVICE



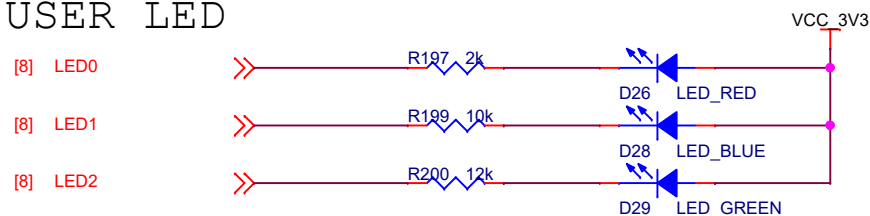
USB-TTL



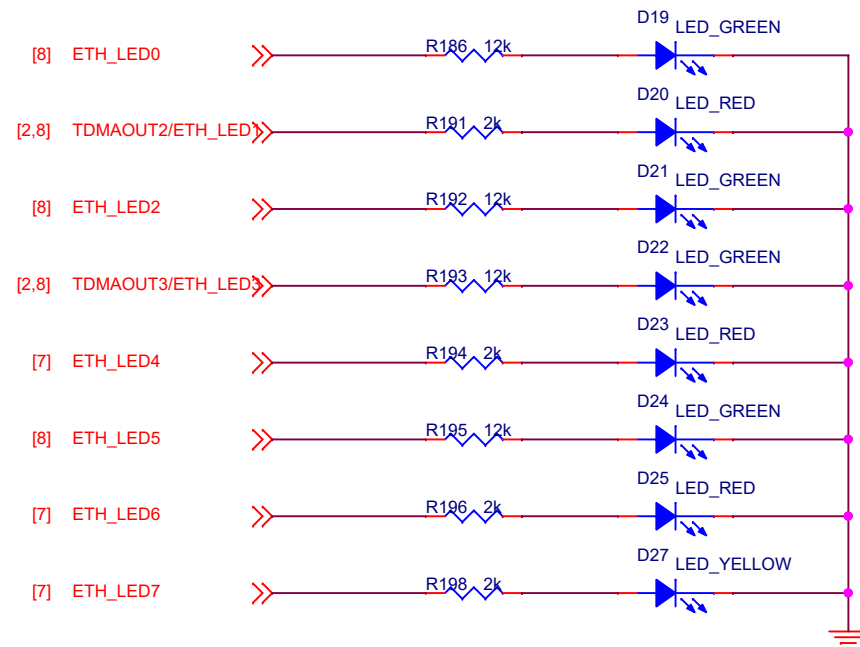
BOOT SWITCH



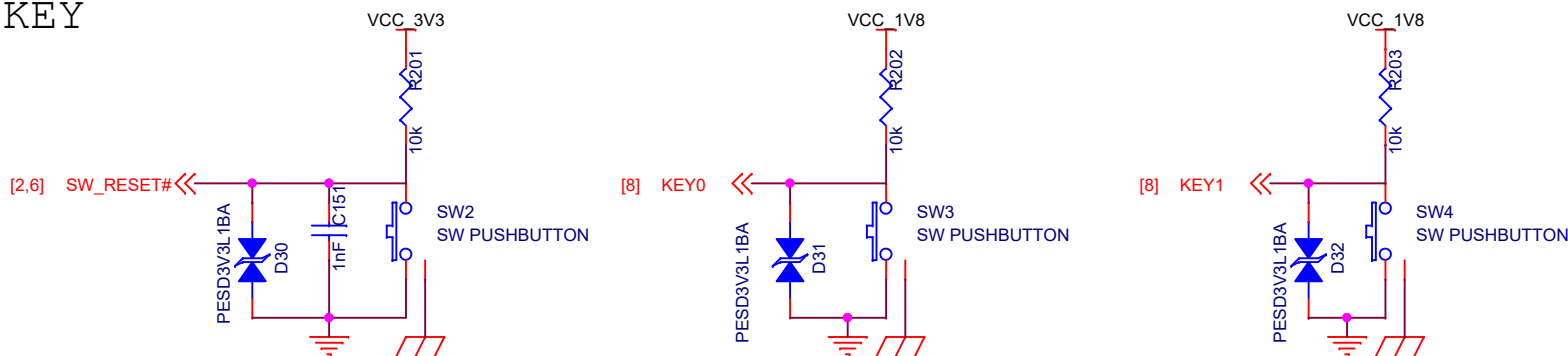
USER LED



ETH-LED



KEY



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| Title | | |
|-------------------|---------------------------|----------------|
| RT-EtherKit以太网开发板 | | |
| Size | Document Number | Rev |
| A4 | <User Interface> | V1.1 |
| Date: | Monday, November 25, 2024 | Sheet 12 of 12 |