

Flasher User Guide

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Manual versions

This manual describes the Flasher device. For further information on topics or routines not yet specified, please contact us.

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About this document

Assumptions

This document assumes that you already have a solid knowledge of the following:

- The software tools used for building your application (assembler, linker, C compiler).
- The C programming language.
- The target processor.
- DOS command line.

If you feel that your knowledge of C is not sufficient, we recommend *The C Programming Language* by Kernighan and Richie (ISBN 0-13-1103628), which describes the standard in C programming and, in newer editions, also covers the ANSI C standard.

How to use this manual

This manual explains all the functions and macros that the product offers. It assumes you have a working knowledge of the C language. Knowledge of assembly programming is not required.

Typographic conventions for syntax

This manual uses the following typographic conventions:

Style	Used for
Body	Body text.
Keyword	Text that you enter at the command prompt or that appears on the display (that is system functions, file- or pathnames).
Parameter	Parameters in API functions.
Sample	Sample code in program examples.
Sample comment	Comments in program examples.
Reference	Reference to chapters, sections, tables and figures or other documents.
GUIElement	Buttons, dialog boxes, menu names, menu commands.
Emphasis	Very important sections.

Table of contents

1	Introduction	10
1.1	Flasher overview	11
1.1.1	Features of the Flasher ATE	11
1.1.2	Features of Flasher ARM/PPC/RX/PRO	11
1.1.3	Features of Flasher Portable/Flasher Portable PLUS	12
1.1.4	Working environment	12
1.2	Specifications	14
1.2.1	Specifications for Flasher ATE	14
1.2.2	Specifications for Flasher ARM	17
1.2.3	Specifications for Flasher RX	19
1.2.4	Specifications for Flasher PPC	21
1.2.5	Specifications for Flasher PRO	23
1.2.6	Specifications for Flasher Portable PLUS	26
1.2.7	Specifications for Flasher Portable	29
2	Working with Flasher	32
2.1	Flasher Portable PLUS	33
2.1.1	Housing & Buttons	33
2.2	Flasher Portable	35
2.2.1	Housing & Buttons	35
2.3	Flasher ATE	37
2.3.1	Power supply	38
2.3.2	UART to TCP transceiver	38
2.4	File system	40
2.5	Setting up the IP interface	41
2.5.1	Connecting the first time	41
2.6	Operating modes	42
2.6.1	J-Link mode	42
2.6.2	Stand-alone mode	43
2.6.3	MSD mode	46
2.7	Setting up Flasher for stand-alone mode	47
2.7.1	Preparing for stand-alone operation manually	50
2.8	Universal Flash Loader mode	52
2.8.1	Preparing manually	52
2.8.2	Preparing using the PC utility	56
2.9	Multiple File Support	57
2.9.1	Flasher Portable specifics	57
2.9.2	Example	58
2.10	Custom labels	59
2.10.1	Hardware and software requirements	59

2.10.2	Assigning labels	59
2.10.3	Considerations	60
2.11	Programming multiple targets	61
2.11.1	Programming multiple targets with J-Flash	61
2.12	Batch Programming in stand-alone mode	62
2.12.1	Flasher Portable specifics	62
2.12.2	Examples	64
2.13	Serial number programming	65
2.13.1	Serial number settings	65
2.13.2	Serial number file	66
2.13.3	Serial number list file	66
2.13.4	Programming process	67
2.13.5	Downloading serial number files to Flasher	68
2.13.6	Sample setup	68
2.14	Patch file support	70
2.15	Limiting the number of programming cycles	71
2.15.1	Changed fail/error LED indicator behavior	71
2.15.2	Required Flasher hardware version for Cntdown.txt support	71
2.16	Authorized flashing	72
2.16.1	Creating / Adding the secure area	72
2.16.2	Moving files to the secure area	72
2.16.3	Considerations to be taken when using the secure area	73
2.16.4	Required Flasher hardware version	73
2.17	Target interfaces	74
2.18	Supported microcontrollers	75
2.18.1	Flasher	75
2.19	Support of external flashes	76
2.19.1	Flasher ARM	76
2.19.2	Flasher RX	76
2.19.3	Flasher PPC	76
2.20	Supported cores	77
2.20.1	Flasher ARM	77
2.20.2	Flasher RX	77
2.20.3	Flasher PPC	77
2.21	Newline encoding	78
2.22	Programming multiple targets in parallel	79
3	TCP Services	80
3.1	FTP Server	81
3.1.1	Access data	81
3.2	Web server	82
4	Remote control	83
4.1	Overview	84
4.2	Handshake control	85
4.3	ASCII command interface	87
4.3.1	Introduction	87
4.3.2	General command and reply message format	87
4.3.3	General usage	87
4.3.4	Settings for ASCII interface via RS232	87
4.3.5	Settings for ASCII interface via Telnet	87
4.3.6	Commands and replies	88
5	Performance	97
5.1	Performance of MCUs with internal flash memory	98
5.1.1	Flasher ARM	98
5.1.2	Flasher RX	98
5.1.3	Flasher PPC	98

6	Hardware	99
6.1	Flasher ARM 20-pin JTAG/SWD Connector	100
6.1.1	Pinout JTAG	100
6.1.2	Pinout SWD	101
6.1.3	Target power supply	102
6.2	Flasher RX 14-pin connector	103
6.2.1	Target power supply	104
6.3	Flasher PPC 14-pin connector	105
6.4	Target board design	106
6.4.1	Pull-up/pull-down resistors	106
6.4.2	RESET, nTRST	106
6.5	Adapters	107
6.5.1	JTAG Isolator	107
6.5.2	J-Link Needle Adapter	108
6.6	How to determine the hardware version	109
7	Support and FAQs	110
7.1	Contacting support	111
7.2	Frequently Asked Questions	112
8	Background information	113
8.1	Flash programming	114
8.1.1	How does flash programming via Flasher work?	114
8.1.2	Data download to RAM	114
8.1.3	Available options for flash programming	114
8.1.4	How does the universal flash programming work?	114
9	Glossary	115
10	Literature and references	119

Chapter 1

Introduction

This chapter gives a short overview about the different models of the Flasher family and their features.

1.1 Flasher overview

Flasher is a programming tool for microcontrollers with on-chip or external flash memory. Flasher is designed for programming flash targets with the J-Flash software or stand-alone. In addition to that Flasher can also be used as a regular J-Link. For more information about J-Link in general, please refer to the *J-Link / J-Trace User Guide* which can be downloaded at <http://www.segger.com>.

Flasher connects to a PC using the USB/Ethernet/RS232 interface (what host interfaces are available depends on the Flasher model), running Microsoft Windows 2000, Windows XP, Windows 2003, Windows Vista, Windows 7 or Windows 8. In stand-alone mode, Flasher can be driven by the start/stop button, or via the RS232 interface (handshake control or ASCII interface). Flasher always has a 20-pin connector, which target interfaces are supported depends on the Flasher model:

- Flasher ARM: JTAG and SWD are supported.
- Flasher RX: JTAG is supported. Flasher comes with additional 14-pin RX adapter
- Flasher PPC: JTAG is supported. Flasher comes with additional 14-pin PPC adapter.
- Flasher PRO: JTAG and SWD are supported.

1.1.1 Features of the Flasher ATE

- Supports up to 10 individual channels
- Stand-alone JTAG/SWD programmer (Once set up, Flasher can be controlled without the use of PC program)
- Flexible power supply (USB, DC)
- 128 MB memory for storage of target program on each module
- Data files can be updated via the integrated FTP server

Flasher model	Supported cores	Supported target interfaces	Flash programming speed (depending on target hardware)
Flasher ATE	ARM7/ARM9/Cortex-M Renesas RX610, RX621, RX62N, RX62T Power PC e200z0	JTAG, SWD	between 30-300 Kbytes/ second

1.1.2 Features of Flasher ARM/PPC/RX/PRO

- Three boot modes: J-Link mode, stand-alone mode, MSD mode
- Stand-alone JTAG/SWD programmer (Once set up, Flasher can be controlled without the use of PC program)
- No power supply required, powered through USB
- Supports internal and external flash devices
- 128 MB memory for storage of target program
- Can be used as J-Link (emulator) with a download speed of up to 720 Kbytes/second
- Data files can be updated via USB/Ethernet (using the J-Flash software), via FTP, via RS232 or via the MSD functionality of Flasher

Flasher model	Supported cores	Supported target interfaces	Flash programming speed (depending on target hardware)
Flasher ARM	ARM7/ARM9/Cortex-M	JTAG, SWD	between 170 and 300 Kbytes/second
Flasher RX	Renesas RX610, RX621, RX62N, RX62T	JTAG	between 30-300 Kbytes/second
Flasher PPC	Power PC e200z0	JTAG	up to 138 Kbytes/second
Flasher PRO	RM7/ARM9/Cortex-M Renesas RX610, RX621, RX62N, RX62T Power PC e200z0	JTAG, SWD	between 30-300 Kbytes/ second

1.1.3 Features of Flasher Portable/Flasher Portable PLUS

- Stand-alone in-circuit-programmer (Once set up, Flasher can be controlled without the use of a PC program)
- Powered by an internal rechargeable battery (standard batteries for Flasher Portable), no Laptop or external power supply required.
- Multiple firmware images can be stored on Flasher
- 128 MB memory for storage of target program
- Easy selection of image to be programmed, via button
- Supported CPUs: ARM Cortex, Legacy ARM7/9, Renesas RX, Freescale PowerPC
- Supports internal and external flash
- Free software updates*, 1 year of support
- Data files can be updated via the MSD functionality or via J-Flash
- Programming speed between 30-300 Kbytes/second (actual speed depends on target hardware)

Note

Ethernet and RS232 as host interface are not available for Flasher Portable

Note

*As a legitimate owner of a SEGGER Flasher, you can always download the latest software free of charge. Though not planned and not likely, we reserve the right to change this policy. Note that older models may not be supported by newer versions of the software. Typically, we support older models with new software at least 3 years after end of life

Supported cores	Supported target interfaces	Flash programming speed (depending on target hardware)
ARM7/ARM9/Cortex-M	JTAG, SWD	between 30-300 Kbytes/second
Renesas RX610, RX621, RX62N, RX62T	JTAG	between 170 and 300 Kbytes/second
Power PC e200z0	JTAG	up to 138 Kbytes/second

1.1.4 Working environment

General

The Flasher can operate from a PC with an appropriate software like J-Flash or in stand-alone mode.

Host System

IBM PC/AT or compatible CPU: 486 (or better) with at least 128MB of RAM, running Microsoft Windows 2000, Windows XP, Windows 2003, Windows Vista, Windows 7 or Windows 8. It needs to have a USB, Ethernet or RS232 interface available for communication with Flasher.

Power supply

Flasher Portable: 3x standard AAA batteries or 5V DC, min. 100 mA via USB connector.

Flasher Portable PLUS: internal rechargeable 680mAh Li-Ion battery, min. 100 mA via USB connector.

Other Flashers: 5V DC, min. 100 mA via USB connector.

Installing Flasher PC-software (J-Flash)

The latest version of the J-Flash software, which is part of the J-Link software and documentation package, can always be downloaded from our website:
<https://www.segger.com/jlink-software.html> For more information about using J-Flash please refer to UM08003_JFlashARM.pdf (J-Flash user guide) which is also available for download on our website.

1.2 Specifications

1.2.1 Specifications for Flasher ATE

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64 Microsoft Windows 10 Microsoft Windows 10 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +60 °C
Relative Humidity (non-condensing)	<90% rH
Mechanical mainboard	
Size (without cables)	108mm x 56mm x 20mm
Weight (without cables)	47g
Mechanical module	
Size (without cables)	108mm x 35mm x 20mm
Weight (without cables)	24g
Available interfaces	
USB Host interface	USB 2.0, hi speed
Ethernet Host interface	10/100 MBit
RS232 Host interface	RS232 9-pin
Target interface (module)	JTAG 20-pin (adapters available)
JTAG Interface, Electrical	
Power Supply	USB powered or via external power supply (5V), max. 3A using 10 modules
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	3 - 15V (5V with no additional supply.)
Target supply current	100 mA (VCC5V - 5V) 400 mA (VTGT - 3 ... 15V)
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF

For $3.6 \leq VIF \leq 5V$	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL $\leq 20\%$ of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH $\geq 80\%$ of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz
Data input rise time (Trdi)	Trdi $\leq 20ns$
Data input fall time (Tfdi)	Tfdi $\leq 20ns$
Data output rise time (Trdo)	Trdo $\leq 10ns$
Data output fall time (Tpdo)	Tpdo $\leq 10ns$
Clock rise time (Trc)	Trc $\leq 10ns$
Clock fall time (Tfc)	Tfc $\leq 10ns$

1.2.1.1 Supported CPU cores

The Flasher ATE supports the following CPU cores:

ARM Cortex

- Cortex-M0
- Cortex-M0+
- Cortex-M1
- Cortex-M3
- Cortex-M4

ARM (legacy cores)

- ARM720T
- ARM7TDMI
- ARM7TDMI-S
- ARM920T
- ARM922T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- ARM1136JF-S
- ARM1136J-S
- ARM1156T2-S
- ARM1156T2F-S
- ARM1176JZ-S
- ARM1176JZF
- ARM1176JZF-S

Renesas RX

- RX610
- RX621
- RX62G
- RX62N
- RX62T

Freescale Power PC

- e200z0

1.2.1.2 Supported Target interfaces

The Flasher ATE supports the following target interfaces:

- JTAG
- SWD
- FINE
- SPD

1.2.1.3 Flasher ATE download speed

The following table lists the Flasher ATE performance values for writing to memory (RAM) via the JTAG interface:

Hardware	ARM7 memory download
Flasher ARM	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.2 Specifications for Flasher ARM

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +60 °C
Relative Humidity (non-condensing)	<90% rH
Mechanical	
Size (without cables)	121mm x 66mm x 30mm
Weight (without cables)	119g
Available interfaces	
USB Host interface	USB 2.0, full speed
Ethernet Host interface	10/100 MBit
RS232 Host interface	RS232 9-pin
Target interface	JTAG 20-pin (14-pin adapter available)
JTAG Interface, Electrical	
Power Supply	USB powered, 100mA for Flasher ARM. 500 mA if target is powered by Flasher ARM
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 5V, max.
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For 3.6 ≤ VIF ≤ 5V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 80% of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz

Data input rise time (Trdi)	Trdi ≤ 20ns
Data input fall time (Tfdi)	Tfdi ≤ 20ns
Data output rise time (Trdo)	Trdo ≤ 10ns
Data output fall time (Tfdo)	Tfdo ≤ 10ns
Clock rise time (Trc)	Trc ≤ 10ns
Clock fall time (Tfc)	Tfc ≤ 10ns

1.2.2.1 Flasher ARM download speed

The following table lists the Flasher ARM performance values for writing to memory (RAM) via the JTAG interface:

Hardware	ARM7 memory download
Flasher ARM	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.3 Specifications for Flasher RX

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +60 °C
Relative Humidity (non-condensing)	<90% rH
Mechanical	
Size (without cables)	121mm x 66mm x 30mm
Weight (without cables)	119g
Available interfaces	
USB Host interface	USB 2.0, full speed
Ethernet Host interface	10/100 MBit
RS232 Host interface	RS232 9-pin
Target interface	JTAG 20-pin (shipped with 14-pin adapter for Renesas RX)
JTAG Interface, Electrical	
Power Supply	USB powered, 100mA for Flasher RX. 500 mA if target is powered by Flasher RX
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 5V, max. (on the J-Link RX 14-pin adapter, the target supply voltage can be switched between 3.3V and 5V)
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For 3.6 ≤ VIF ≤ 5V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 80% of VIF

JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz
Data input rise time (Trdi)	Trdi ≤ 20ns
Data input fall time (Tfdi)	Tfdi ≤ 20ns
Data output rise time (Trdo)	Trdo ≤ 10ns
Data output fall time (Tfdo)	Tfdo ≤ 10ns
Clock rise time (Trc)	Trc ≤ 10ns
Clock fall time (Tfc)	Tfc ≤ 10ns

1.2.3.1 Flasher RX download speed

The following table lists the Flasher RX performance values for writing to memory (RAM) via the JTAG interface:

Hardware	Flasher RX600 series memory download
Flasher RX	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.4 Specifications for Flasher PPC

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +60 °C
Relative Humidity (non-condensing)	<90% rH
Mechanical	
Size (without cables)	121mm x 66mm x 30mm
Weight (without cables)	119g
Available interfaces	
USB Host interface	USB 2.0, full speed
Ethernet Host interface	10/100 MBit
RS232 Host interface	RS232 9-pin
Target interface	JTAG 20-pin (shipped with 14-pin adapter for Renesas PPC)
JTAG Interface, Electrical	
Power Supply	USB powered, 100mA for Flasher PPC. 500 mA if target is powered by Flasher PPC
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 5V, max.
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For 3.6 ≤ VIF ≤ 5V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 80% of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz

Data input rise time (Trdi)	Trdi ≤ 20ns
Data input fall time (Tfdi)	Tfdi ≤ 20ns
Data output rise time (Trdo)	Trdo ≤ 10ns
Data output fall time (Tfdo)	Tfdo ≤ 10ns
Clock rise time (Trc)	Trc ≤ 10ns
Clock fall time (Tfc)	Tfc ≤ 10ns

1.2.4.1 Flasher RX download speed

The following table lists the Flasher PPC performance values for writing to memory (RAM) via the JTAG interface:

Hardware	Memory download
Flasher PPC	530 Kbytes/s (8 MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.5 Specifications for Flasher PRO

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +60 °C
Relative Humidity (non-condensing)	<90% rH
Mechanical	
Size (without cables)	121mm x 66mm x 30mm
Weight (without cables)	119g
Available interfaces	
USB Host interface	USB 2.0, full speed
Ethernet Host interface	10/100 MBit
RS232 Host interface	RS232 9-pin
Target interface	JTAG 20-pin (14-pin adapter available)
JTAG Interface, Electrical	
Power Supply	USB powered, 100mA for Flasher PRO. 500 mA if target is powered by Flasher PRO
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 5V, max.
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For 3.6 ≤ VIF ≤ 5V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 80% of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz
Data input rise time (Trdi)	Trdi ≤ 20ns

Data input fall time (Tfdi)	$Tfdi \leq 20\text{ns}$
Data output rise time (Trdo)	$Trdo \leq 10\text{ns}$
Data output fall time (Tfdo)	$Tfdo \leq 10\text{ns}$
Clock rise time (Trc)	$Trc \leq 10\text{ns}$
Clock fall time (Tfc)	$Tfc \leq 10\text{ns}$

1.2.5.1 Supported CPU cores

The Flasher PRO supports the following CPU cores:

ARM Cortex

- Cortex-A5
- Cortex-A8
- Cortex-A9
- Cortex-R4
- Cortex-R5
- Cortex-M0
- Cortex-M0+
- Cortex-M1
- Cortex-M3
- Cortex-M4

ARM (legacy cores)

- ARM720T
- ARM7TDMI
- ARM7TDMI-S
- ARM920T
- ARM922T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- ARM1136JF-S
- ARM1136J-S
- ARM1156T2-S
- ARM1156T2F-S
- ARM1176JZ-S
- ARM1176JZF
- ARM1176JZF-S

Renesas RX

- RX111
- RX210
- RX220
- RX21A
- RX610
- RX621
- RX62G
- RX62N
- RX62T
- RX630
- RX631
- RX63N
- RX63T

Freescale Power PC

- e200z0

1.2.5.2 Supported Target interfaces

The Flasher PRO supports the following target interfaces:

- JTAG
- SWD
- FINE
- SPD

1.2.5.3 Flasher PRO download speed

The following table lists the Flasher PRO performance values for writing to memory (RAM) via the JTAG interface:

Hardware	ARM7 memory download
Flasher PRO	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.6 Specifications for Flasher Portable PLUS

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64 Microsoft Windows 10 Microsoft Windows 10 x64
Operating Temperature	+5 °C ... +60 °C (normal operation) +5 °C ... +45 °C (battery charging)
Storage Temperature	-20 °C ... +45 °C
Relative Humidity (non-condensing)	<90% rH
Power Supply	a) USB powered, 100mA for Flasher Portable PLUS. 500 mA if target is powered by Flasher Portable PLUS b) Rechargeable 680mAh Li-Ion battery (Sony US14500VR)
Charging via USB	70 minutes (at 1A charging current)
Mechanical	
Size (without cables)	126mm x 70mm x 28mm
Weight (without cables)	140g
Available interfaces	
USB Host interface	USB 2.0
Target interface	Standard 20-pin 0.1" connector (Adapters available).
Target Interface, Electrical	
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 4.5V, max. (depends on the current battery voltage).
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range ($1.8V \leq VIF \leq 5V$)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For $1.8V \leq VIF \leq 3.6V$	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For $3.6 \leq VIF \leq 5V$	

LOW level output voltage (VOL) with a load of 10 kOhm	VOL \leq 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH \geq 80% of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz
Data input rise time (Trdi)	Trdi \leq 20ns
Data input fall time (Tfdi)	Tfdi \leq 20ns
Data output rise time (Trdo)	Trdo \leq 10ns
Data output fall time (Tfdo)	Tfdo \leq 10ns
Clock rise time (Trc)	Trc \leq 10ns
Clock fall time (Tfc)	Tfc \leq 10ns

1.2.6.1 Supported CPU cores

The Flasher Portable PLUS supports the following CPU cores:

ARM Cortex

- Cortex-A5
- Cortex-A8
- Cortex-A9
- Cortex-R4
- Cortex-R5
- Cortex-M0
- Cortex-M0+
- Cortex-M1
- Cortex-M3
- Cortex-M4

ARM (legacy cores)

- ARM720T
- ARM7TDMI
- ARM7TDMI-S
- ARM920T
- ARM922T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- ARM1136JF-S
- ARM1136J-S
- ARM1156T2-S
- ARM1156T2F-S
- ARM1176JZ-S
- ARM1176JZF
- ARM1176JZF-S

Renesas RX

- RX111
- RX210
- RX220
- RX21A
- RX610
- RX621
- RX62G
- RX62N
- RX62T

- RX630
- RX631
- RX63N
- RX63T

Freescale Power PC

- e200z0

1.2.6.2 Supported Target interfaces

The Flasher Portable PLUS supports the following target interfaces:

- JTAG
- SWD
- FINE
- SPD

1.2.6.3 Flasher Portable PLUS download speed

The following table lists the Flasher Portable PLUS performance values for writing to memory (RAM) via the JTAG interface:

Hardware	ARM7 memory download
Flasher PRO	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

1.2.7 Specifications for Flasher Portable

General	
Supported OS	Microsoft Windows 2000 Microsoft Windows XP Microsoft Windows XP x64 Microsoft Windows 2003 Microsoft Windows 2003 x64 Microsoft Windows Vista Microsoft Windows Vista x64 Microsoft Windows 7 Microsoft Windows 7 x64 Microsoft Windows 8 Microsoft Windows 8 x64
Operating Temperature	+5 °C ... +60 °C
Storage Temperature	-20 °C ... +65 °C
Relative Humidity (non-condensing)	<90% rH
Power Supply	a) USB powered, 100mA for Flasher Portable. 500 mA if target is powered by Flasher Portable b) Batteries powered (3xAAA)
Mechanical	
Size (without cables)	130mm x 65mm x 25mm
Weight (without cables)	120g
Available interfaces	
USB Host interface	USB 2.0
Target interface	Standard 20-pin 0.1" connector (Adapters available).
Target Interface, Electrical	
Target interface voltage (VIF)	1.2 ... 5V
Target supply voltage	Supply voltage is 4.5V, max. (depends on the current battery voltage).
Target supply current	max. 400mA
Reset Type	Open drain. Can be pulled low or tristated
Reset low level output voltage (VOL)	VOL ≤ 10% of VIF
For the whole target voltage range (1.8V ≤ VIF ≤ 5V)	
LOW level input voltage (VIL)	VIL ≤ 40% of VIF
HIGH level input voltage (VIH)	VIH ≥ 60% of VIF
For 1.8V ≤ VIF ≤ 3.6V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 10% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 90% of VIF
For 3.6V ≤ VIF ≤ 5V	
LOW level output voltage (VOL) with a load of 10 kOhm	VOL ≤ 20% of VIF
HIGH level output voltage (VOH) with a load of 10 kOhm	VOH ≥ 80% of VIF
JTAG Interface, Timing	
Max. JTAG speed	up to 12MHz

Data input rise time (Trdi)	Trdi ≤ 20ns
Data input fall time (Tfdi)	Tfdi ≤ 20ns
Data output rise time (Trdo)	Trdo ≤ 10ns
Data output fall time (Tfdo)	Tfdo ≤ 10ns
Clock rise time (Trc)	Trc ≤ 10ns
Clock fall time (Tfc)	Tfc ≤ 10ns

1.2.7.1 Supported CPU cores

The Flasher Portable supports the following CPU cores:

ARM Cortex

- Cortex-A5
- Cortex-A8
- Cortex-A9
- Cortex-R4
- Cortex-R5
- Cortex-M0
- Cortex-M0+
- Cortex-M1
- Cortex-M3
- Cortex-M4

ARM (legacy cores)

- ARM720T
- ARM7TDMI
- ARM7TDMI-S
- ARM920T
- ARM922T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- ARM1136JF-S
- ARM1136J-S
- ARM1156T2-S
- ARM1156T2F-S
- ARM1176JZ-S
- ARM1176JZF
- ARM1176JZF-S

Renesas RX

- RX111
- RX210
- RX220
- RX21A
- RX610
- RX621
- RX62G
- RX62N
- RX62T
- RX630
- RX631
- RX63N
- RX63T

Freescale Power PC

- e200z0

1.2.7.2 Supported Target interfaces

The Flasher Portable supports the following target interfaces:

- JTAG
- SWD
- FINE
- SPD

1.2.7.3 Flasher Portable download speed

The following table lists the Flasher Portable performance values for writing to memory (RAM) via the JTAG interface:

Hardware	ARM7 memory download
Flasher PRO	720 Kbytes/s (12MHz JTAG)

Note

The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

Chapter 2

Working with Flasher

This chapter describes functionality and how to use Flasher.

2.1 Flasher Portable PLUS

The Flasher Portable PLUS is a portable version of SEGGER's Flasher family, which has been designed to fill the need of an extremely portable, production grade, Flash programmer used for in-field firmware updates. No need to be tethered to an outlet, it is powered by an integrated Li-Ion cell (680mAh). The Flasher Portable PLUS programs flash targets in stand-alone mode or via J-Flash PC software.

Furthermore the Flasher Portable PLUS allows the user to select between eight data images to be programmed. The images can be easily selected by using the SEL button on the front of the housing. For more information about support for multiple images, please refer to *Multiple File Support* on page 57.

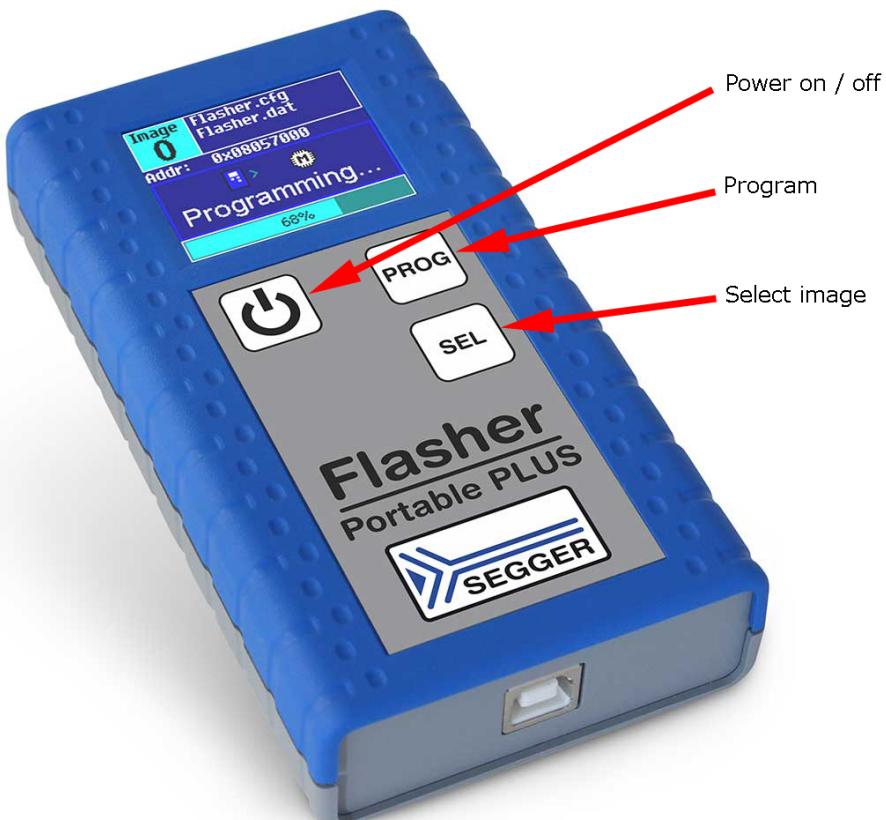
For setup and configuration purposes, the Flasher Portable PLUS connects to a PC via USB interface, running Microsoft Windows 2000, Windows XP, Windows 2003, Windows Vista, Windows 7, Windows 8 or Windows 10 and has a built-in standard 20-pin J-Link target connector.

Note

Ethernet and RS232 as host interface are not available for the Flasher Portable PLUS.

2.1.1 Housing & Buttons

The Flasher Portable PLUS comes with a display, which is for example used to represent the status of an ongoing flash progress. Furthermore, there are three buttons which allow the user to control Flasher Portable PLUS. For a detailed description of the functions, take a look at the table below:



Button	Description
Program	Start programming process with the currently selected image.

Button	Description
Select image	Select the image to be programmed next time the Program button is pressed.
Power on/off	Used to power-on / power-off the Flasher Portable PLUS. Please note that to power up the Flasher Portable PLUS, the button should be hold for at least 1 second to make sure software can boot and take control of power circuit, so the Flasher Portable PLUS keeps powered, after releasing the button.

2.2 Flasher Portable

The Flasher Portable is a portable version of SEGGER's Flasher family, which has been designed to fill the need of an extremely portable, production grade, Flash programmer used for in-field firmware updates. No need to be tethered to an outlet, it is powered by three standard AAA batteries. The Flasher Portable programs flash targets in stand-alone mode or via J-Flash PC software.

Furthermore the Flasher Portable allows the user to select between four data images to be programmed. The images can be easily selected by using the arrow buttons on the front of the housing. For more information about support for multiple images, please refer to *Multiple File Support* on page 57.

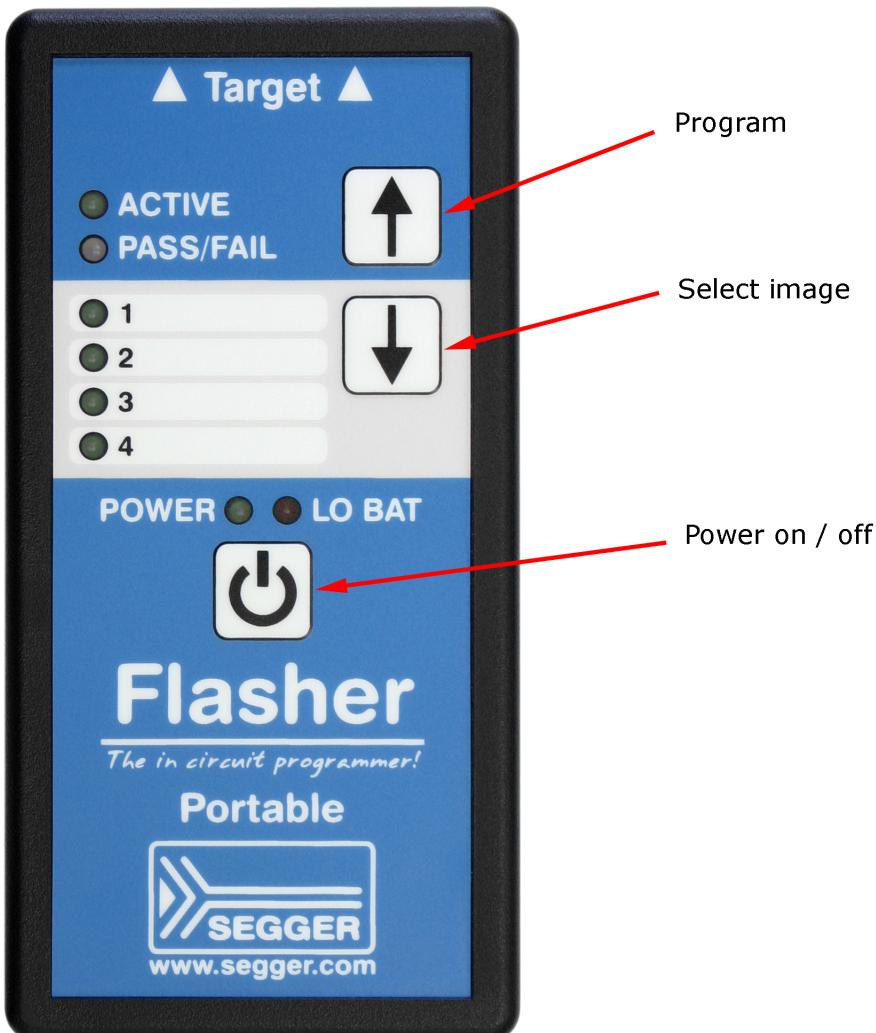
For setup and configuration purposes, the Flasher Portable connects to a PC via USB interface, running Microsoft Windows 2000, Windows XP, Windows 2003, Windows Vista, Windows 7 or Windows 8 and has a built-in standard 20-pin J-Link target connector.

Note

Ethernet and RS232 as host interface are not available for the Flasher Portable.

2.2.1 Housing & Buttons

The Flasher Portable comes with several LEDs which are for example used to represent the status of an ongoing flash progress. Furthermore, there are three buttons which allow the user to control Flasher Portable. For a detailed description of the functions, take a look at the two tables below:

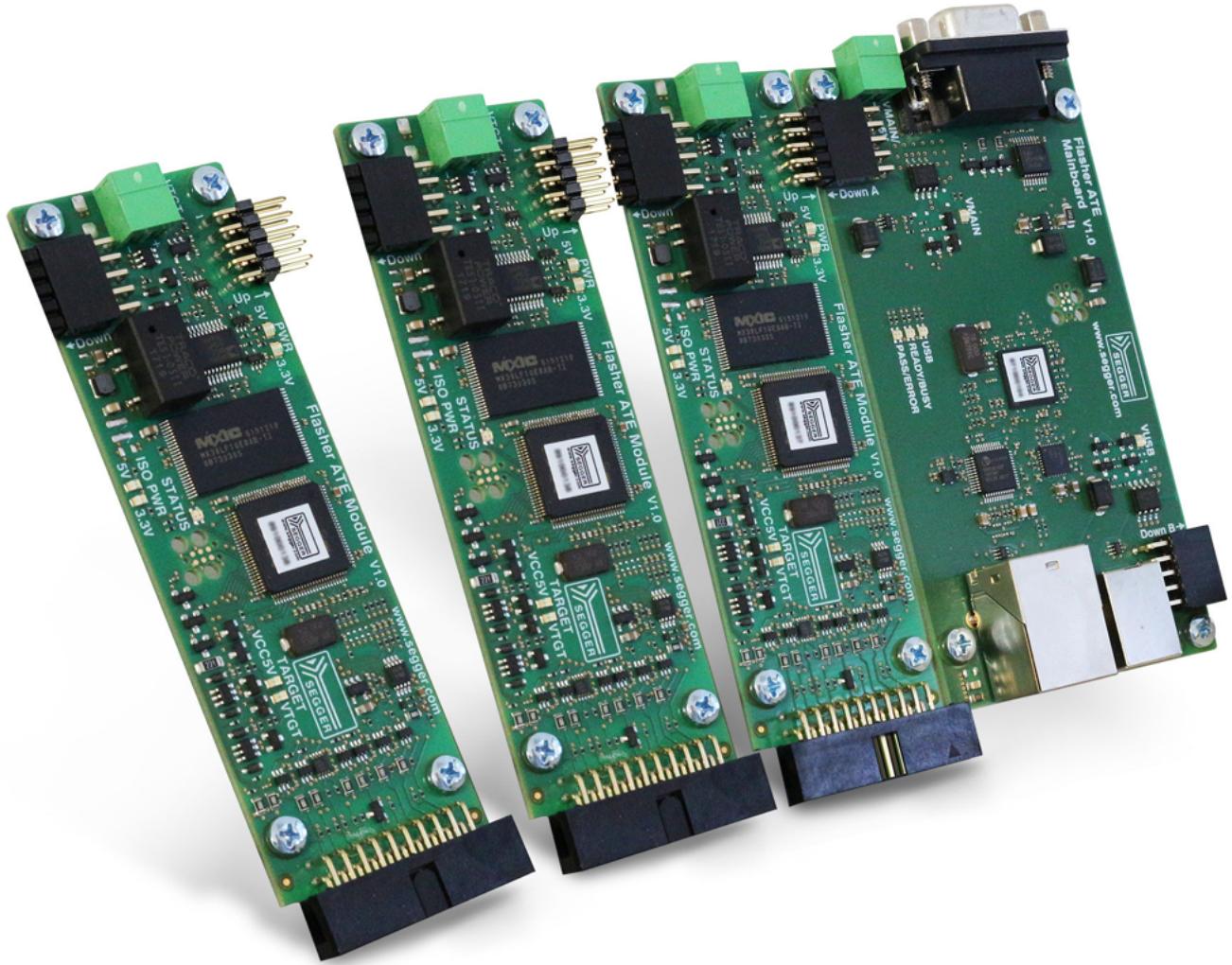


LED	Description
ACTIVE	GREEN. Blinks while the Flasher Portable is busy / performs operations on the target.
PASS/FAIL	GREEN/RED. Indicates, if the last flashing cycle was successful. <ul style="list-style-type: none"> • GREEN: Flashing cycle completed successfully. • RED: Flashing cycle completed with error.
1/2/3/4	GREEN: Indicates which image is currently selected for programming. For more information about multiple image support, please refer to <i>Multiple File Support</i> on page 57.
POWER	GREEN: Indicates if Flasher is currently powered. Blanks while Flasher Portable tries to enumerate via USB.
LO BAT	1) The LED is off, meaning battery voltage is > 3.3V 2) The red LED is constant on if battery voltage is low (\leq 3.3V). In this mode, Flasher still allows programming. 3) The red LED starts blinking in case of the battery voltage is below 3.0V. Flasher refuse programming attempts.

Button	Description
Program	Start programming process with the currently selected image.
Select image	Select the image to be programmed next time the Program button is pressed.
Power on/off	Used to power-on / power-off the Flasher Portable. Please note that to power up the Flasher Portable, the button should be hold for at least 1 second to make sure software can boot and take control of power circuit, so the Flasher Portable keeps powered, after releasing the button

2.3 Flasher ATE

The Flasher ATE is the version of SEGGER's Flasher family, which has been designed to be used in conjunction with automated test equipment (ATE). It is modular and scalable from 1 to 10 individual programming modules.



2.3.1 Power supply

2.3.1.1 Mainboard power supply

The Flasher ATE mainboard is powered either by a dedicated power connector (VMAIN) or by USB. The power source may not be switched, while the mainboard is running.

2.3.1.2 Target power supply

For target power supply, the Flasher ATE offers two options and additionally a power discharge option is available. The target in general is electrically isolated from the mainboard's power supply.

The first option is to use the VTGT connector. It is possible to provide any voltage between 3 and 15V, however the target interface is limited to 1,5V - 5V signal voltage. The VTGT connector is directly coupled to the target, so any electric isolation has to be done in the external power supply.

The second option is to use the module's internal power source VCC5V, which is able to deliver up to 100mA to the target.

The power discharge option applies, when the operation has finished. A 220 ohms resistor is used to discharge any remaining charge on the target's capacitors. As the thermal load of the resistor is limited, the target's capacity may not exceed 27mF at 15V or 250mF at 5V supply voltage.

Note

There is no protection against overload when using the internal power source.

2.3.2 UART to TCP transceiver

The Flasher ATE includes an UART to TCP transceiver. Each module is accessible via a connection to a module specific TCP port.

Module	TCP port
#1	41
#2	42
#3	43
#4	44
#5	45
#6	46
#7	47
#8	48
#9	49
#10	50

While the transceiver is switched off, status messages from the module are sent out to the terminal. If the transceiver is active, incoming data on pin17 on the debug interface is sent to the TCP connection and incoming data on the TCP connection is sent to pin 5 on the debug interface. The parameters for the UART configuration are handed over when activating the transceiver mode with the command #TERMINAL <Baudrate>,<Databits>,<Parity>,<Stopbits> on page 89.

Note

The transceiver cannot be enabled, while the module is programming and vice versa.

2.4 File system

The Flasher supports 8.3 filenames only (8 characters filename, 3 characters file extension). Using longer filenames may result in incorrect operation. Integrated functions, like the FTP server or the terminal server, will refuse writing files with long filenames.

2.5 Setting up the IP interface

Setting up the IP interface Some of the Flasher models come with an additional Ethernet interface to communicate with the host system. These Flashers also come with a built-in webserver which allows some basic setup of the emulator, e.g. configuring a default gateway which allows using it even in large intranets. For more information, please refer to *TCP Services* on page 75.

2.5.1 Connecting the first time

When connecting Flasher the first time, it attempts to acquire an IP address via DHCP. The recommended way for finding out which IP address has been assigned to Flasher is, to use the J-Link Configurator. The J-Link Configurator is a small GUI-based utility which shows a list of all emulator that are connected to the host PC via USB and Ethernet. For more information about the J-Link Configurator, please refer to [UM08001_JLink.pdf](#) (*J-Link / J-Trace user guide*), chapter *Setup*, section *J-Link Configurator*. The setup of the IP interface of Flasher is the same as for other emulators of the J-Link family. For more information about how to setup the IP interface of Flasher, please refer to [UM08001_J-Link / J-Trace User Guide](#), chapter *Setup*, section *Setting up the IP interface*. For more information about how to use Flasher via Ethernet or prepare Flasher via Ethernet for stand-alone mode, please refer to *Operating modes* on page 42.

2.6 Operating modes

All Flashers except the Flasher ATE are able to boot in 3 different modes:

- J-Link mode
- Stand-alone mode
- MSD (Mass storage device) mode

Note

The Flasher ATE only supports the stand-alone mode.

Definition J-Link mode

Flasher is connected to a PC via USB/Ethernet and controlled by a PC application (J-Flash). If there is an RS232 connection to a PC, does not have any influence on if J-Link mode is entered or not. In this mode, Flasher can be used as a J-Link and controlled by the software in the J-Link software and documentation package (J-Link Commander, J-Flash, ...)

Definition Stand-alone mode

This mode is entered when there is no active USB/Ethernet connection to a host PC, e.g. if Flasher is only powered via a USB power supply.

Definition MSD mode

Entered only if Flasher Start/Stop button (on Flasher Portable the "PROG" button) is kept pressed for at least 2 seconds while connection Flasher via USB. In this mode, Flasher enumerates as a mass storage device (like an USB Stick) at the host PC. In this mode, configuration + data files can be manually placed on the Flasher and the flasher logfile can be read out.

2.6.1 J-Link mode

If you want to use Flasher for the first time you need to install the J-Link software and documentation package. After installation, connect Flasher to the host PC via USB or Ethernet. For more information about how to install the J-Link software and documentation package please refer to the *J-Link / J-Trace User Guide*, chapter *Setup* which can be downloaded from <https://www.segger.com/jlink-software.html>.

2.6.1.1 Connecting the target system

Power-on sequence

In general, Flasher should be powered on before connecting it with the target device. That means you should first connect Flasher with the host system via USB / Ethernet and then connect Flasher with the target device via JTAG or SWD. Power-on the device after you connected Flasher to it. Flasher will boot in "J-Link mode".

Verifying target device connection with J-Link.exe

If the USB driver is working properly and your Flasher is connected with the host system, you may connect Flasher to your target hardware. Then start the J-Link command line tool `JLink.exe`, which should now display the normal Flasher related information and in addition to that it should report that it found a JTAG target and the targets core ID. The screenshot below shows the output of `JLink.exe`.

The screenshot shows the J-Link Commander window with the following text output:

```

J-Link C:\Program Files (x86)\SEGGER\JLinkARM_V458a\JLink.exe
SEGGER J-Link Commander V4.58a <?> for help>
Compiled Dec 5 2012 18:38:27
DLL version V4.58a, compiled Dec 5 2012 18:38:08
Firmware: J-Link ARM / Flasher ARM V3 compiled Nov 28 2012 18:57:58
Hardware: V3.00
S/N: 163000100
Feature(s): JFlash
IP-Addr.: No configuration received via DHCP
UTarget = 3.267V
Info: TotalIRLen = 4, IRPrint = 0x01
Found 1 JTAG device, Total IRLen = 4:
#0 Id: 0x3F0F0F0F, IRLen: 04, IRPrint: 0x1, ARM7TDMI Core
Found ARM with core Id 0x3F0F0F0F <ARM7>
JTAG speed: 100 kHz
J-Link>_

```

2.6.1.2 LED status indicators

In J-Link mode, there are also certain LED status codes defined:

#	Status of LED	Meaning
0	GREEN high frequency blinking (On/Off time: 50ms => 10Hz)	Flasher is waiting for USB enumeration or ethernet link. As soon as USB has been enumerated or ethernet link has been established, the green LED stops flashing and is switched to constant green.
0	GREEN constant	Flasher enumeration process is complete and it is ready to be controlled by a PC application.

2.6.2 Stand-alone mode

In order to use Flasher in “stand-alone mode”, it has to be configured first, as described in *Setting up Flasher for stand-alone mode* on page 47. To boot Flasher in the “stand-alone mode”, only the power supply to Flasher has to be enabled (Flasher should not be connected to a PC). In the “stand-alone mode” Flasher can be used as a stand-alone flash programmer.

Note

Flasher can only program the target device it was configured for. In order to program another target device, you have to repeat the steps described in *Setting up Flasher for stand-alone mode* on page 47.

2.6.2.1 LED status indicators

Progress and result of an operation is indicated by Flasher’s LEDs. The behavior is different for J-Link and stand-alone mode. For a definition of the different modes, please refer to *Operating modes* on page 42.

The following table describes the behavior of the LEDs in stand-alone mode.

#	Status of LED	Meaning
0	GREEN constant	Flasher waits for a start trigger to perform an operation in stand-alone mode.
1	GREEN slow blinking	Flashing operation in progress: <ul style="list-style-type: none"> Erasing (slow blinking on/off time: 80 ms => 6.25 Hz) Programming (slow blinking on/off time: 300ms => ~1.67 Hz) Verifying (slow blinking, on/off time: 100ms => 5 Hz)
2	GREEN: constant RED: off or constant	GREEN constant, RED off: Operation successful. GREEN constant, RED constant: Operation failed Goes back to state #0 automatically, but in case of operation failed, RED remains on until state #1 is entered the next time.

Older Flasher models have a different behavior. The following serial number ranges behave different from the table above Flashers with the following serial number ranges behave different and comply to the table below:

- 1621xxxxx (Flasher ARM V2)
- 1630xxxxx (Flasher ARM V3)
- 4210xxxxx (Flasher PPC V1)
- 4110xxxxx (Flasher RX V1)

#	Status of LED	Meaning
0	GREEN high frequency blinking (On/Off time: 50ms => 10Hz)	Flasher is waiting for USB enumeration or ethernet link. As soon as USB has been enumerated or ethernet link has been established, the green LED stops flashing and is switched to constant green. In stand-alone-mode, Flasher remains in the high frequency blinking state until state #1 is reached. Flasher goes to state #1 as soon as a #START command has been received via the ASCII interface or the Start button has been pushed.
1	GREEN constant	Connect to target and perform init sequence.
2	GREEN slow blinking	Flashing operation in progress: <ul style="list-style-type: none"> Erasing (slow blinking on/off time: 80 ms => 6.25 Hz) Programming (slow blinking on/off time: 300ms => ~1.67 Hz) Verifying (slow blinking, on/off time: 100ms => 5 Hz)
3	GREEN: constant RED: off or constant	GREEN constant, RED off: Operation successful. GREEN constant, RED constant: Operation failed Goes back to state #0 automatically, but in case of operation failed, RED remains on until state #1 is entered the next time.

The Flasher ATE uses different LEDs, see the following table:

#	Status of mainboard LEDs	Meaning
0	USB GREEN high frequency blinking (On/Off time: 50ms => 10Hz)	The mainboard is waiting for USB enumeration. As soon as USB has been enumerated, the green LED stops flashing and is switched to constant green.
1	USB GREEN constant	The mainboard has completed USB enumeration or no USB connection present.
2	USB RED constant	The mainboard is in bootloader mode.
3	READY/BUSY GREEN	No operation, the Flasher is ready.
4	READY/BUSY RED	Flashing operation in progress on at least one module.
5	PASS/ERROR GREEN	The last operation was successful on all modules.
6	PASS/ERROR RED	The last operation has failed on at least one module.
7	VMAIN GREEN	VMAIN is used as module power supply.
8	VUSB GREEN	VUSB is used as module power supply ¹ .

¹ The VUSB LED does not work on some mainboards. This has no influence on functionality.

#	Status of module LED	Meaning
0	STATUS GREEN short flashing	The module waits for a start trigger.
1	STATUS GREEN slow blinking	Flashing operation in progress: <ul style="list-style-type: none"> Erasing (slow blinking on/off time: 80 ms => 6.25 Hz) Programming (slow blinking on/off time: 300ms => ~1.67 Hz) Verifying (slow blinking, on/off time: 100ms => 5 Hz)
2	STATUS RED constant	a) The module is in bootloader mode. b) The last operation has failed.
3	5V GREEN	Internal power supply for 5V is working.
4	3.3V GREEN	Internal power supply for 3.3V is working.
5	VCC5V GREEN	Target power is enabled via internal supply.
6	VTGT GREEN	Target power is enabled via external supply.

2.6.3 MSD mode

When pressing the Start/Stop button of Flasher while connecting it to the PC, Flasher will boot in the “MSD mode”. This mode can be used to downdate a Flasher firmware version if a firmware update did not work properly and it can be used to configure Flasher for the “stand-alone mode”, without using J-Flash. If Flasher has been configured for “stand-alone mode” as described in the section above, there will be four files on the MSD, `FLASHER.CFG`, `FLASHER.DAT`, `FLASHER.LOG`, `SERIAL.TXT`.



`FLASHER.CFG` contains the configuration settings for programming the target device and `FLASHER.DAT` contains the data to be programmed. `FLASHER.LOG` contains all logging information about the commands, performed in stand-alone mode. The `SERIAL.TXT` contains the serial number, which will be programmed next. J-Flash supports to configure Flasher for automated serial number programming.

Currently, J-Flash does not support to configure Flasher for automated serial number programming.

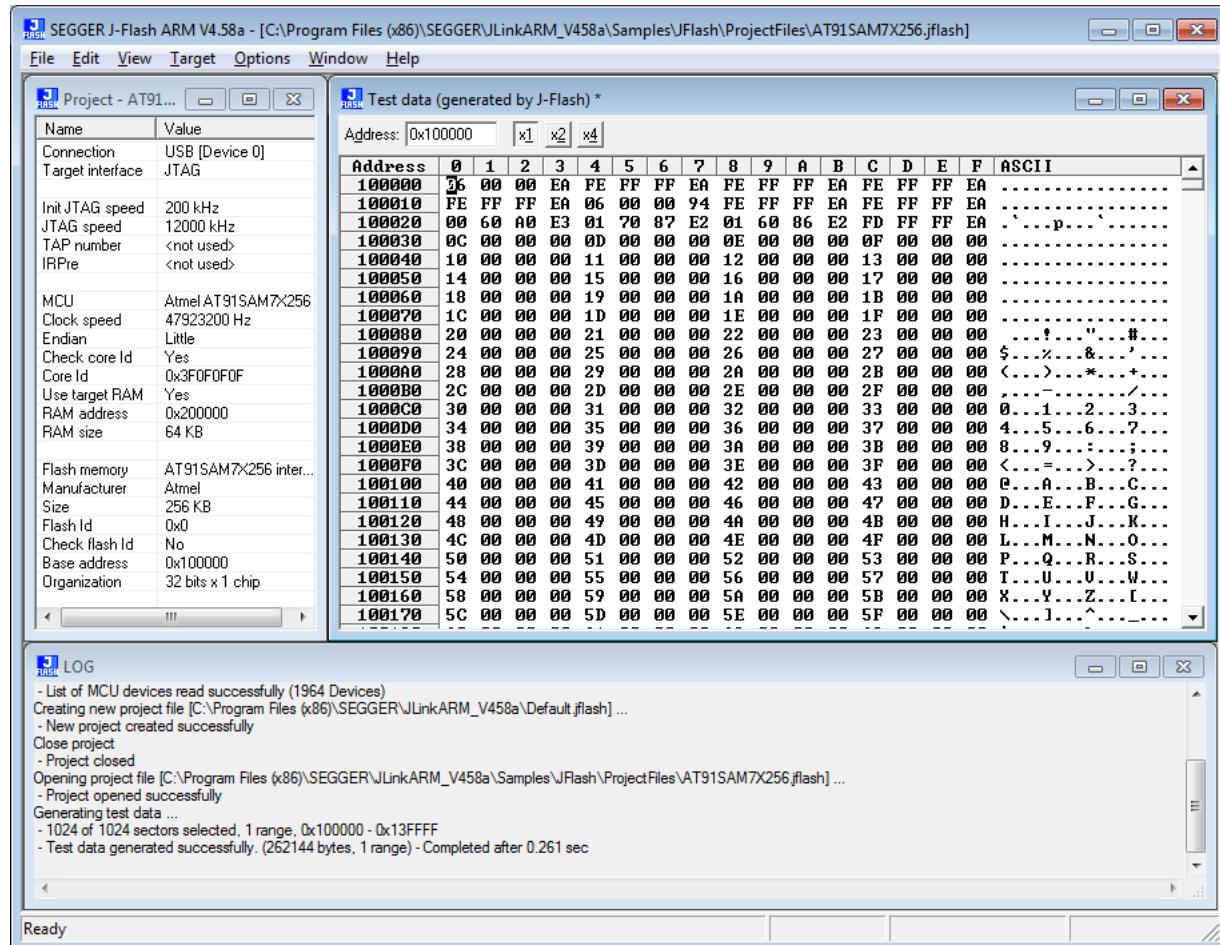
If you want to configure multiple Flasher for the same target you do not have to use J-Flash all the time. It is also possible to copy the `FLASHER.CFG` and the `FLASHER.DAT` files from a configured Flasher to another one. To copy these files boot Flasher in “MSD mode”.

2.7 Setting up Flasher for stand-alone mode

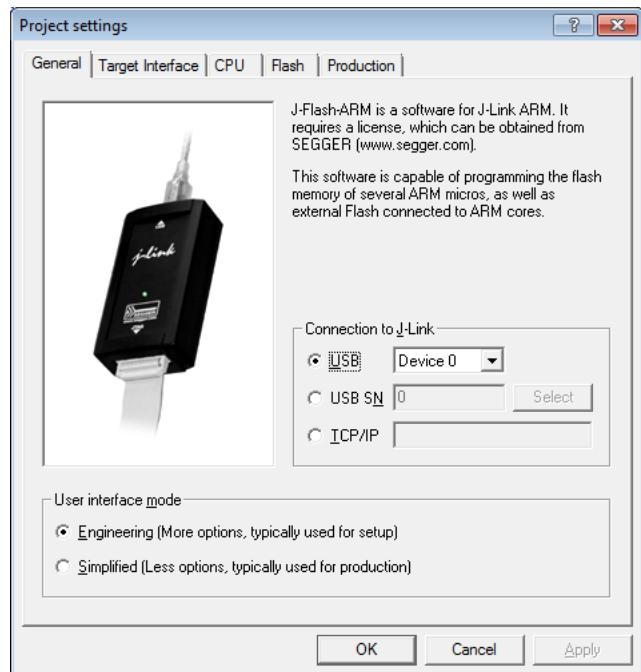
In order to set up Flasher for the stand-alone mode it needs to be configured once using the J-Flash software. For more information about J-Flash, please refer to the *J-Flash User Guide*.

After starting J-Flash, open the appropriate J-Flash project for the target Flasher shall be configured for, by selecting **File -> Open Project**. If J-Flash does not come with an appropriate sample project for the desired hardware, a new project needs to be created by selecting **File -> New Project**.

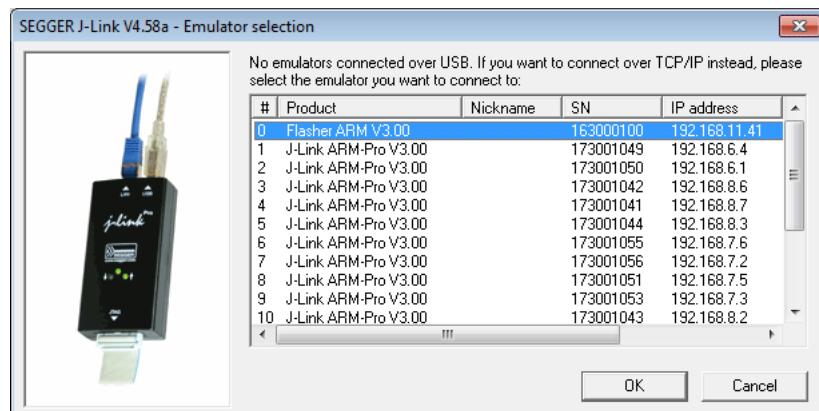
After the appropriate project has been opened / created, the data file which shall be programmed needs to be loaded, by selecting **File -> Open**. After this J-Flash should look like in the screenshot below.



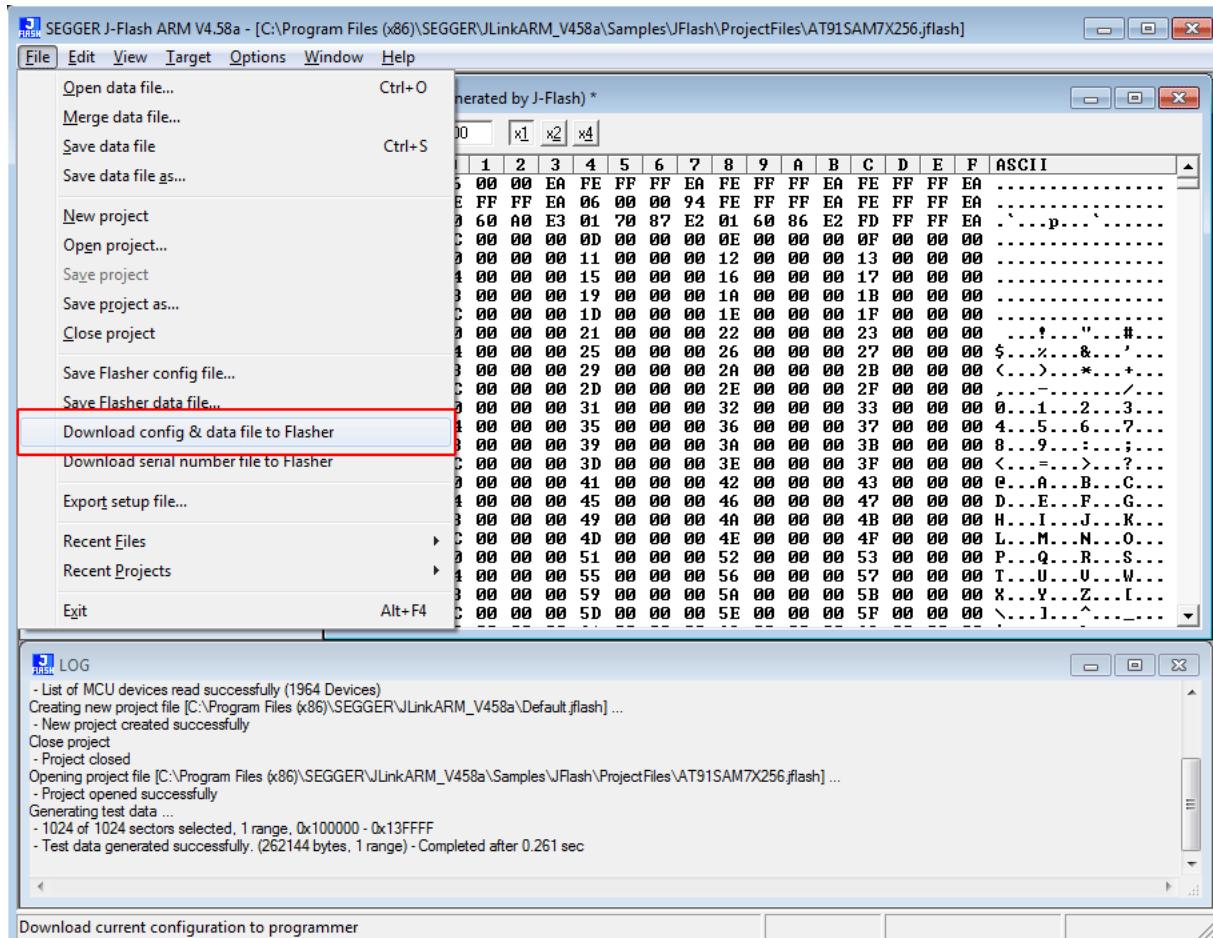
Before downloading the configuration (project) and program data (data file) to Flasher, the connection type (USB/IP) needs to be selected in the project. These settings are also saved on a per-project basis, so this also only needs to be setup once per J-Flash project. The connection dialog is opened by clicking **Options -> Project settings -> General**.



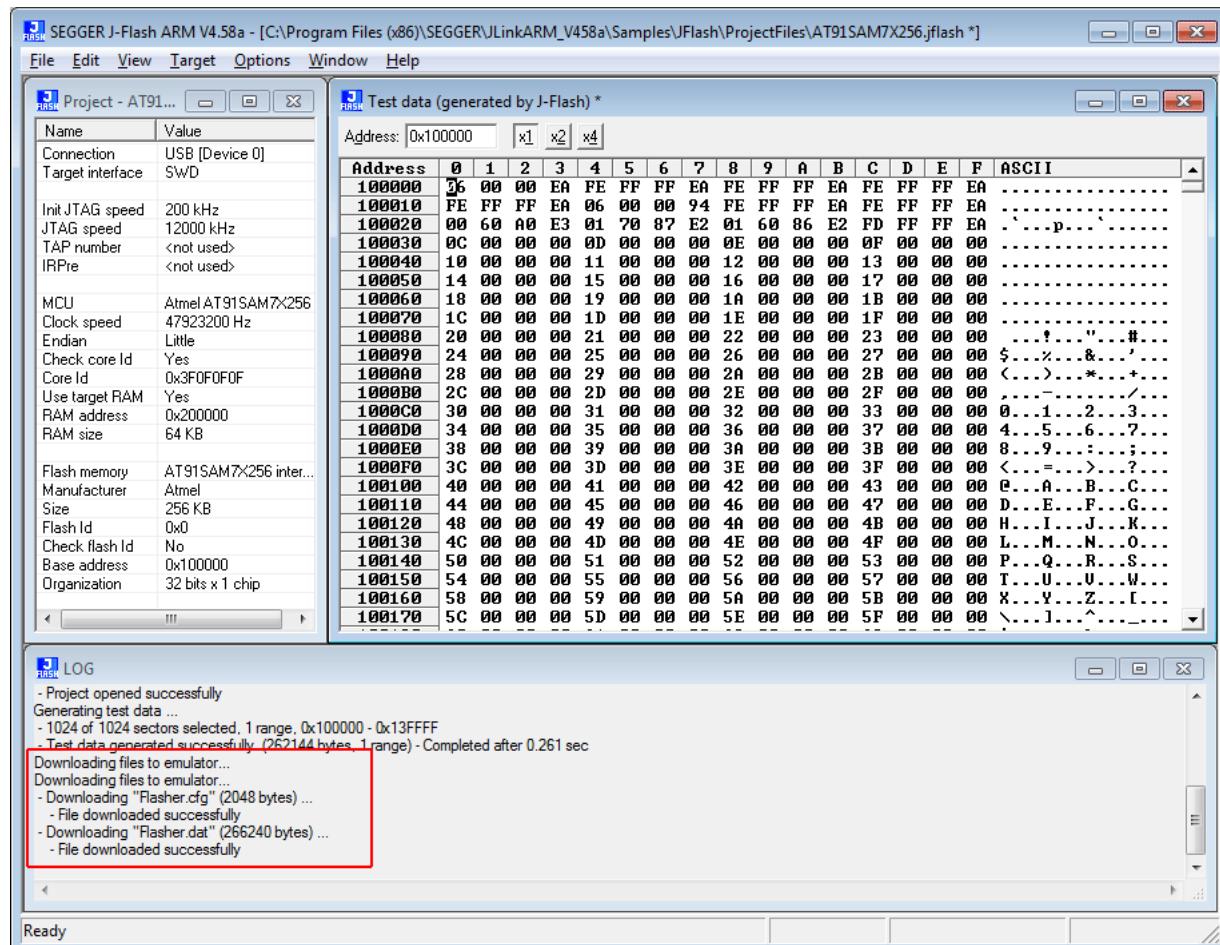
The connection dialog allows the user to select how to connect to Flasher. When connecting to a Flasher via TCP/IP it is not mandatory to enter an IP address. If the field is left blank and **File -> Download to programmer** is selected, an emulator selection dialog pops up which shows all Flasher which have been found on the network. The user then can simply select the Flash he wants to download the configuration to.



In order to download the configuration and program data to the Flasher, simply select **File -> Download config & data file to Flasher**.



The J-Flash log window indicates that the download to the emulator was successful.



From now on, Flasher can be used in stand-alone mode (without host PC interaction) for stand-alone programming.

2.7.1 Preparing for stand-alone operation manually

As an alternative, J-Flash can also be used to save config and data file to a hard drive.

This files can later be copied to a Flasher without using J-Flash, which is useful to prepare additional Flasher for stand-alone programming, if for example a company plans to widen its production, new Flasher units can be bought and used in production by simply copying the files to the new units.

Creating config and data files

J-Flash config (*.CFG) and data (*.DAT) files can be created by using the "Save Flasher config file..." and "Save Flasher data file..." options in the "File" menu.

For some devices, additional files (*.PEX) are needed. J-Flash will create a subdirectory (in the same directory as the config file) with the same name as the config file and place the files needed in this directory.

About *.PEX files

When using the "Save Flasher config file..." menu point or when using the "multiple configurations stored on Flasher" feature (See *Multiple File Support* on page 57.) feature, the following needs to be taken care of:

For some devices, special connect, reset etc. sequences are necessary which are stored in so-called *.PEX files on the Flasher.

When using the "Download config & data file to Flasher", J-Flash takes care of correct use and download of these files to Flasher.

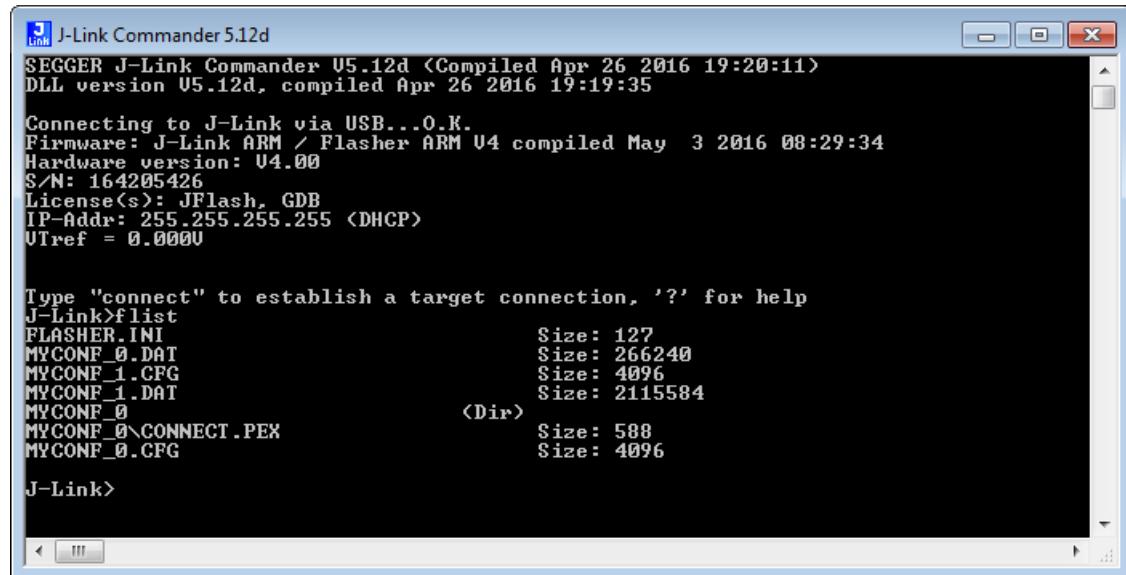
When creating the config files manually and later download them to the Flasher manually, it is user's responsibility to put them at the right place.

The *.PEX files need to be placed in a subdirectory with the same name as the corresponding *.cfg file. J-Flash creates a directory with the correct name automatically when a config file is created.

Example:

MyConf0 is a project for a device that requires a *.PEX file for connect.

MyConf1 is a project for a device that requires no *.PEX file at all.



J-Link Commander 5.12d
SEGGER J-Link Commander V5.12d <Compiled Apr 26 2016 19:20:11>
DLL version V5.12d, compiled Apr 26 2016 19:19:35

Connecting to J-Link via USB...O.K.
Firmware: J-Link ARM / Flasher ARM V4 compiled May 3 2016 08:29:34
Hardware version: V4.00
S/N: 164205426
License(s): JFlash, GDB
IP-Addr: 255.255.255.255 <DHCP>
UTref = 0.0000

Type "connect" to establish a target connection, '?' for help
J-Link>flist

FLASHER.INI	Size: 127
MYCONF_0.DAT	Size: 266240
MYCONF_1.CFG	Size: 4096
MYCONF_1.DAT	Size: 2115584
MYCONF_0	<Dir>
MYCONF_0\CONNECT.PEX	Size: 588
MYCONF_0.CFG	Size: 4096

J-Link>

2.8 Universal Flash Loader mode

As an alternative to the stand-alone mode, configured via J-Flash, there is the Universal Flash Loader mode. While the normal stand-alone mode relies on using the debug interface of the device, the Universal Flash Loader mode uses device or vendor specific programming interfaces and protocols and therefore it is independent of the CPU core.

The Universal Flash Loader is available for the Flasher PRO and for the Flasher Portable. For some of the supported devices, SEGGER offers specific adapters.

2.8.1 Preparing manually

The Universal Flash Loader uses an initialization file (*.UNI), a device specific flash programming algorithm (*.PEX) and a data file (*.HEX, *.MOT, *.BIN or *.DAT).

2.8.1.1 Configuration

The initialization file basically is split into three parts. The first part, the section [DEVICE], controls the generic behavior of the Universal Flash Loader. It specifies, which protocol driver and data file to use. It allows to enable and configure target powering. And it defines, which actions to perform. The second part consists of one or more [BANKx] sections, which contain information about the memories. The third part, the section [CONFIG], includes configuration settings for the protocol driver.

An .ini file might be as follows:

```
[OPTIONS]
TargetPower = "0"
ChipErase = "0"

[TASKS]
CheckBlank = "1"
Erase = "1"
Program = "1"
Verify = "1"
Secure = "1"

[DEVICE]
Algo = "RL78.PEX"
Data = "ALL_6k.mot"
Offset = "0x00000000"

[BANK0]
; Code Flash
Base = "0x00000000"
Size = "0x00004000"
Sect = "0x00000400"

[BANK1]
; Data Flash
Base = "0x000F1000"
Size = "0x00000800"
Sect = "0x00000400"

[CONFIG]
BaudRate = "1000000"
ClearConfigOnConnect = "0x00"
Security = "0xFF"
ShieldStart = "0x0000"
ShieldEnd = "0x000F"
```

[OPTIONS]

TargetPower

If set to a value >0, power is applied to the target. The value defines the delay (in ms) after enabling the target power supply and before starting to communicate with the target.

ChipErase

If set to 1, the chip erase function is called for erasing the chip.

Note

Do not set enable this setting, if the flash programming algorithm does not support chip erase.

[TASKS]

CheckBlank

Defines if a blank check should be performed before erasing a sector.

Erase

Defines if the sector should be erased before programming.

Program

Defines if the sector should be programmed.

Verify

Defines if the sector should be verified after programming.

Secure

Defines if the device should be secured or protected against read-out after verifying.

[DEVICE]

Algo

File name of the flash programming algorithm. This file is provided by SEGGER and will typically support a series of devices

Data

File name of the data file to program. The flasher supports the Flasher DTA, the Intel HEX, the Motorola S-Record and the binary file format. Flasher DAT files are generated by J-Flash and offer high performance together with high flexibility. The other file formats produce a small overhead, because they have to be parsed before the data can be programmed.

Offset

Offset to apply when programming a binary data file. Unless specified differently, binary files start at offset 0x00000000.

[BANKx]

x blocks with configuration data for the flash banks. All three parameters (Base, Size and Sect) are mandatory.

Base

Base address of the flash bank.

Size

Total size of the flash bank.

Sect

Sector size of the flash bank.

[CONFIG]

This section includes specific configuration data for the flash programming algorithm.
There are no general parameters.

2.8.1.2 Configuration Data for RL78/G10

The RL78/G10 series does not require any configuration data.

2.8.1.3 Configuration Data for RL78**BaudRate**

The baud rate used for programming. Possible values are 115,200, 250,000, 500,000 and 1,000,000.

ClearConfigOnConnect

If this is set to 1, the first sector holding the configuration is cleared on connect. This will especially reset the clock configuration to its default value allowing a higher programming speed.

Security

Security configuration byte:

Item	Contents
Bit 7	Fixed to 1
Bit 6	Fixed to 1
Bit 5	Fixed to 1
Bit 4	Programming disable flag (1: Enable programming, 0: Disable programming)
Bit 3	Fixed to 1
Bit 2	Block erase disable flag (1: Enable block erase, 0: Disable block erase)
Bit 1	Boot block cluster rewrite disable flag (1: Enable boot block cluster rewrite, 0: Disable boot block cluster rewrite)
Bit 0	Fixed to 1

Note

Any bit set to 0 cannot be set to 1 again.

ShieldStart

Flash shield window start block number

ShieldEnd

Flash shield window end block number

2.8.1.4 Configuration Data for STM8

HighSpeed

The interface mode (0 = low speed / 1 = high speed) used for communication.

ROP

Read out protection configuration byte: Depending on the target, 0x00 or 0xAA.

SectorSize

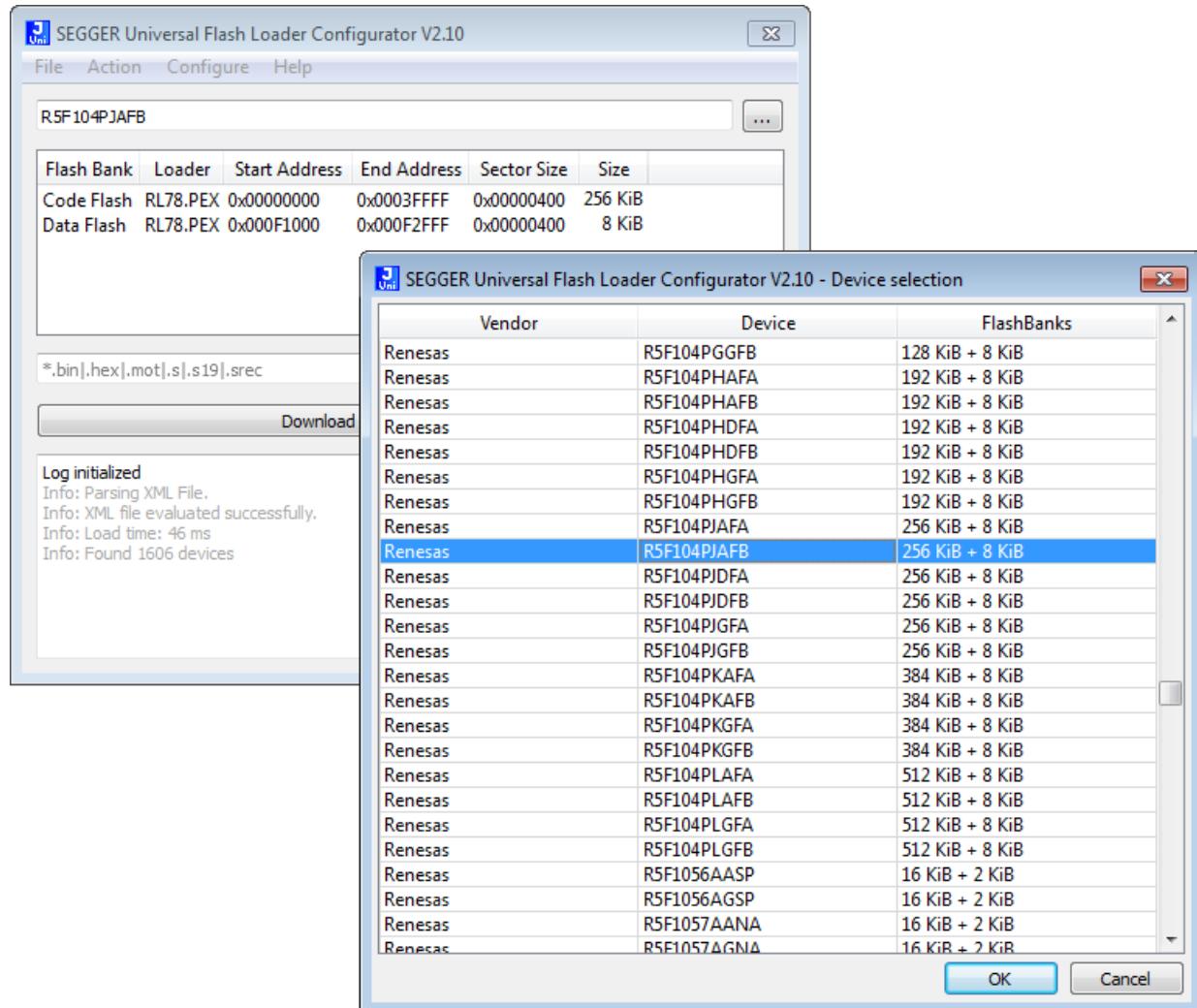
Sector size: Depending on the target: "64" for low density flash memory, "128" for medium or high density flash memory.

Note

As the STM8's option bytes are part of the data image, the data image must not enable the read out protection for the device in order to allow verification after programming. The read out protection can be set finally by enabling the step "Secure". This function only changes the ROP option byte to the appropriate value.

2.8.2 Preparing using the PC utility

In order to set up Flasher for the Universal Flash Loader mode, a PC utility called SEGGER Universal Flash Loader Configurator is available for download.



2.9 Multiple File Support

It is also possible to have multiple data files and config files on Flasher, to make Flasher more easy to use in production environment. To choose the correct configuration file and data file pair, a `FLASHER.INI` file is used. This init file contains a `[FILES]` section which describes which configuration file and which data file should be used for programming. A sample content of a `FLASHER.INI` file is shown below:

```
[FILES]
DataFile = "Flasher1.dat"
ConfigFile = "Flasher1.cfg"
```

Using this method all configuration files and data files which are used in the production only have to be downloaded once. From there on a configuration file / data file pair can be switched by simply replacing the `FLASHER.INI` by a new one, which contains the new descriptions for the configuration file and data file. The `FLASHER.INI` can be replaced in two ways:

1. Boot Flasher in MSD mode in order to replace the `FLASHER.INI`
2. If Flasher is already integrated into the production line, runs in stand-alone mode and can not be booted in other mode: Use the file I/O commands provided by the ASCII interface of Flasher, to replace the `FLASHER.INI`. For more information about the file I/O commands, please refer to *File I/O commands* on page 92.

Note

Flasher supports 8.3 filenames only (8 characters filename, 3 characters file extension). Using longer filenames may result in incorrect operation.

2.9.1 Flasher Portable specifics

Flasher Portable allows to choose between four configuration and data file pairs during runtime by using the select/arrow button on the front of Flasher Portable.

Which config / data file pair is used for which image selection position is determined by the contents of the `FLASHER.INI`. For this, the `FLASHER.INI` contents in the `[FILES]` section have been extended. The sample below shows how to enable the user to select between four different images on the Flasher portable via the select / arrow button:

```
[FILES]
DataFile = "First.dat"
ConfigFile = "First.cfg"
DataFile1 = "Second.dat"
ConfigFile1 = "Second.cfg"
DataFile2 = "Third.dat"
ConfigFile2 = "Third.cfg"
DataFile3 = "Fourth.dat"
ConfigFile3 = "Fourth.cfg"
```

Using this method, all configuration files and data files which are used in the production only have to be stored on Flasher Portable via MSD mode. From there on, switching between the files can be done by simply using the selection button of Flasher Portable.

2.9.2 Example

1 Target, 2 Datafiles (e.g. boot loader und application) -> same configuration file (*.CFG) but different data files (*.DAT) should be used.

- Open pre-configured J-Flash project
- File -> Save Flasher config file ... (DEFAULT.CFG)
- Open data file 1 (boot loader)
- File -> Save Flasher data file ... (BOOT.DAT)
- Open data file 2 (application)
- File -> Save Flasher data file ... (APP.DAT)
- Create the a FLASHER.INI file (content see below)
- Connect the Flasher in MSD mode to the PC
- Copy DEFAULT.CFG, BOOT.DAT, APP.DAT and FLASHER.INI on the Flasher

FLASHER.INI content:

```
[FILES]
DataFile      = "BOOT.DAT"
ConfigFile   = "DEFAULT.CFG"
DataFile1    = "APP.DAT"
ConfigFile1 = "DEFAULT.CFG"
```

2.10 Custom labels

Flasher supports to assign custom labels to configurations. This allows to specify easy to remember names for configurations that are stored on the Flasher.

2.10.1 Hardware and software requirements

This feature is supported by the following models:

- Flasher Portable PLUS

This feature is supported since V6.30e of the software package and firmware

2.10.2 Assigning labels

The configuration and data file pairs are specified in the `FLASHER.INI` file:

```
[FILES]
DataFile      = "IMAGE0.dat"
ConfigFile    = "IMAGE0.cfg"
DataFile1     = "IMAGE1.dat"
ConfigFile1   = "IMAGE1.cfg"
```

By default, Flasher will show the names of the configuration and data file:



By adding `DisplayName`, `DisplayName1`, ... keys to the `FLASHER.INI`, a custom label can be shown instead:

```
[FILES]
DisplayName    = "FW smartwatch"
DataFile       = "IMAGE0.dat"
ConfigFile     = "IMAGE0.cfg"
DisplayName1   = "FW smart meter"
DataFile1      = "IMAGE1.dat"
ConfigFile1    = "IMAGE1.cfg"
```

The images will now be shown as follows:



2.10.3 Considerations

- The maximum length of a custom label is 32 characters. If this length is exceeded, the label is ignored and Flasher switches back to default mode for the affected configuration.

2.11 Programming multiple targets

It is possible to program multiple targets which are located in a JTAG chain. The targets will be programmed each with a configuration and a data file. The configuration for the desired target must be selected before it can be programmed, this can be done with the #SELECT command. For more information how to use the #SELECT command please refer to Chapter "3.3.5 Commands to Flasher".

Example

Three devices should be programmed.

JTAG Chain: *TDI* -> *Device2* -> *Device1* -> *Device0* -> *TDO*

Three configurations would be stored on the flasher:

Config 0: Configured to program Device0 (DEVICE0.CFG, DEVICE0.DAT)

Config 1: Configured to program Device1 (DEVICE1.CFG, DEVICE1.DAT)

Config 2: Configured to program Device2 (DEVICE2.CFG, DEVICE2.DAT)

Selection and programming of the target will be done via the ASCII interface:

```
#SELECT DEVICE0
#AUTO
#SELECT DEVICE1
#AUTO
#SELECT DEVICE2
#AUTO
```

2.11.1 Programming multiple targets with J-Flash

Programming multiple targets can also be done via J-Flash using the command line interface. For this each target must be handled with its own project file.

Example

```
JFlash.exe -openproj"Device0.jflash" -open"Device0.hex" -auto -exit
JFlash.exe -openproj"Device1.jflash" -open"Device1.hex" -auto -exit
JFlash.exe -openproj"Device2.jflash" -open"Device2.hex" -auto -exit
```

2.12 Batch Programming in stand-alone mode

Batch programming allows to execute different stand-alone mode jobs in batch to be executed in immediate succession, without any user interaction in between. This can be used for example to program multiple targets in a JTAG-Chain or multiple data files to a target. A batch may contain an unlimited number of configurations which consist of a data file (*.DAT) and config file (*.CFG). For further information regarding config and data files, please refer to *Preparing for stand-alone operation manually* on page 50.

In order to specify the batch jobs, a FLASHER.INI file is used. This init file contains a [BATCH] section which describes which configuration pairs (*.DAT and *.CFG file) should be used for each batch job. A sample content of a FLASHER.INI file is shown below:

```
[BATCH]
DataFile = "Flasher0.dat"
ConfigFile = "Flasher0.cfg"
DataFile1 = "Flasher1.dat"
ConfigFile1 = "Flasher1.cfg"
```

Creating / Replacing of the FLASHER.INI file can done in two ways:

1. Boot Flasher in MSD mode in order to replace the FLASHER.INI
2. If Flasher is already integrated into the production line, runs in stand-alone mode and can not be booted in other mode: Use the file I/O commands provided by the ASCII interface of Flasher, to replace the FLASHER.INI . For more information about the file I/O commands, please refer to *File I/O commands* on page 92. In case of an error occurred during execution, the Flasher terminates the entire batch processing.

Note

Please note that the batch programming feature can not be used with the multiple file support feature. Therefore, neither the #SELECT ASCII command nor the [FILES] tag in the FLASHER.INI file can be used.

Note

Flasher supports 8.3 filenames only (8 characters filename, 3 characters file extension). Using longer filenames may result in incorrect operation.

2.12.1 Flasher Portable specifics

Flasher Portable allows to choose between four different batches during runtime by using the select/arrow button on the front of Flasher Portable. Which batch configuration is used for which image selection position is specified in the FLASHER.INI. For this, the FLASHER.INI contents in the [BATCH] section have been extended. The sample below shows how to enable the user to select between four different batches on the Flasher Portable via the select / arrow button:

```
[BATCH]
DataFile = "Flasher0.dat"
ConfigFile = "Flasher0.cfg"
DataFile1 = "Flasher1.dat"
ConfigFile1 = "Flasher1.cfg"
DataFile2 = "Flasher2.dat"
ConfigFile2 = "Flasher2.cfg"
[BATCH1]
DataFile = "TEST.dat"
ConfigFile = "Test.cfg"
[BATCH2]
DataFile = "VALIDATE.dat"
ConfigFile = "Flasher0.cfg"
```

Using this method allows to have different batches for different setups used in the production to be stored once on the Flasher Portable via MSD mode. From there on, switching between the batches can be done by simply using the selection button of Flasher Portable.

2.12.2 Examples

Example 1: Programming two Data files to the same target

- Open your J-Flash project.
- Use File -> Save Flasher config file... to save the .CFG file (in this example: STM32F4.CFG).
- Select the first binary and use File -> Save Flasher Data file... to save the first .DAT file (in this example: DATA0.DAT).
- Select the second binary and use File -> Save Flasher Data file... to save the second data file .DAT file (in this example: DATA1.DAT).
- Copy the Files to the Flasher e.g. by using MSD mode.
- Create a FLASHER.INI file in the root directory of the Flasher.
- Exemplary content of FLASHER.INI:

```
[BATCH]
DataFile = "DATA0.dat"
ConfigFile = "STM32F4.cfg"
DataFile1 = "DATA1.dat"
ConfigFile1 = "STM32F4.cfg"
```

Example 2: Programming one Data file to the first target in a JTAG-Chain and then programming two data files to another device in the JTAG chain.

Example scenario: 2 Devices in a JTAG chain, a STM32F1 and a STM32F4.

- Follow the same as described before and additionally:
- Create one project file per target (and create a .CFG file of each one).
- Make sure each project file is configured correctly, especially the JTAG-Chain position (See UM8003 "J-Flash" for more detailed info).
- Exemplary content of FLASHER.INI:

```
[BATCH]
DataFile = "F1DATA.dat"
ConfigFile = "STM32F1.cfg"
DataFile1 = "F4DATA0.dat"
ConfigFile1 = "STM32F4.cfg"
DataFile2 = "F4DATA1.dat"
ConfigFile2 = "STM32F4.cfg"
```

Example 3: Using multiple Batch sections with Flasher Portable.

Example scenario: 2 Devices in a JTAG chain, a STM32F1 and a STM32F4.

Selection 1 will program the STM32F1 target.

Selection 2 will program the STM32F4 target using "F4DATA0.dat".

Selection 3 will program the STM32F4 target using "F4DATA1.dat".

Selection 4 will execute 1, 2 and 3 in sequence.

- Exemplary content of FLASHER.INI:

```
[BATCH]
DataFile = "F1DATA.dat"
ConfigFile = "STM32F1.cfg"
[BATCH1]
DataFile = "F4DATA0.dat"
ConfigFile = "STM32F4.cfg"
[BATCH2]
DataFile = "F4DATA1.dat"
ConfigFile = "STM32F4.cfg"
[BATCH3]
DataFile = "F1DATA.dat"
ConfigFile = "STM32F1.cfg"
DataFile1 = "F4DATA0.dat"
ConfigFile1 = "STM32F4.cfg"
DataFile2 = "F4DATA1.dat"
ConfigFile2 = "STM32F4.cfg"
```

2.13 Serial number programming

Flasher supports programming of serial numbers. In order to use the serial number programming feature, the J-Flash project to be used as well as some files on the Flasher (depending on the configuration) need to be configured first.

In general, Flasher supports two ways of programming a serial number into the target:

1. Programming continuous serial numbers. Serial number is 1-4 bytes in size. Start serial number, increment, serial number size and address is configured in the J-Flash project.
2. Programming custom serial numbers from a serial number list file. Start line into serial number list file to get next serial number bytes, line increment, serial number size and address is configured in J-Flash project. Serial number list file needs to be specified and created by user.

In the following some generic information how to setup Flasher & the J-Flash project for serial number programming are given.

Note

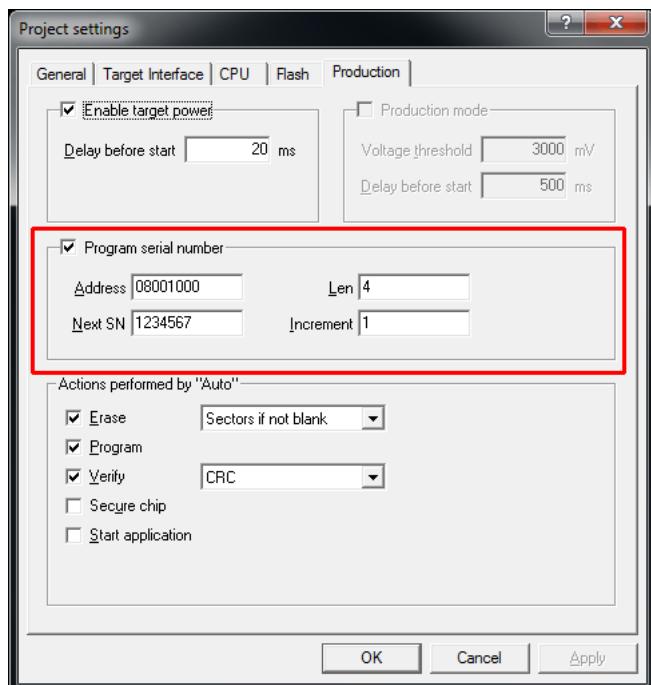
Full serial number programming support has been introduced with V4.51d of the J-Flash software and the Flasher firmware that comes with it.

Note

Currently, programming of serial numbers is only supported for stand-alone mode. Future versions of J-Flash may also support serial number programming in J-Link mode.

2.13.1 Serial number settings

In order to enable the programming of serial numbers in stand-alone mode, the J-Flash project has to be configured to enable programming a serial number at a specific address. This is done by enabling the **Program serial number** option as shown in the screenshot and table below:



Setting	Meaning
Address	The address the serial number should be programmed at.
Len	The length of the serial number (in bytes) which should be programmed. If no serial number list file is given, J-Flash allows to use a 1-4 byte serial number. In case of 8 is selected as length, the serial number and its complementary is programmed at the given address. In case a serial number list file is given, Flasher will take the serial number bytes from the list file. If a serial number in the list file does not define all bytes of Len , the remaining bytes are filled with 0s. No complements etc. are added to the serial number.
Next SN	In case no serial number list file is given, Next SN is next serial number which should be programmed. The serial number is always stored in little endian format in the flash memory. In case a serial number list file is given, Next SN describes the line of the serial number list file where to read the next serial number bytes from. Flasher starts counting with line 0, so in order to start serial number programming with the first line of the <code>SNList.txt</code> , Next SN needs to be set to 0.
Increment	Specifies how much Next SN is incremented.

2.13.2 Serial number file

When selecting **File -> Download serial number file to Flasher**, J-Flash will create a Serial number file named as `<JFlashProjectName>_Serial.txt`. This file is downloaded as `SERIAL.TXT` on Flasher. The file is generated based on the serial number settings in the J-Flash project and will contain the value defined by the [Next SN](#) option. The serial number file can also be manually edited by the user, since the serial number is written ASCII encoded in the `SERIAL.TXT` file.

2.13.3 Serial number list file

In order to program custom serial numbers which can not be covered by the standard serial number scheme provided by J-Flash (e.g. when programming non-continuous serial numbers or having gaps between the serial numbers), a so called serial number list file needs to be created by the user. When selecting **File-> Download serial number file to Flasher**, J-Flash will look for a serial number list file named as `<JFlashProjectName>_SNList.txt` in the directory where the J-Flash project is located. This file is downloaded as `SNList.txt` on Flasher. The serial number list file needs to be created manually by the user and has the following syntax:

- One serial number per line
- Each byte of the serial number is described by two hexadecimal digits.

Example

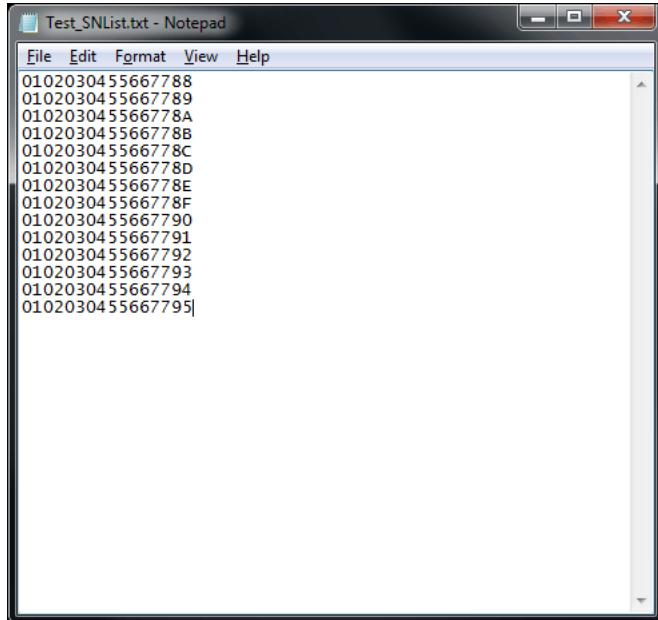
A 8-byte serial number should be programmed at address `0x08000000`.

It should be programmed as follows in the memory:

0x08000000: 0x01 0x02 0x03 0x04 0x55 0x66 0x77 0x88

The serial number list file should look as follows:

0102030455667788



The number of bytes to read per line is configured via the [Len](#) option in J-Flash. For more information, please refer to *Serial number settings* on page 65.

Which line Flasher will read at the next programming cycle, is configured via the [Next SN](#) option in J-Flash. For more information, please refer to *Serial number settings* on page 65. In this case [Next SN](#) needs to be set to 0, since programming should be started with the serial number bytes defined in the first line of the file.

Note

If the number of bytes specified in a line of the serial number list file is less than the serial number length defined in the project, the remaining bytes filled with 0s by Flasher.

Note

If the number of bytes specified in a line of the serial number list file is greater than the serial number length defined in the J-Flash project, the remaining bytes will be ignored by Flasher.

2.13.4 Programming process

Flasher will increment the serial number in SERIAL.TXT by the value defined in [Increment](#), after each successful programming cycle.

For each programming cycle, the FLASHER.LOG on the Flasher is updated and contains the value from SERIAL.TXT that has been used for the programming cycle.

Note

The serial number in SERIAL.TXT will also be incremented in case if serial number programming is disabled, to make sure that for the Flasher logfile there is a reference which programming cycle passed and which not. As long as serial number program-

ming has not been enabled in the J-Flash project, Flasher does not merge any serial number data into the image data to be programmed.

2.13.5 Downloading serial number files to Flasher

Downloading the serial number files needs to be done explicitly by selecting **File-> Download serial number file to Flasher**. Please note that the **File -> Download config & data file to Flasher** option does only download the configuration and data file to Flasher since usually the current serial number used for programming shall not be reset/overwritten when just updating the image Flasher shall program.

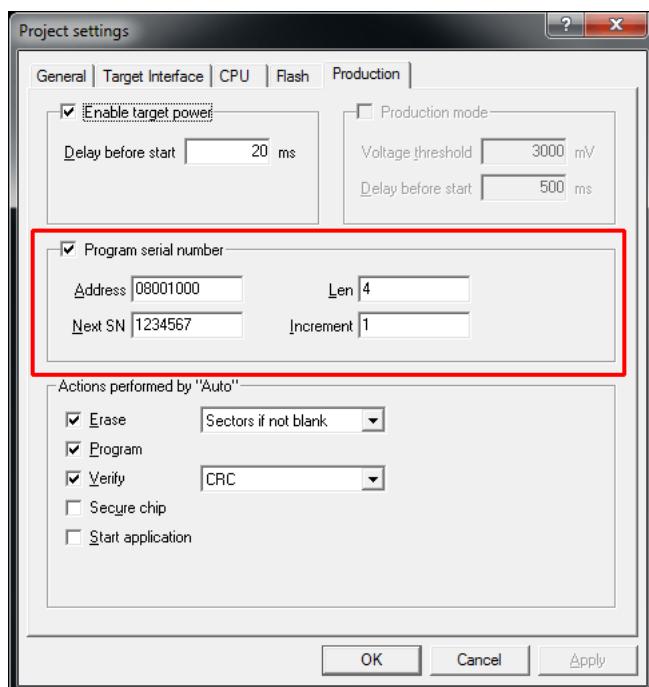
2.13.6 Sample setup

In the following a small sample is given how to setup Flasher for serial number programming. In the following sample, 4-byte serial numbers starting at 1234567 (0x12D687) shall be programmed at address 0x08001000.

Defining serial number address, length and start value

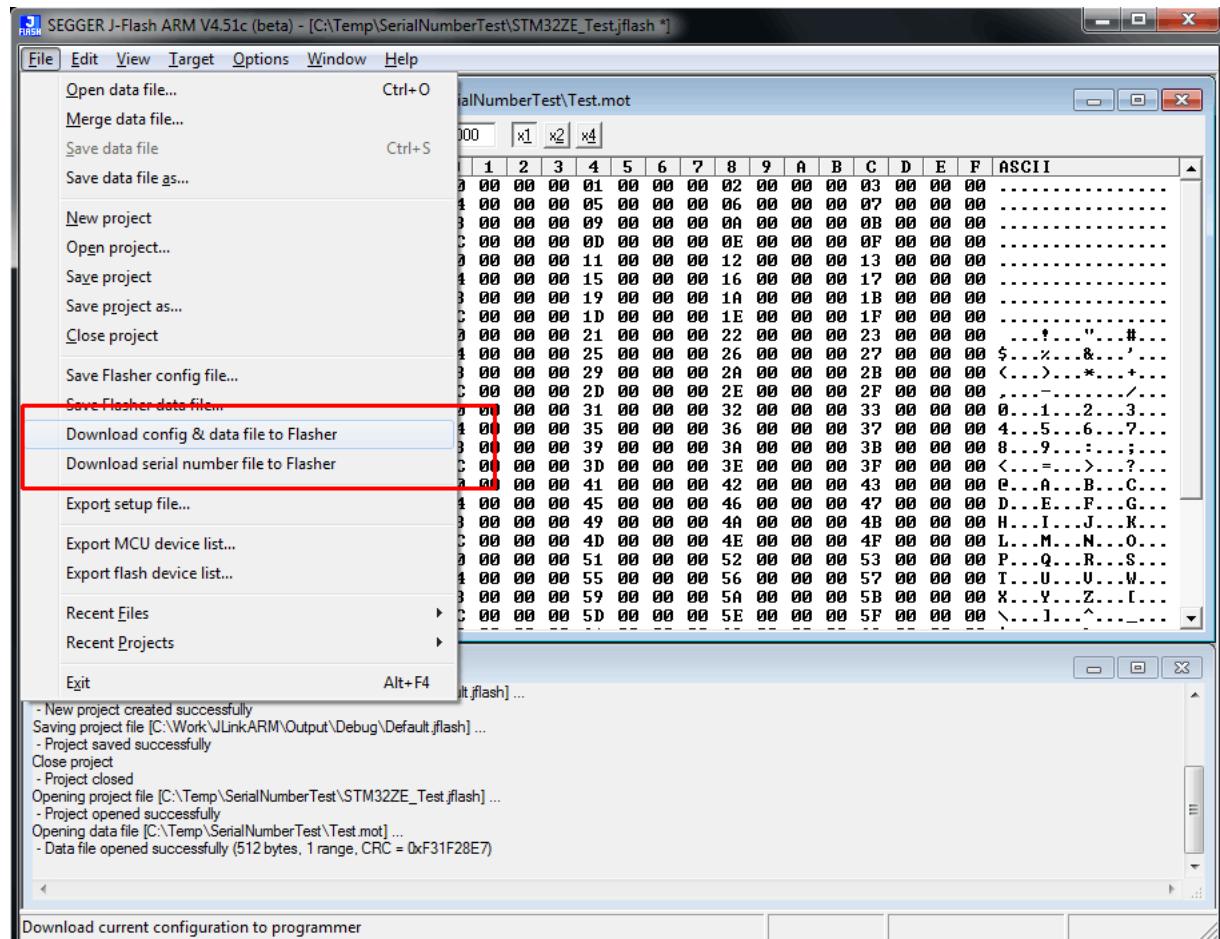
In the J-Flash project the following needs to be defined:

- **Address** is 0x08001000
- **Next SN** is 1234567
- **Increment** is 1
- **Len** is 4 (bytes)



Downloading configuration, data and serial number to Flasher.

After setting up the rest of the configuration (Target interface etc.) and selecting an appropriate data file, the configuration, data and serial number file is downloaded into Flasher via the **File -> Download config & data file to Flasher** and **File-> Download serial number file to Flasher** option.



After downloading the serial number to Flasher, J-Flash also created the `<JFlashProject-Name>_Serial.txt`.

Name	Date modified	Type	Size
STM32F_Test.iflash	25/06/2012 19:34	JFLASH File	3 KB
STM32ZE_Test_Serial.txt	26/06/2012 13:57	Text Document	1 KB
Test.mot	25/06/2012 19:32	MOT File	2 KB

Now Flasher is prepared to program the 8-byte serial number.

2.14 Patch file support

In stand-alone mode Flasher supports patch files which allows to patch the content of the data to be programmed. Before starting programming process in stand-alone mode, Flasher will look for a file named `Patches.txt` being present on the Flasher. This file includes the patches. If this file is present, the number in `Serial.txt` describes the line number of the `Patches.txt` that will be used for the current cycle (line counting starts at 0).

Each line in the `Patches.txt` can hold up to 4 patches, where each patch can be up to 32 bytes in length.

Syntax

Each line begins with `<NumPatches>` followed by each patch `<Addr>, <NumBytes>:<Data>` in sequence and separated by commas. So the syntax for `<NumPatches> == 4` would be as follows:

```
<NumPatches>, <Addr>, <NumBytes>:<Data>, <Addr>, <NumBytes>:<Data>, <Addr>,
<NumBytes>:<Data>, <Addr>, <NumBytes>:<Data>\r\n
```

Find below a table which describes each parameter.

Parameter	Description
<code><NumPatches></code>	Describes the number of patches in this patch line. Max. value is 4.
<code><Addr></code>	Describes the address to be patched. Value is expected in hex.
<code><NumBytes></code>	Number of bytes for the current patch. Max. value is 20h (32 in decimal). Value is expected in hex.
<code><Data></code>	Describes the data to be patched. <code><Data></code> is always expected as 2 hexadecimal characters per byte.

Note

All values are expected in hexadecimal format (hex).
`<Data>` section is always preceded by ":", not ";".

Example

Please find below a sample sequence which clarifies the usage of patch files.

`Patches.txt`, which is located on the Flasher, contains the following line:

3,100025,3:AABBCC,100063,2:DDEE,100078,1:FF

`Serial.txt` contains a "0" which forces the Flasher to use line 0 from `Patches.txt`.

After starting the programming cycle, the following data will be patched:

`Addr 0x100025: 3 byte 0xAA 0xBB 0xCC`

`Addr 0x100063: 2 byte 0xDD 0xEE`

`Addr 0x100078: 1 byte 0xFF`

Single patch via RS232

Alternatively, you can start a programming cycle with patch data that is only valid for this one cycle (no need for a `Patches.txt` file):

Send the `#AUTO PATCH <NumPatches>, <Addr>, <NumBytes>:<Data>`

command via Flasher ASCII interface. The parameters have the same function as described in the table above.

2.15 Limiting the number of programming cycles

Flasher provides a mechanism to limit the number of programming cycles that can be performed in stand-alone mode with the configuration that is stored on the Flasher. To make use of this feature, a file called `Cntdown.txt` needs to be placed on the Flasher. This file simply contains a decimal number (32-bit unsigned integer) that describes how many programming cycles can be performed with the current setup.

This feature especially makes sense when used in combination with authorized flashing. For more information about authorized flashing, please refer to *Authorized flashing* on page 72.

Note

The number in the `Cntdown.txt` is only updated on a successful programming cycle. Programming cycles that failed, do not affect the `Cntdown.txt`.

2.15.1 Changed fail/error LED indicator behavior

In case a `Cntdown.txt` is found at boot time, the fail/error LED of Flasher behaves different from normal. If the number of programming cycles left is 10 or below, the following will happen:

- The red error/fail LED will lit for 1 second
- After this, it will blink/toggle x times @ 5 Hz, indicating the number of programming cycles left. (blinking 5 times for 5 cycles left, ...)

2.15.2 Required Flasher hardware version for `Cntdown.txt` support

Older Flasher models do not support the limiting of programming cycles. The Flashers with the following serial number ranges do not support limiting of programming cycles:

- 1621xxxx (Flasher ARM V2)
- 1630xxxx (Flasher ARM V3)
- 4210xxxx (Flasher PPC V1)
- 4110xxxx (Flasher RX V1)

All other models / hardware versions support limiting of programming cycles.

2.16 Authorized flashing

Current hardware versions of Flasher support creation of a so called **secure area** which allows to pre-configure the Flasher with a given setup and then give it to external production facilities etc. without the possibility to read out the Flasher contents via MSD, FILE I/O functionality (J-Link Commander) or RS232 commands. This section describes how to setup a secure area on a Flasher and how to move the configuration/data file(s) into it.

2.16.1 Creating / Adding the secure area

By default, Flashers are shipped with a public area only (full Flasher flash size accessible via MSD etc.). The secure area has to be activated / created once, to make use of it. This will reserve half of the Flasher storage size (on current models this will be ~64 MB) for the secure area. The secure area can be removed at any time, providing the full flasher storage to the public area again. The secure area can be created / removed via J-Link Commander, which is part of the software package that comes with Flasher.

The following secure area related commands are available in J-Link Commander:

- `securearea create`
- `securearea remove`

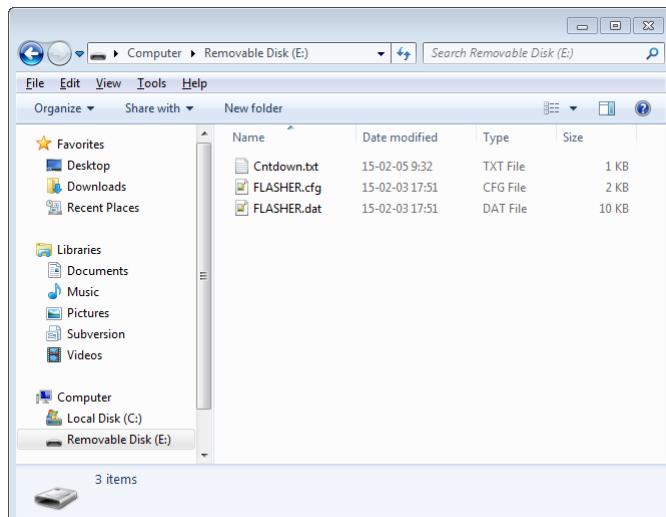
Note

When creating or removing the secure area, all configuration and data files being stored on the Flasher, are lost. Please make sure that they are not needed anymore, before adding / removing the security area.

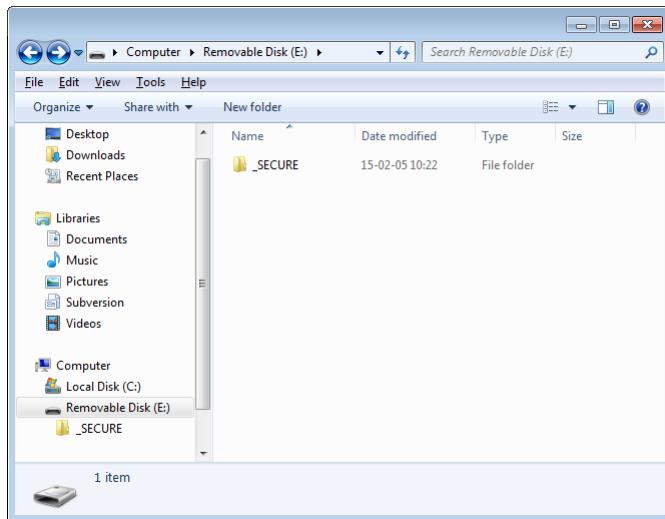
2.16.2 Moving files to the secure area

Before moving configuration + data to the secure area, proper functionality of the setup should be tested in stand-alone mode. Once the setup is working as expected, do the following, to move the configuration + data into the secure area:

- Start Flasher in MSD mode (For more info, please refer to: *MSD mode* on page 46)



- Create a folder “**_SECURE**”
- Move all files that shall be moved to the secure area, into this folder



- Reboot Flasher (Do not enter MSD mode again, yet! Otherwise, contents will not be moved). Now, depending on the configuration and data file size, it may take a bit, before the Flasher Power LED lit. Once it lit, all contents have been moved to the secure area and the `_SECURE` folder in the public area has been deleted.
- Now Flasher can be used in stand-alone mode, as normal, but the files cannot be read back by the user / operator.

2.16.3 Considerations to be taken when using the secure area

When using the secure area, some things need to be considered:

- All features like multiple file support, patch file support etc. can also be used when operating from the secure area.
- The secure area cannot be read back by any utility. Solely the `FLASHER.LOG` is always placed and updated in the public area, even when Flasher operates from the secure area.
- If there is any file/folder in the public area, except the `FLASHER.LOG` and there is also any configuration / data present in the secure area, stand-alone flashing will fail because it is not unambiguous which configuration / data shall be used. In such cases, Flashers with Ethernet / RS232 interface will output an appropriate error message on programming. All Flasher models will output an appropriate error message in the `FLASHER.LOG`.
- Moving files from the public into the secure area can be done multiple times, as explained in *Moving files to the secure area* on page 72. Each time files are moved from the public area to the secure area, all contents of the secure area are erased first, to make sure that no previous configuration is present there.

2.16.4 Required Flasher hardware version

Older Flasher models do not support authorized flashing. The Flashers with the following serial number ranges do not support authorized flashing:

- 1621xxxx (Flasher ARM V2)
- 1630xxxx (Flasher ARM V3)
- 4210xxxx (Flasher PPC V1)
- 4110xxxx (Flasher RX V1)

All other models / hardware versions support authorized flashing.

2.17 Target interfaces

The table below shows the supported target interfaces of the different Flasher models.

Hardware	Supported interfaces
Flasher ATE	JTAG, SWD
Flasher PRO	JTAG, SWD
Flasher ARM	JTAG, SWD
Flasher RX	JTAG
Flasher PPC	JTAG

For more information about the target interfaces itself, please refer to:

- *UM08001, chapter "Working with J-Link and J-Trace", section "JTAG interface"*
- *UM08001, chapter "Working with J-Link and J-Trace", section "SWD interface"*

2.18 Supported microcontrollers

2.18.1 Flasher

Flasher supports download into the internal flash of a large number of microcontrollers. The number of supported devices is steadily growing, so you can always find the latest list of supported devices on our website:

<http://www.segger.com/supported-devices.html>

2.19 Support of external flashes

2.19.1 Flasher ARM

In general Flasher ARM supports programming of external flashes listed below:

- parallel NOR flash
- serial NOR flash
- NAND flash
- DataFlash

If the parallel NOR flash device which is used is not CFI-compliant you have to select the flash device in J-Flash explicitly, for a list of all parallel NOR flash devices which can be explicitly selected in J-Flash, please refer to *UM08003, J-Flash User Guide*, chapter *Supported Flash Devices*. For serial NOR flash, NAND flash and DataFlash devices a custom RAMCode is needed since the connection of the flash to the CPU differs from device to device. The J-Flash software comes with sample projects for custom RAMCodes. For a complete list of all custom RAMCode projects which come with the J-Flash software, please refer to: <http://www.segger.com/supported-devices.html>

2.19.2 Flasher RX

Programming of external parallel NOR flash is currently not supported by Flasher RX. This limitation will be lifted in the near future.

2.19.3 Flasher PPC

Programming of external parallel NOR flash is currently not supported by Flasher PPC. This limitation will be lifted in the near future.

2.20 Supported cores

2.20.1 Flasher ARM

Flasher ARM supports and has been tested with the following cores, but should work with any ARM7/9, Cortex-M0/M1/M3/M4 core. If you experience problems with a particular core, do not hesitate to contact Segger.

- ARM7TDMI (Rev 1)
- ARM7TDMI (Rev 3)
- ARM7TDMI-S (Rev 4)
- ARM920T
- ARM922T
- ARM926EJ-S
- ARM946E-S
- ARM966E-S
- Cortex-M0
- Cortex-M1
- Cortex-M3
- Cortex-M4

2.20.2 Flasher RX

Flasher RX supports and has been tested with the following cores. If you experience problems with a particular core, do not hesitate to contact Segger.

- RX610
- RX621
- RX62N
- RX62T

2.20.3 Flasher PPC

Flasher PPC supports and has been tested with the following cores. If you experience problems with a particular core, do not hesitate to contact Segger.

- e200z0

2.21 Newline encoding

In general, for all patch files, init files etc. Flasher supports both newline encodings:

- Windows: \r\n
- Unix/Mac: \n

All parser functionality etc. are written to be independent from the host operating system.

2.22 Programming multiple targets in parallel

To program multiple targets in parallel, a Flasher ATE or multiple Flashers will be needed (each connecting to one CPU). The Flasher ATE can be used to program up to 10 targets in parallel by using a single control unit.

Using multiple Flashers, this can be done using the production programming software J-Flash. For further information, please refer to Chapter “*Command Line Interface*” Sub-chapter “*Programming multiple targets in parallel*” of the J-Flash User Guide (UM08003_JFlash.pdf) which is part of the J-Link Software and Documentation package.

Chapter 3

TCP Services

This chapter describes the integrated TCP services.

3.1 FTP Server

The FTP server provides easy access to the files on the internal file system. The server supports a maximum of 2 simultaneous connections and works with all common FTP clients.

For the Flasher ATE, the FTP server is the only way to access files on the modules. Here the root directory is a virtual directory and cannot be written to. It contains a subdirectory for each module.

3.1.1 Access data

Anonymous access to the FTP server is limited to read-only access to the file system. For write access, special login credentials have to be used:

Login: admin
Password: 1234

Note

The access data for read/write access can not be modified and it is intended to be used only as a convenience feature to avoid unintended modification of the Flasher's file system. It is not meant as a security feature.

3.2 Web server

All Flashers which come with an Ethernet interface also come with a built-in web server, which provides a web interface for information and network configuration. For the network, the IP address settings can be changed and a nick name can be assigned to the device.

Additionally, the web interface provides information about the status of the integrated operating system, the IP stack and the target hardware. The Flasher ATE's web interface furthermore allows monitoring of the individual modules.

Chapter 4

Remote control

This chapter describes how to control Flasher via the 9-pin serial interface connector or via the integrated Telnet interface.

4.1 Overview

There are 3 ways to control Flasher operation:

- Manual: Programming operation starts when pressing the button. The LEDs serve as visible indicators.
- Via Handshake lines: 3 lines on the serial interface are used:
1 line is an input and can be used to start operation,
2 lines are outputs and serve as busy and status signals.
- Terminal communication via RS232.
- Terminal communication via Telnet.

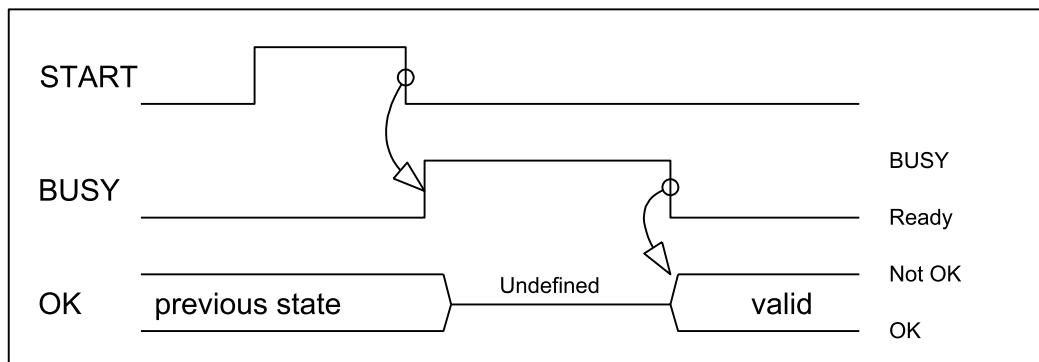
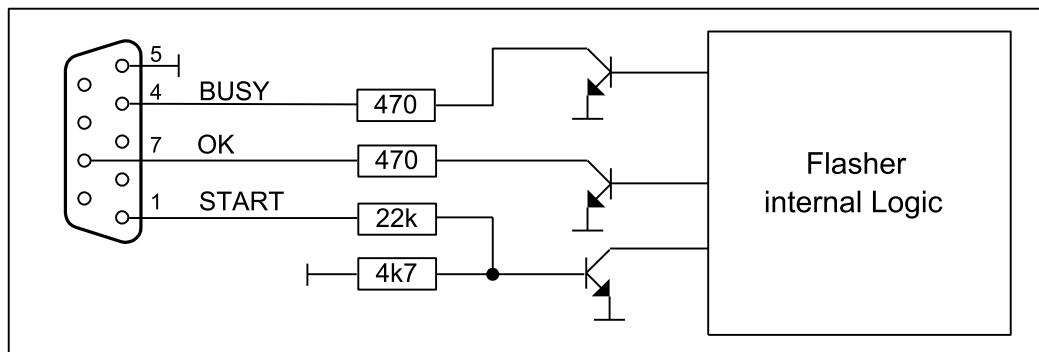
Note

All ways to control Flasher operation are working only if Flasher is in standalone mode.
In J-Link / MSD mode they have no effect.

4.2 Handshake control

The Flasher can be remote controlled by automated testers without the need of a connection to a PC. Therefore the Flasher is equipped with additional hardware control functions, which are connected to the SUBD9 male connector, normally used as RS232 interface to PC.

The following diagrams show the internal remote control circuitry of Flasher:



Pin No.	Function	Description
1	START	A positive pulse of any voltage between 5 and 30V with duration of min. 30 ms starts "Auto" function (Clear / Program / Verify) on falling edge of pulse. The behavior of the "Auto" function depends on the project settings, chosen in J-Flash at the Production tab.
4	BUSY	As soon as the "Auto" function is started, BUSY becomes active, which means that transistor is switched OFF.
5	GND	Common Signal ground.
7	OK	This output reflects result of last action. It is valid after BUSY turned back to passive state. The output transistor is switched ON to reflect OK state.

Note

As the Flasher ATE is a modular system, using the handshake remote control START always triggers the "Auto" function of every connected module. The BUSY line is signaled as long as any module is still busy and the OK line only reports "OK" in case of every module has successfully completed the operation. We recommend using the

ASCII command interface, described in the next chapter, for the Flasher ATE as it gives better remote control capabilities.

4.3 ASCII command interface

4.3.1 Introduction

Once set up using J-Flash, the Flasher can be driven by any application or just a simple terminal using ASCII commands.

Every known command is acknowledged by the Flasher and then executed. After command execution, Flasher sends an ASCII reply message.

Note

There are situations where the execution of a known command is rejected with #NACK:ERRxxx if Flasher is currently busy and the received command is not allowed to be sent while Flasher is busy

4.3.2 General command and reply message format

- Any ASCII command has to start with the start delimiter #.
- Any ASCII command has to end with simple carriage return ('\r', ASCII code 13).
- Commands can be sent upper or lower case

4.3.3 General usage

Reply messages must be considered in each case. In general, a new command must not be sent before a reply for the last one has been received. For the Flasher ATE, all commands triggering a flash programming function (#AUTO, #CANCEL, #ERASE, #PROGRAM, #VERIFY) may be used for other modules, before a reply for the current operation has been received.

4.3.4 Settings for ASCII interface via RS232

Flasher is driven via a RS232 serial port with the following interface settings:

- 9600 baud
- 8 data bits
- no parity
- 1 stop bit

The baud rate can be changed by using the #BAUDRATE command.

4.3.5 Settings for ASCII interface via Telnet

A client application can connect to Flasher via Telnet on port 23. Find below a screenshot of Flasher which is remote controlled via Telnet:

```

telnet 192.168.11.41
J-Link ARM / Flasher ARM U3 telnet-shell.
J-Link ARM / Flasher ARM U3 compiled Jan 29 2013 17:58:39
>#auto
#ACK
#STATUS:INITIALIZING
#STATUS:CONNECTING
#STATUS:UNLOCKING
#STATUS:ERASING
#STATUS:PROGRAMMING
#STATUS:VERIFYING
#OK <Total 7.797s, Erase 0.004s, Prog 3.949s, Verify 0.087s>

```

4.3.6 Commands and replies

The table below gives an overview about the commands which are supported by the current version of Flasher firmware. Click on the names for a detailed description:

Commands to the Flasher	ARM/PRO	ATE
#BAUDRATE<Baudrate>	Yes	Yes
#AUTO	Yes	Yes ¹
#AUTO PATCH	Yes	Yes ¹
#AUTO NOINFO	Yes	No
#CANCEL	Yes	Yes ¹
#ERASE	Yes	Yes ¹
#PROGRAM	Yes	Yes ¹
#RESULT	Yes	Yes ¹
#SELECT <Filename>	Yes	Yes ¹
#START	Yes	Yes ¹
#STATUS	Yes	Yes
#VERIFY	Yes	Yes ¹
#SELMODULE <Module1>[,<Module2>]	No	Yes
#TERMINAL <Baudrate>,<Databits>,<Parity>,<Stopbits>	No	Yes ¹
	YES	Yes
File I/O commands		
#FCLOSE	Yes	No
#FCRC	Yes	No
#FDELETE <Filename>	Yes	No
#FOPEN <Filename>	Yes	No
#FREAD <Offset>,<NumBytes>	Yes	No
#FSIZE	Yes	No
#FWRITE <Offset>,<NumBytes>:<Data>	Yes	No
#FLIST	Yes	No
#MKDIR <Dirname>	Yes	No
Replies from the Flasher		
#ACK	Yes	Yes
#NACK	Yes	Yes
#OK	Yes	Yes
#OK:<NumBytes>:<Data>	Yes	Yes
#OK:<Size>	Yes	Yes
#STATUS:	Yes	No
#RESULT:<Module>:	No	Yes
#DONE	No	Yes
#ERRxxx	Yes	Yes

¹ For these commands, the module number has to be given as the first parameter when using a Flasher ATE.

4.3.6.1 Commands to the Flasher

#AUTO

The #AUTO command behaves exactly as the start button or external remote control input. Usually, the following command sequence will be performed when receiving the #AUTO command:

- The Flasher erases the target CPU (if not blank)
- The Flasher programs the target CPU
- The Flasher verifies the target CPU

Depending on the settings chosen in the **Production** tab in J-Flash, this sequence can differ from the one shown above.

Finally, Flasher responds with

- #OK if no error occurred
- #ERRxxxx if any error occurred during operation. xxxx represents the error code, normally replied to Flasher PC program. The #ERRxxxx message may be followed by an additional error text.

During execution of the #AUTO command, Flasher automatically sends “status” messages via RS232 to reflect the state of execution. Typically during execution of #AUTO command, Flasher will reply the following sequence of messages:

```
#ACK
#STATUS: INITIALIZING
#STATUS: CONNECTING
#STATUS: UNLOCKING
#STATUS: ERASING
#STATUS: PROGRAMMING
#STATUS: VERIFYING
#OK (Total 13.993s, Erase 0.483s, Prog 9.183s, Verify 2.514s)
```

#AUTO PATCH

The #AUTO PATCH command allows patching of the content of the data to be programmed.

Flasher responds with

- #OK if no error occurred
- #ERRxxxx if any error occurred during operation. xxxx represents the error code, normally replied to Flasher PC program. The #ERRxxxx message may be followed by an additional error text.

For further information about the usage of the #AUTO PATCH command please refer to *Patch file support* on page 70.

#AUTO NOINFO

This command may be used instead of #AUTO, if no status messages from Flasher should be sent during execution. The NOINFO extension is also available for all other commands.

The command ends with #OK or #ERRxxxx

#BAUDRATE<Baudrate>

This command can be sent in order to change the baud rate of the Flasher’s RS232 interface used for communication. <Baudrate> is expected in decimal format.

If this command succeeds, Flasher responds with:

```
#ACK
#OK
```

Otherwise it will respond with one of the following error messages:

```
#ERR255: Invalid parameters
```

or
#ERR255: Baudrate is not supported

Note

After sending the #BAUDRATE command you will first have to wait until the Flasher responds with the #OK message. It is recommended wait 5ms before sending the next command with the new baudrate in order to give the Flasher the time to change the baudrate.

#CANCEL

This command can be sent to abort a running program. It may take a while until the current program is actually canceled.

Flasher will respond with:

#ERR007:CANCELED.

#ERASE

This command can be sent to erase all selected target flash sectors.

Flasher will reply the following sequence of messages:

```
#ACK  
#STATUS:INITIALIZING  
#STATUS:CONNECTING  
#STATUS:UNLOCKING  
#STATUS:ERASING  
#OK (Total 0.893s, Erase 0.483s)
```

#PROGRAM

This command can be used instead of #AUTO to program a target without erasing the target before programming and without performing a final verification.

#RESULT

This command can be sent any time, even during other command execution. Flasher responds with the last result of the previously executed command.

#SELECT <Filename>

The #SELECT command is used to select a specific config and data file pair which should be used by Flasher to program the target. <Filename> specifies the name of file pair without extensions (.CFG and .DAT) on the Flasher which should be selected. Flasher saves the selected config and data file in the FLASHER.INI file. So this selection is remembered even between power-cycling Flasher.

This may be very helpful in cases where several config and data files are stored on Flasher. The user can easily switch between these config and data files without connecting Flasher to a host.

If this command succeeds, Flasher responds with:

```
#ACK  
#OK
```

Find below a sample sequence which shows how to use the #SELECT command:

```
#SELECT ATSAM7_1 // ATSAM7_1.CFG and ATSAM7_1.DAT is selected  
#ACK  
#OK  
#AUTO // Start auto programming  
#ACK  
#STATUS:INITIALIZING
```

```

#STATUS:CONNECTING
#STATUS:UNLOCKING
#STATUS:ERASING
#STATUS:PROGRAMMING
#STATUS:VERIFYING
#OK (Total 8.416s, Erase 0.005s, Prog 6.845s, Verify 0.959s)
#SELECT ATSAM7_2 // ATSAM7_2.CFG and ATSAM7_2.DAT is selected
#ACK
#OK
#AUTO // Start auto programming
#ACK
#STATUS:INITIALIZING
#STATUS:CONNECTING
#STATUS:UNLOCKING
#STATUS:ERASING
#STATUS:PROGRAMMING
#STATUS:VERIFYING
#OK (Total 8.632s, Erase 0.005s, Prog 7.051s, Verify 0.969s)

```

#START

This command can be sent to release Flasher's target interface. All signals from Flasher to target will be set into high-Z mode, reset of target will be released. It may be used to start target application program.

Flasher will reply with the following sequence of messages:

```

#ACK
#STATUS:INITIALIZING
#STATUS:CONNECTING
#OK (Total 1.148s)

```

#STATUS

This command can be sent any time, even during other command execution. Flasher responds with its current state. All defined state messages are described under *Replies from Flasher* on page 95.

#VERIFY

This command can be used to verify the target flash content against the data stored in Flasher.

#SELMODULE <Module1>[,<Module2>]

This command is used to select one or more modules on a Flasher ATE system. The module numbers are separated by a comma. If all modules shall be selected, the keyword "all" can be used (#SELMODULE ALL) instead of a list with all module numbers.

Note

This command only applies to the Flasher ATE.

#TERMINAL <Baudrate>,<Databits>,<Parity>,<Stopbits>

This command enables the UART transceiver. The baud rate can be up to 115,200 bits per second. The databits parameter currently is ignored and defaults to 8 bit. Parity can be

- N (no parity)
- E (even parity)
- O (odd parity)

The stopbits parameter is currently also ignored and defaults to 1 stop bit. We recommend to set the ignored parameters to their default values, as these parameters may be used in future versions of the ASCII interface.

Note

This command only applies to the Flasher ATE.

4.3.6.2 File I/O commands

The ASCII interface of the Flasher, except the Flasher ATE, also supports file I/O operations. For the Flasher ATE, the integrated FTP server has to be used in order to perform file I/O operations on the modules.

The following file I/O commands are supported:

#FCLOSE

The #FCLOSE command closes the file on Flasher which was opened via #FOPEN. After this command has been issued further file I/O operations except #FDELETE are not allowed until the #FOPEN command is send again.

A typical sequence when using the #FCLOSE command does look like as follows:

```
#FCLOSE  
#ACK  
#OK
```

Note

When using the #FCLOSE command a file has to be open (previously opened by #FOPEN). Otherwise Flasher will respond with the following if no file has been opened:

```
#ACK  
#ERR255:No file opened
```

#FCRC

The #FCRC command calculates a 32-bit CRC of the given file. This CRC can be used to verify file integrity. This command should not be used while a file has been opened via #FOPEN. The CRC will be also reported by J-Flash when downloading or saving files via J-Flash.

A typical sequence when using the #FCRC command does look like as follows:

```
#FCRC flasher.dat  
#ACK  
#OK: 0x75BC855A
```

#FDELETE <Filename>

The #FDELETE command is used to delete a file on Flasher where <Filename> specifies the name of the file.

A typical sequence when using the #FDELETE command does look like as follows:

```
#FDELETE flasher.dat  
#ACK  
#OK
```

Note

If deletion of the file fails for example if the file does not exist, Flasher will respond with the following sequence:

```
#ACK  
#ERR255:Failed to delete file
```

#FOPEN <Filename>

The #FOPEN command is used to open a file on Flasher for further file I/O operations. <Filename> specifies the file on the Flasher which should be opened. If <Filename> can not be found on Flasher a new one will be created.

A typical sequence using the #FOPEN command does look like as follows:

```
#FOPEN flasher.dat  
#ACK  
#OK
```

Note

Currently only one file can be open at the same time. If #FOPEN is send and another file is already open, Flasher will respond with:

```
#ACK  
#ERR255:A file has already been opened
```

#FREAD <Offset>,<NumBytes>

The #FREAD command is used to read data from a file on Flasher. <Offset> specifies the offset in the file, at which data reading is started. <NumBytes> specifies the number of bytes which should be read.

A typical sequence when using the #FREAD command does look like as follows:

```
#FREAD 0,4  
#ACK  
#OK:04:466c6173
```

If the #FREAD command succeeds, Flasher will finally respond with a #OK:<NumBytes>:<Data> reply message. For more information about the Flasher reply messages, please refer to *Replies from Flasher* on page 95.

Note

In order to use the #FREAD command. A file has to be opened before, via the #FOPEN command. Otherwise Flasher will respond with the following sequence:

```
#ACK  
#ERR255>No file opened
```

#FSIZE

The #FSIZE command is used to get the size of the currently opened file on Flasher.

A typical sequence when using the #FSIZE command does look like as follows:

```
#FSIZE  
#ACK  
#OK:10 // file on flasher which is currently open, has a size of 16 bytes
```

If the #FSIZE command succeeds, Flasher will respond with a #OK:<Size> reply message. For more information about the Flasher reply messages, please refer to *Replies from Flasher* on page 95.

Note

In order to use the #FREAD command. A file has to be opened before, via the #FOPEN command. Otherwise Flasher will respond with the following sequence:

```
#ACK
```

```
#ERR255:No file opened
```

#FWRITE <Offset>,<NumBytes>:<Data>

The #FWRITE command is used to write to a file on Flasher. <Offset> specifies the offset in the file, at which data writing is started. <NumBytes> specifies the number of bytes which are send with this command and which are written into the file on Flasher. <NumBytes> is limited to 512 bytes at once. This means, if you want to write e.g. 1024 bytes, you have to send the #FWRITE command twice, using an appropriate offset when sending it the second time.

<Offset> and <NumBytes> are expected in hexadecimal format.

```
#FWRITE 0,200:<Data>
#FWRITE 200,200:<Data>
```

The data is expected in hexadecimal format (two hexadecimal characters per byte). The following example illustrates the use of #FWRITE:

```
Data to be send: Hello !
ASCII values: 0x48, 0x65, 0x6C, 0x6C, 0x6F, 0x20, 0x21
#FWRITE 0,7:48656C6C6F2021
```

Note

In order to use the #FWRITE command a file has to be opened via the #FOPEN command, first. Otherwise Flasher will respond with the following sequence:

```
#ACK
#ERR255:No file opened
```

#FLIST

The #LIST command is used to list all files stored on the Flasher.

A typical sequence using the #FLIST command does look like as follows:

```
#FLIST
#ACK
FLASHER.INI Size: 60
SERIAL.TXT Size: 3
FLASHER.LOG Size: 207
FOLDER (DIR)
FOLDER\TEST1.CFG Size: 2048
FOLDER\TEST1.DAT Size: 12288
#OK
```

#MKDIR <Dirname>

The #MKDIR command is used to create a directory on Flasher. <Dirname> specifies the name of the new directory. <Dirname> may also specify a path to create a subdirectory.

A typical sequence using the #MKDIR command does look like as follows:

```
#MKDIR folder
#ACK
#OK
```

Note

If the directory can not be created because of a bad <Dirname> argument, Flasher will respond with:

```
#ACK
```

```
#ERR255:Failed to create directory
```

4.3.6.3 Replies from Flasher

The reply messages from Flasher follow the same data format as commands. Any reply message starts with ASCII start delimiter #, ends with simple carriage return (ASCII code 13) and is sent in uppercase. In contrast to commands, replies can be followed by a descriptive message, which gives more detailed information about the reply. This description is sent in mixed case. The #OK reply, for example, is such a reply. It is followed by a string containing information about the performance time needed for the operations:

```
#OK (Total 13.993s, Erase 0.483s, Prog 9.183s, Verify 2.514s)
```

The following reply messages from Flasher are defined:

#ACK

Flasher replies with #ACK message on reception of any defined command before the command itself is executed.

#NACK

Flasher replies with #NACK, if an undefined command was received.

#OK

Flasher replies with #OK, if a command other than #STATUS or #RESULT was executed and ended with no error.

#OK:<NumBytes>:<Data>

Flasher replies with #OK:<Len>:<Data> if a #FREAD command was executed. <NumBytes> is the number of bytes which could be read. This value may differ from the number of requested bytes, for example if more bytes than available, were requested. <NumBytes> and <Data> are send in hexadecimal format (for <Data>: two hexadecimal characters per byte).

#OK:<Size>

Flasher replies if #OK:<Size> if a #FSIZE command has been executed. <Size> is the size (in bytes) of the currently opened file. <Size> is send in hexadecimal format.

#STATUS:

The Flasher replies with its current state.

The following status messages are currently defined:

Message	Description
#STATUS:READY	Flasher is ready to receive a new command.
#STATUS:CONNECTING	Flasher initializes connection to target CPU.
#STATUS:INITIALIZING	Flasher performs self check and internal init.
#STATUS:UNLOCKING	Unlocking flash sectors.
#STATUS:ERASING	Flasher is erasing the flash of the target device.
#STATUS:PROGRAMMING	Flasher is programming the flash of the target device.
#STATUS:VERIFYING	Flasher verifies the programmed flash contents.

#RESULT:<Module>:

The Flasher ATE reports the result of an operation on a specific module. If the operation has been completed successfully, it will report the outcome with a single message of this type followed by the last result of the operation.

Note

This command only applies to the Flasher ATE.

#DONE

For the Flasher ATE, getting a result from a module does not necessarily mean, the Flasher ATE is in idle state. Therefore, this message is being sent, once all operations are finished and all modules are back in idle state.

A typical sequence for using the Flasher ATE is as follows:

```
J-Link / Flasher ATE Mainboard V1 telnet-shell.
J-Link / Flasher ATE Mainboard V1 compiled Nov 22 2017 12:28:43
#SELMODULE 1,2
#ACK
#SELECTED:1,2
#AUTO *
#ACK
#RESULT:1:#ERR255:Error while flashing
#RESULT:2:#OK (Total 2.653s, Erase 0.327s, Prog 1.960s, Verify 0.234s)
#DONE
```

Note

This command only applies to the Flasher ATE.

#ERRxxx

If any command other than #STATUS or #RESULT was terminated with an error, Flasher cancels the command and replies with an error message instead of #OK message.

Some error codes may be followed by colon and an additional error text.

For example:

```
#ERR007:CANCELED.
```

The error code numbers are described in the following table:

Message	Description
#ERR007	Flasher received #CANCEL command and has canceled the current operation.
#ERR008	Flasher is already busy with execution of previous command.
#ERR009	Failed to allocate memory.
#ERR010	Failed to open file.
#ERR011	Failed to read file.
#ERR012	Failed to write file.
#ERR013	Failed to delete file.
#ERR255	Undefined error occurred. This reply is followed by an error string.

Chapter 5

Performance

The following chapter lists programming performance of common flash devices and microcontrollers.

5.1 Performance of MCUs with internal flash memory

5.1.1 Flasher ARM

The following table lists program and erase performance values of Flasher ARM for different controllers.

Microcontroller	Size [kByte]	Erase time [sec]	Program time [sec]	Verify time [sec]	Total time [sec]
Analog Devices	62	2.943	2.286	0.563	5.792
Atmel AT91SAM7S64	64	—	3.488	0.438	3.926
Atmel AT91SAM7S256	256	—	7.709	1.053	8.762
NXP LPC1768	512	3.740	8.559	5.092	17.391
NXP LPC2106	120	0.448	1.204	0.634	2.286
NXP LPC2129	248	0.449	2.916	1.347	4.712
NXP LPC2138	500	0.448	5.488	2.649	8.585
NXP LPC2148	500	0.448	5.632	2.721	8.801
NXP LPC2294	2048	0.808	15.976	9.669	26.453
NXP LPC2478	504	0.448	5.419	2.559	8.426
ST STM32F103ZE	512	0.028	18.763	3.939	22.730
ST STR711	272	0.429	5.476	4.742	10.647
ST STR912	544	1.167	12.907	5.236	19.310
TI TMS470R1B1M	1024	2.289	8.147	5.362	15.798

5.1.2 Flasher RX

The following table lists program and erase performance values of Flasher RX.

Microcontroller	Size [kByte]	Erase time [sec]	Program time [sec]	Verify time [sec]	Total time [sec]
R5F56108	2.048	9.523	11.915	3.890	25.585

5.1.3 Flasher PPC

The following table lists program and erase performance values of Flasher PPC.

Microcontroller	Size [kByte]	Erase time [sec]	Program time [sec]	Verify time [sec]	Total time [sec]
ST SPC560B50	576	4.747	4.159	1.929	10.917

Chapter 6

Hardware

This chapter gives an overview about Flasher specific hardware details, such as the pinouts and available adapters.

6.1 Flasher ARM 20-pin JTAG/SWD Connector

Flasher has a JTAG connector compatible with ARM's Multi-ICE. The JTAG connector is a 20 way Insulation Displacement Connector (IDC) keyed box header (2.54mm male) that mates with IDC sockets mounted on a ribbon cable.

6.1.1 Pinout JTAG

VTref	1 •	• 2	Vsupply
nTRST	3 •	• 4	GND
TDI	5 •	• 6	GND
TMS	7 •	• 8	GND
TCK	9 •	• 10	GND
RTCK	11 •	• 12	GND
TDO	13 •	• 14	GND
RESET	15 •	• 16	GND
DBGREQ	17 •	• 18	GND
V5-Supply	19 •	• 20	GND

The following table lists the Flasher JTAG pinout.

PIN	SIGNAL	TYPE	Description
1	VTref	Input	This is the target reference voltage. It is used to check if the target has power, to create the logic-level reference for the input comparators and to control the output logic levels to the target. It is normally fed from Vdd of the target board and must not have a series resistor.
2	Vsupply	NC	This pin is not connected to Flasher ARM. It is reserved for compatibility with other equipment. Connect to Vdd or leave open in target system.
3	nTRST	Output	JTAG Reset. Output from Flasher ARM to the Reset signal of the target JTAG port. Typically connected to nTRST of the target CPU. This pin is normally pulled HIGH on the target to avoid unintentional resets when there is no connection.
5	TDI	Output	JTAG data input of target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TDI of target CPU.
7	TMS	Output	JTAG mode set input of target CPU. This pin should be pulled up on the target. Typically connected to TMS of target CPU.
9	TCK	Output	JTAG clock signal to target CPU. It is recommended that this pin is pulled to a defined state of the target board. Typically connected to TCK of target CPU.
11	RTCK	Input	Return test clock signal from the target. Some targets must synchronize the JTAG inputs to internal clocks. To assist in meeting this requirement, you can use a returned, and re-timed, TCK to dynamically control the TCK rate. Flasher ARM supports adaptive clocking, which waits for TCK changes to be echoed correctly before making further changes. Connect to RTCK if available, otherwise to GND.
13	TDO	Input	JTAG data output from target CPU. Typically connected to TDO of target CPU.
15	RESET	I/O	Target CPU reset signal. Typically connected to the RESET pin of the target CPU, which is typically called "nRST", "nRESET" or "RESET".

PIN	SIGNAL	TYPE	Description
17	DBGREQ	NC	This pin is not connected in Flasher ARM. It is reserved for compatibility with other equipment to be used as a debug request signal to the target system. Typically connected to DBGRQ if available, otherwise left open.
19	5V-Target supply	Output	This pin is used to supply power to some eval boards. Typically left open on target hardware.

Pins 4, 6, 8, 10, 12, 14, 16, 18, 20 are GND pins connected to GND in Flasher ARM. They should also be connected to GND in the target system.

6.1.2 Pinout SWD

The 20-pin connector of Flasher is also compatible to ARM's Serial Wire Debug (SWD) interface.

VTref	1	●	● 2	Vsupply
Not used	3	●	● 4	GND
Not used	5	●	● 6	GND
SWDIO	7	●	● 8	GND
SWCLK	9	●	● 10	GND
Not used	11	●	● 12	GND
SWO	13	●	● 14	GND
RESET	15	●	● 16	GND
Not used	17	●	● 18	GND
V5-Supply	19	●	● 20	GND

The following table lists the J-Link / J-Trace SWD pinout.

PIN	SIGNAL	TYPE	Description
1	VTref	Input	This is the target reference voltage. It is used to check if the target has power, to create the logic-level reference for the input comparators and to control the output logic levels to the target. It is normally fed from Vdd of the target board and must not have a series resistor.
2	Vsupply	NC	This pin is not connected in J-Link. It is reserved for compatibility with other equipment. Connect to Vdd or leave open in target system.
3	Not Used	NC	This pin is not used by J-Link. If the device may also be accessed via JTAG, this pin may be connected to nTRST, otherwise leave open.
5	Not used	NC	This pin is not used by J-Link. If the device may also be accessed via JTAG, this pin may be connected to TDI, otherwise leave open.
7	SWDIO	I/O	Single bi-directional data pin.
9	SWCLK	Output	Clock signal to target CPU. It is recommended that this pin is pulled to a defined state of the target board. Typically connected to TCK of target CPU.
11	Not used	NC	This pin is not used by J-Link. This pin is not used by J-Link when operating in SWD mode. If the device may also be accessed via JTAG, this pin may be connected to RTCK, otherwise leave open.
13	SWO	Output	Serial Wire Output trace port. (Optional, not required for SWD communication.)

PIN	SIGNAL	TYPE	Description
15	RESET	I/O	Target CPU reset signal. Typically connected to the RESET pin of the target CPU, which is typically called "nRST", "nRESET" or "RESET".
17	Not used	NC	This pin is not connected in J-Link.
19	5V-Target supply	Output	This pin is used to supply power to some eval boards. Not all J-Links supply power on this pin, only the KS (Kickstart) versions. Typically left open on target hardware.

Pins 4, 6, 8, 10, 12, 14, 16, 18, 20 are GND pins connected to GND in J-Link. They should also be connected to GND in the target system.

6.1.3 Target power supply

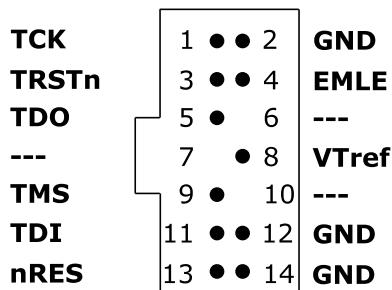
Pin 19 of the connector can be used to supply power to the target hardware. Supply voltage is 5V, max. current is 400mA. The output current is monitored and protected against overload and short-circuit.

Power can be controlled via the J-Link commander. The following commands are available to control power:

Command	Explanation
<code>power on</code>	Switch target power on
<code>power off</code>	Switch target power off
<code>power on perm</code>	Set target power supply default to "on"
<code>power off perm</code>	Set target power supply default to "off"

6.2 Flasher RX 14-pin connector

Flasher RX itself has a 20-pin JTAG connector mounted but comes with a 14-pin adapter for Renesas RX devices. This adapter also enables Flasher RX to optionally power the connected target hardware. On the adapter there is a jumper which allows selection between 3.3V and 5V supply target voltage supply. The target is supplied via the VTref connection when the supply option is jumpered.



The following table lists the Flasher RX 14-pin JTAG pinout.

Pin	Signal	Type	Description
1	TCK	Output	JTAG clock signal to target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TCK on target CPU.
3	TRSTn	Output	JTAG Reset. Output from Flasher ARM to the Reset signal of the target JTAG port. Typically connected to nTRST of the target CPU. This pin is normally pulled HIGH on the target to avoid unintentional resets when there is no connection.
4	EMLE	Output	Pin for the on-chip emulator enable signal. When the on-chip emulator is used, this pin should be driven high. When not used, it should be driven low. Pulled HIGH to VTref via 1k pull-up resistor on 14-pin adapter.
5	TDO	Input	JTAG data output from target CPU. Typically connected to TDO on target CPU.
6	—	NC	This pin is not connected to Flasher RX.
7	—	NC	This pin is not connected to Flasher RX.
8	VTref	Input	This is the target reference voltage. It is used to check if the target has power, to create the logic-level reference for the input comparators and to control the output logic levels to the target. It is normally fed from Vdd of the target board and must not have a series resistor.
9	TMS	Output	JTAG mode set input of target CPU. This pin should be pulled up on the target. Typically connected to TMS on target CPU.
10	—	NC	This pin is not connected to Flasher RX.
11	TDI	Output	JTAG data input of target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TDI on target CPU.
13	nRES	I/O	Target CPU reset signal. Typically connected to the RESET pin of the target CPU, which is typically called "nRST", "nRESET" or "RESET".

- All pins marked NC are not connected to Flasher RX. Any signal can be applied here; Flasher RX will simply ignore such a signal.
- Pins 2, 12, 14 are GND pins connected to GND in Flasher RX. They should also be connected to GND in the target system.

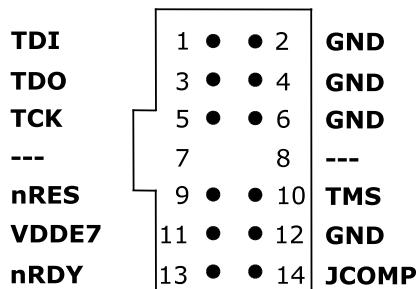
6.2.1 Target power supply

Pin 8 of the 14-pin connector can be used to supply power to the target hardware. Supply voltage is 3.3V / 5V, max. current is 400mA. The output current is monitored and protected against overload and short-circuit. Power can be controlled via the J-Link commander. The following commands are available to control power:

Command	Explanation
<code>power on</code>	Switch target power on
<code>power off</code>	Switch target power off
<code>power on perm</code>	Set target power supply default to "on"
<code>power off perm</code>	Set target power supply default to "off"

6.3 Flasher PPC 14-pin connector

Flasher PPC itself has a 20-pin JTAG connector mounted but comes with a 14-pin adapter for PowerPC devices.



The following table lists the Flasher PPC 14-pin JTAG pinout.

Pin	Signal	Type	Description
1	TDI	Output	JTAG data input of target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TDI on target CPU.
3	TDO	Input	JTAG data output from target CPU. Typically connected to TDO on target CPU.
5	TCK	Output	JTAG clock signal to target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TCK on target CPU.
7	—	NC	This pin is not connected to Flasher PPC.
8	—	NC	This pin is not connected to Flasher PPC.
9	nRES	I/O	Target CPU reset signal. Typically connected to the RESET pin of the target CPU, which is typically called "nRST", "nRESET" or "RESET".
10	TMS	Output	JTAG mode set input of target CPU. This pin should be pulled up on the target. Typically connected to TMS on target CPU.
11	VDDE7	Input	This is the target reference voltage. It is used to check if the target has power, to create the logic-level reference for the input comparators and to control the output logic levels to the target. It is normally fed from Vdd of the target board and must not have a series resistor.
13	nRDY	Input	Nexus ready output. Indicates to the development tools that the data is ready to be read from or written to the Nexus read/write access registers.
14	JCOMP	Output	JTAG TAP Controller Enable / JTAG Compliancy (JCOMP). JCOMP is used to enable the TAP controller for communication to the JTAG state machine for boundary scan and for debug access. This pin is set to HIGH by Flasher PPC (in order to enable the JTAG TAP controller on the target device).

- All pins marked NC are not connected to Flasher PPC. Any signal can be applied here; Flasher PPC will simply ignore such a signal.
- Pins 2, 4, 6, 8, 10, and 12 are GND pins connected to GND in Flasher PPC. They should also be connected to GND in the target system.

6.4 Target board design

We strongly advise following the recommendations given by the chip manufacturer. These recommendations are normally in line with the recommendations. Please refer to the appropriate tables depending on the core:

- *Pinout JTAG* on page 100
- *Pinout SWD* on page 101
- *Flasher RX 14-pin connector* on page 103
- *Flasher PPC 14-pin connector* on page 105

In case of doubt you should follow the recommendations given by the semiconductor manufacturer.

6.4.1 Pull-up/pull-down resistors

Unless otherwise specified by developer's manual, pull-ups/pull-downs are recommended to be between 2.2 kOhms and 47 kOhms.

6.4.2 RESET, nTRST

The debug logic is reset independently from the CPU core with nTRST. For the core to operate correctly it is essential that both signals are asserted after power-up.

The advantage of having separate connection to the two reset signals is that it allows the developer performing software debug to setup breakpoints, which are retained by the debug logic even when the core is reset. (For example, at the reset vector address, to allow the code to be single-stepped as soon as it comes out of reset). This can be particularly useful when first trying to bring up a board with a new ASIC.

6.5 Adapters

6.5.1 JTAG Isolator

The JTAG Isolator can be connected between Flasher and JTAG adapter, to provide electrical isolation. This is essential when the development tools are not connected to the same ground as the application. For more information about the JTAG Isolator, please refer to *J-Link JTAG Isolator User Manual* (UM08010) which can be downloaded from our website.

VCC	1 ■ ■ 2	VCC
nTRST	3 ■ ■ 4	GND
TDI	5 ■ ■ 6	GND
TMS	7 ■ ■ 8	GND
TCK	9 ■ ■ 10	GND
RTCK	11 ■ ■ 12	GND
TDO	13 ■ ■ 14	GND
RESET	15 ■ ■ 16	GND
N/C	17 ■ ■ 18	GND
N/C	19 ■ ■ 20	GND

6.5.1.1 Pinout

The following table shows the target-side pinout of the J-Link JTAG Isolator.

Pin	Signal	Type	Description
1	VCC	Output	The target side of the isolator draws power over this pin.
2	VCC	Output	The target side of the isolator draws power over this pin.
3	nTRST	Output	JTAG Reset. Output from Flasher to the Reset signal of the target JTAG port. Typically connected to nTRST of the target CPU. This pin is normally pulled HIGH on the target to avoid unintentional resets when there is no connection.
5	TDI	Output	JTAG data input of target CPU. It is recommended that this pin is pulled to a defined state on the target board. Typically connected to TDI of target CPU.
7	TMS	Output	JTAG mode set input of target CPU. This pin should be pulled up on the target. Typically connected to TMS of target CPU.
9	TCK	Output	JTAG clock signal to target CPU. It is recommended that this pin is pulled to a defined state of the target board. Typically connected to TCK of target CPU.
11	RTCK	Input	Return test clock signal from the target. Some targets must synchronize the JTAG inputs to internal clocks. To assist in meeting this requirement, you can use a returned, and re-timed, TCK to dynamically control the TCK rate.
13	TDO	Input	JTAG data output from target CPU. Typically connected to TDO of target CPU.
15	RESET	I/O	Target CPU reset signal. Typically connected to the RESET pin of the target CPU, which is typically called "nRST", "nRESET" or "RESET".
17	N/C	N/C	This pin is not connected on the target side of the isolator.
19	N/C	N/C	This pin is not connected on the target side of the isolator.

Pins 4, 6, 8, 10, 12, 14, 16, 18, 20 are connected to GND.

6.5.2 J-Link Needle Adapter

To connect to the J-Link OB via programming interface the *J-Link Needle Adapter* is recommended.



Why to choose the J-Link Needle Adapter:

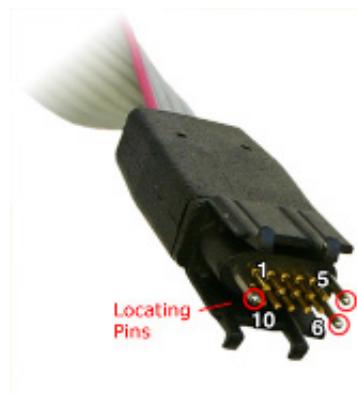
1. No additional connector required on your PCB
2. Very small footprint
3. High reliability spring pins for secure connections
4. Designed with 3 locating pins, so the adapter can not be connected the wrong way
5. No external power supply required! The J-Link Needle Adapter comes with the option to power the target hardware via J-Link.

These features make the J-Link Needle Adapter the perfect solution for production purposes.

The pinout of the J-Link Needle Adapter is based on the pinout of the needle adapter by Tag-Connect. Please note, that both pinouts are not identical since the J-Link Needle Adapter comes with a 5V-supply pin.

As you can see on the image below, the three locating pins ensure, that the adapter cannot be connected to the PCB the wrong way.

Moreover, the two "legs" on each side of the connector guarantee a stable and secure contact between pins and the PCB.



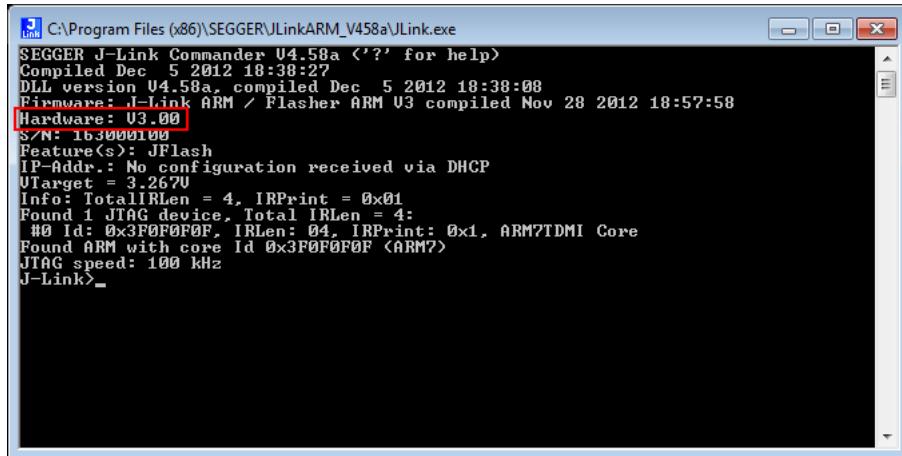
	V _{Tref}	1 ● ● 10	nRESET
SWDIO/TMS	2 ● ● 9	TRST	
GND	3 ● ● 8	TDI	
SWCLK/TCK	4 ● ● 7	RTCK	
5V-Supply	5 ● ● 6	SWO/TDO	

The J-Link Needle Adapter can be connected to J-Link via the *20-pin 0.1" JTAG to a 10-pin needle connector*.

6.6 How to determine the hardware version

To determine the hardware version of your Flasher, the first step should be to look at the label at the bottom side of the unit. Flasher has the hardware version printed on the back label.

If this is not the case with your Flasher, you can use JLink.exe to determine your hardware version (if Flasher is in J-Link mode). As part of the initial message, the hardware version is displayed. For more information about how to ensure that Flasher is in J-Link mode, please refer to *J-Link mode* on page 42.



```
C:\Program Files (x86)\SEGGER\JLinkARM_V458a\JLink.exe
SEGGER J-Link Commander V4.58a <'?' for help>
Compiled Dec 5 2012 18:38:27
DLL version 04.58a, compiled Dec 5 2012 18:38:08
Firmware: J-Link ARM / Flasher ARM V3 compiled Nov 28 2012 18:57:58
Hardware: U3.00
S/N: 163000100
Feature(s): JFlash
IP-Addr.: No configuration received via DHCP
UTarget = 3.2670
Info: TotalIRlen = 4, IRPrint = 0x01
Found 1 JTAG device. Total IRlen = 4:
#0 Id: 0x3F0F0F0F, IRlen: 04, IRPrint: 0x1, ARM7TDMI Core
Found ARM with core Id 0x3F0F0F0F <ARM7>
JTAG speed: 100 kHz
J-Link>_
```

Chapter 7

Support and FAQs

This chapter contains troubleshooting tips together with solutions for common problems which might occur when using Flasher. There are several steps you can take before contacting support. Performing these steps can solve many problems and often eliminates the need for assistance. This chapter also contains a collection of frequently asked questions (FAQs) with answers.

7.1 Contacting support

Before contacting support, make sure you tried to solve your problem by trying your Flasher with another PC and if possible with another target system to see if it works there. If the device functions correctly, the USB setup on the original machine or your target hardware is the source of the problem, not Flasher.

If you need to contact support, send the following information to support@segger.com

- A detailed description of the problem
- Flasher serial number
- Information about your target hardware (processor, board, etc.).
- FLASHER.CFG, FLASHER.DAT, FLASHER.LOG, SERIAL.TXT file from Flasher. To get these files, Flasher has to be in MSD mode. For more information about how to boot Flasher in MSD mode, please refer to *MSD mode* on page 46.

Flasher is sold directly by SEGGER.

7.2 Frequently Asked Questions

Maximum JTAG speed

Q: What is the maximum JTAG speed supported by Flasher?

A: Flasher's maximum supported JTAG speed is 12MHz.

Maximum download speed

Q: What is the maximum download speed?

A: The maximum download speed is currently about 720 Kbytes/second when downloading into RAM. The actual speed depends on various factors, such as JTAG, clock speed, host CPU core etc.

Chapter 8

Background information

This chapter provides background information about flash programming in general. It also provides information about how to replace the firmware of Flasher manually.

8.1 Flash programming

Flasher comes with a DLL, which allows - amongst other functionalities - reading and writing RAM, CPU registers, starting and stopping the CPU, and setting breakpoints.

8.1.1 How does flash programming via Flasher work?

This requires extra code. This extra code typically downloads a program into the RAM of the target system, which is able to erase and program the flash. This program is called RAM code and "knows" how to program the flash; it contains an implementation of the flash programming algorithm for the particular flash. Different flash chips have different programming algorithms; the programming algorithm also depends on other things, such as endianess of the target system and organization of the flash memory (for example 1 * 8 bits, 1 * 16 bits, 2 * 16 bits or 32 bits). The RAM code requires data to be programmed into the flash memory. The data is supplied by downloading it to RAM.

8.1.2 Data download to RAM

The data (or part of it) is downloaded to another part of the RAM of the target system. The Instruction pointer (PC) of the CPU is then set to the start address of the Ram code, the CPU is started, executing the RAM code. The RAM code, which contains the programming algorithm for the flash chip, copies the data into the flash chip. The CPU is stopped after this. This process may have to be repeated until the entire data is programmed into the flash.

8.1.3 Available options for flash programming

In general, there are two possibilities in order to use Flasher for flash programming:

- Using Flasher stand-alone to program the target flash memory (stand-alone mode)
- Using Flasher in combination with J-Flash to program the target flash memory (Flasher in "J-Link mode")

8.1.3.1 Using Flasher in stand-alone mode

In order to use the Flasher in stand-alone mode, it has to be configured first. For more information about how to setup Flasher for using in "stand-alone mode", please refer to *Setting up Flasher for stand-alone mode* on page 47.

8.1.3.2 J-Flash - Complete flash programming solution

J-Flash is a stand-alone Windows application, which can read / write data files and program the flash in almost any ARM system. For more information about J-Flash please refer to the *J-Flash User Guide*, which can be downloaded from our website <http://www.segger.com>.

8.1.4 How does the universal flash programming work?

In universal flash programming mode, the flasher typically communicates with a boot loader running on the device using a vendor specific protocol, rather than using the debug interface.

Universal flash programming is available only for stand-alone mode.

Chapter 9

Glossary

This chapter describes important terms used throughout this manual.

Big-endian

Memory organization where the least significant byte of a word is at a higher address than the most significant byte. See Little-endian.

Cache cleaning

The process of writing dirty data in a cache to main memory.

Coprocessor

An additional processor that is used for certain operations, for example, for floating-point math calculations, signal processing, or memory management.

Dirty data

When referring to a processor data cache, data that has been written to the cache but has not been written to main memory is referred to as dirty data. Only write-back caches can have dirty data because a write-through cache writes data to the cache and to main memory simultaneously. See also cache cleaning.

Halfword

A 16-bit unit of information.

Host

A computer which provides data and other services to another computer. Especially, a computer providing debugging services to a target being debugged.

ICache

Instruction cache.

ID

Identifier.

IEEE 1149.1

The IEEE Standard which defines TAP. Commonly (but incorrectly) referred to as JTAG.

Image

An executable file that has been loaded onto a processor for execution.

Instruction Register

When referring to a TAP controller, a register that controls the operation of the TAP.

IR

See Instruction Register.

Joint Test Action Group (JTAG)

The name of the standards group which created the IEEE 1149.1 specification.

Little-endian

Memory organization where the least significant byte of a word is at a lower address than the most significant byte. See also Big-endian.

Memory coherency

A memory is coherent if the value read by a data read or instruction fetch is the value that was most recently written to that location. Obtaining memory coherency is difficult when

there are multiple possible physical locations that are involved, such as a system that has main memory, a write buffer, and a cache.

Memory management unit (MMU)

Hardware that controls caches and access permissions to blocks of memory, and translates virtual to physical addresses.

Memory Protection Unit (MPU)

Hardware that controls access permissions to blocks of memory. Unlike an MMU, a MPU does not translate virtual addresses to physical addresses.

RESET

Abbreviation of System Reset. The electronic signal which causes the target system other than the TAP controller to be reset. This signal is also known as "nSRST" "nSYSRST", "nRST", or "nRESET" in some other manuals. See also nTRST.

nTRST

Abbreviation of TAP Reset. The electronic signal that causes the target system TAP controller to be reset. This signal is known as nICERST in some other manuals. See also nSRST.

Open collector

A signal that may be actively driven LOW by one or more drivers, and is otherwise passively pulled HIGH. Also known as a "wired AND" signal.

Processor Core

The part of a microprocessor that reads instructions from memory and executes them, including the instruction fetch unit, arithmetic and logic unit, and the register bank. It excludes optional coprocessors, caches, and the memory management unit.

Remapping

Changing the address of physical memory or devices after the application has started executing. This is typically done to make RAM replace ROM once the initialization has been done.

RTOS

Real Time Operating System.

TAP Controller

Logic on a device which allows access to some or all of that device for test purposes. The circuit functionality is defined in IEEE1149.1.

Target

The actual processor (real silicon or simulated) on which the application program is running.

TCK

The electronic clock signal which times data on the TAP data lines TMS, TDI, and TDO.

TDI

The electronic signal input to a TAP controller from the data source (upstream). Usually, this is seen connecting the J-Link Interface Unit to the first TAP controller.

TDO

The electronic signal output from a TAP controller to the data sink (downstream). Usually, this is seen connecting the last TAP controller to the J-Link Interface Unit.

Test Access Port (TAP)

The port used to access a device's TAP Controller. Comprises TCK, TMS, TDI, TDO, and nTRST (optional).

Transistor-transistor logic (TTL)

A type of logic design in which two bipolar transistors drive the logic output to one or zero. LSI and VLSI logic often used TTL with HIGH logic level approaching +5V and LOW approaching 0V.

Word

A 32-bit unit of information. Contents are taken as being an unsigned integer unless otherwise stated.

Chapter 10

Literature and references

This chapter lists documents, which we think may be useful to gain a deeper understanding of technical details.

Reference	Title	Comments
[J-Link]	J-Link / J-Trace User Guide	This document describes J-Link and J-Trace. It is publicly available from SEGGER (www.segger.com).
[J-Flash]	J-Flash User Guide	This document describes J-Flash. It is publicly available from SEGGER (www.segger.com).