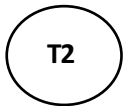
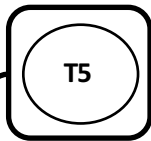


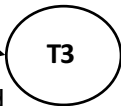
**CPU0**



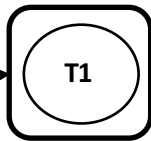
**T2**



**CPU1**



**T3**



**CPU2**

Preempted

T1 is preempted by T4.  
T1 swaps again with T5.