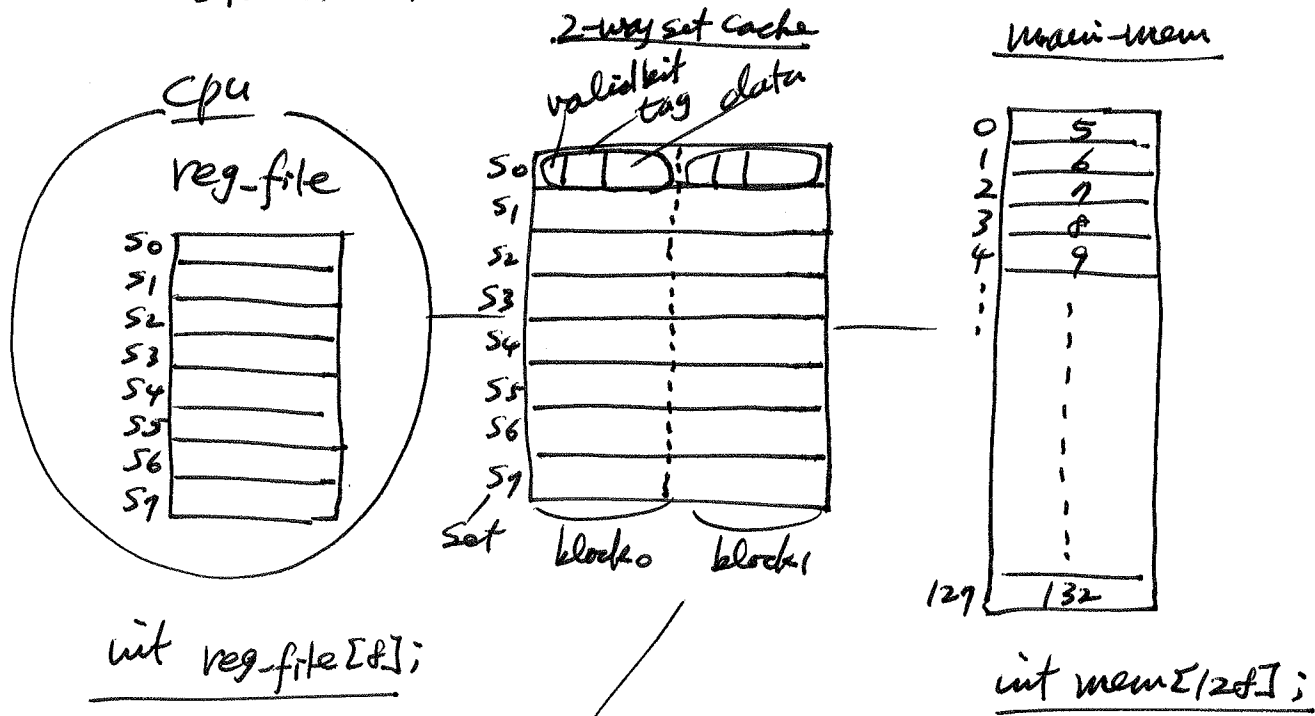


— Mem. Sim. program

data structures needed



struct block

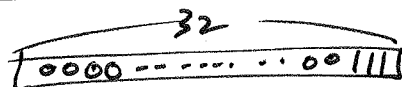
```
{
    int valid;
    int tag;
    int data;
}
```

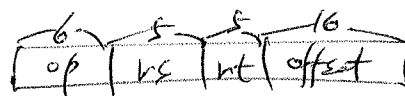
struct block [8][2];

— during simulation, use int types and values;  
when displaying the contents, use bit-print().

ex) int x = 7

cout << x; → bit-print(x);





logic:

1. get an instr. and decode
2. Compute effective address
  - rs + offset  $\rightarrow$  byte address
  - then  $\times 4 \rightarrow$  word address.
3. determine Cache Set# and Tag;
  - wordaddr mod  $(\times) 8$
  - word-addr DIV  $(\div) 8$
4. Cache hit/mis operation;

opcode — 35 (load), 43 (store)  
 base reg. — \$zero, always  $\phi$ .  
 offset value  
 rt — target of load, source of store  
 can be \$50 ~ \$57  
 16 ~ 23

once having index (Set#) and Tag from word-addr,

Search Cache:

using index, find set;  
 then check valid bit == 1, if so,  
 Check Tag field == Tag from word-addr.  
 if match  $\rightarrow$  hit  
 else  $\rightarrow$  mis

4 Cases:

load [ read hit — ①  
 read mis — ② ]

store [ write hit — ③  
 write mis — ④ ]

Cache data  $\rightarrow$  reg.file [rt]

$\rightarrow$  next page

reg.file [rt]  $\rightarrow$  Cache data; (update Cache only)

reg.file [rt]  $\rightarrow$  memory [word-addr];  
 (update mem. only)

WB cache

↓

↓  
— Case 2 (read miss)

- ① select victim block from 2 blocks:
  2. if victim's valid bit  $\neq 1$ , update memory:  $\rightarrow$  How to know mem. addr?
  3. bring (copy) memory block to victim block:  

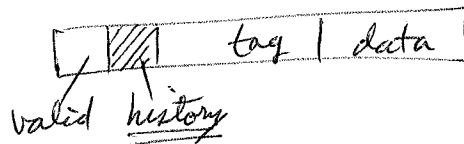
data
data
  4. set valid bit = 1 and Tag field:
  5.  $\text{reg. file [rt]} \leftarrow \text{cache block data}$ :
- Ⓐ

$\downarrow$

see below

→ How to select victim block? — LRU

⇒ use history bit (1/0)



— whenever a block is accessed (read hit, write hit)  
(read miss),

(history  $\leftarrow 1$   
and the other block's history  $\leftarrow \emptyset$ .

✓  
Q - How to determine mem. address for victim block?

+ we know cache set# (index), (when valid bit == 1)

↓ and the victim block has Tag field:

$$\Rightarrow \boxed{\text{Tag} * 8 + \text{index}}$$

tot # sets

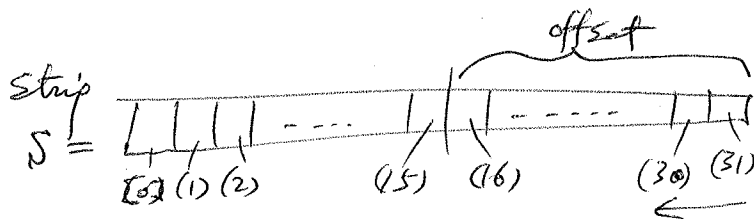
from addr. -  
Computation

94)  $127 \div 8 = 15, 7$

$$\Rightarrow \underset{\text{tag}}{Q \times 8} + \underset{\substack{\text{set \#} \\ (\text{index})}}{R} = \underline{\underline{127}} \quad \text{mem. addr.}$$

— from instruction (I-type),

How to extract offset value (int)?

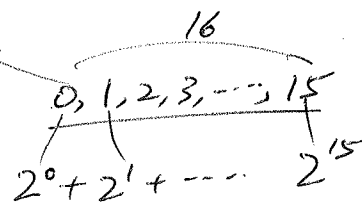


int offset = 0;

for (int i = 31; i > 15; i--)

if (S.at(i) == '1')

offset += power(2, 31-i)



Then  $\text{offset} / 4 \Rightarrow \text{word address}$

