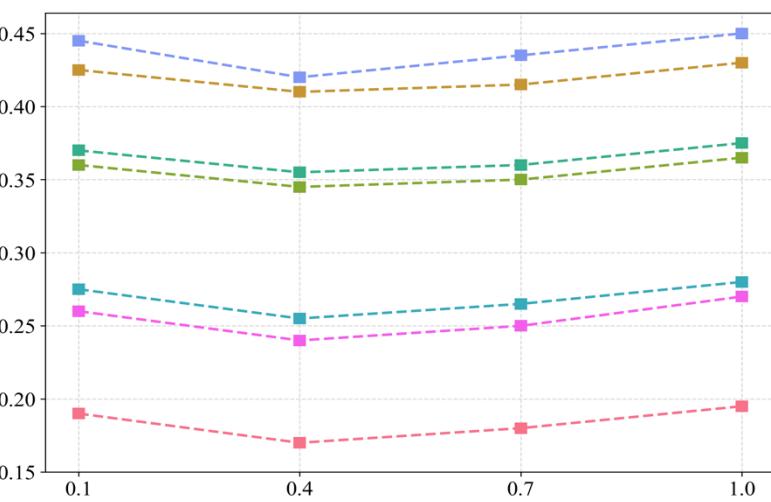
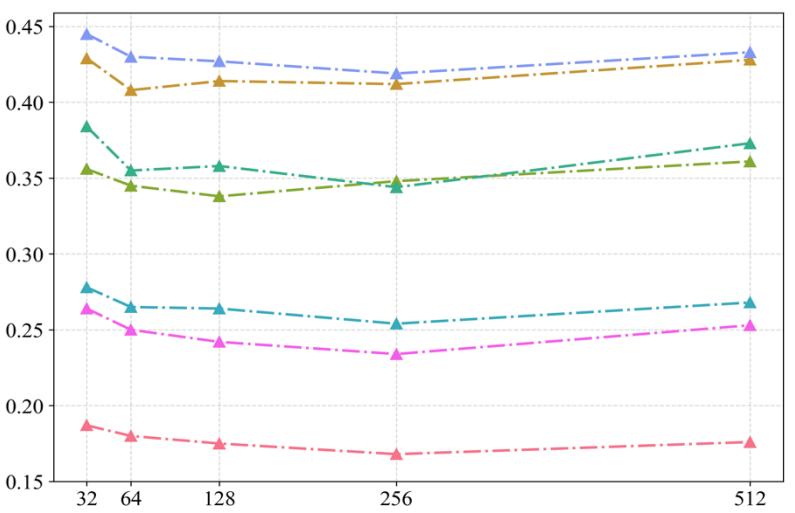


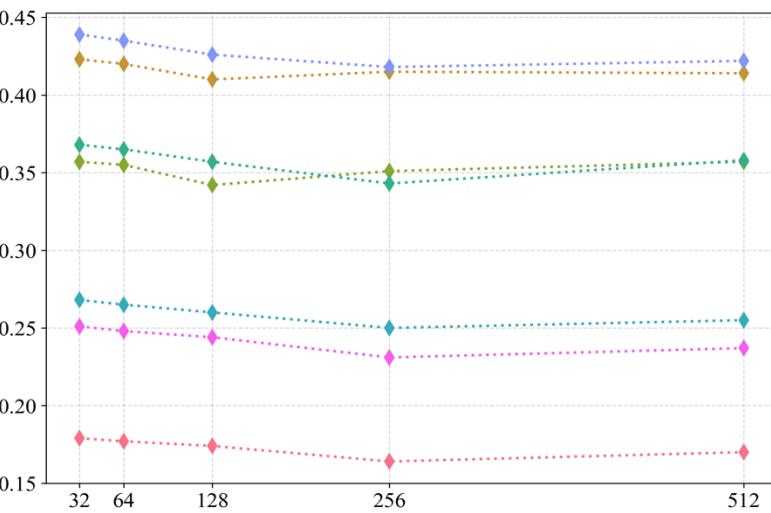
**a) Analysis on Input Length**



**b) Analysis on Norm Constant**



**c) Analysis on Model Dimension**



**d) Analysis on Gate Network Dimension**