



PCB trace widths
Signal type
Low current
High current
Digital
Analog

Minimal
0.5 mm
1.5 mm
0.2 mm
0.5 mm

Recommended
1.0 mm
>= 2.5 mm
0.5 mm
0.8 mm

Application
Low voltage DC power supply lines
Mains, high power DC
I/O, logic, microcontroller, CPLD
ADC, low power op-amps

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| IEQ Measurement Box | |
| main module | |
| PCB | |
| Designed by Qirui Huang | |
| E3D – Institute of Energy Efficiency and Sustainable Building – RWTH Aachen University | |
| Sheet: | |
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