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Question: Design a transistor-level circuit using CMOS only for the follo...

Design a transistor-level circuit using CMOS only for the following Boolean expressions. Also do the same using NMOS logic.

[Replace the letters U,V,W,X,Y & Z with the letters of your name sequentially

Cosider now, U=N, V=A, W=H, X=I, Y=S, Z=L

$$Y = (\overline{X + YZ})\overline{WV} + U$$

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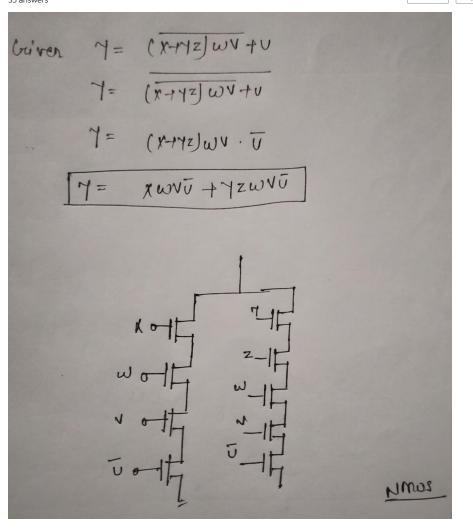
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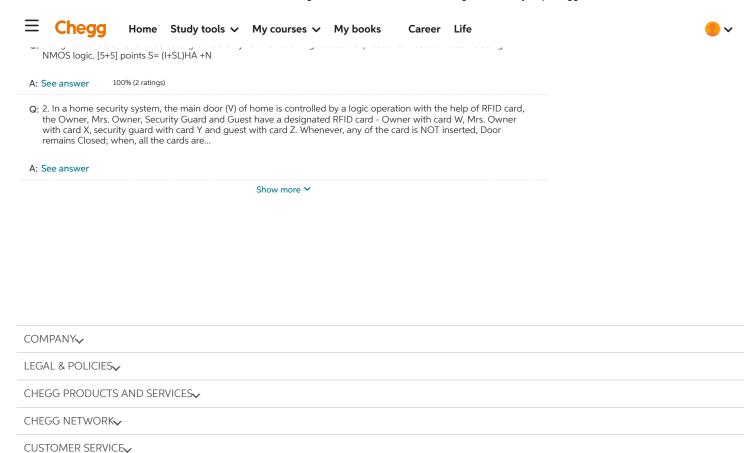
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