



**Computer architecture and logic design**

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**2022-BSE-067**

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**2022-BSE-064**

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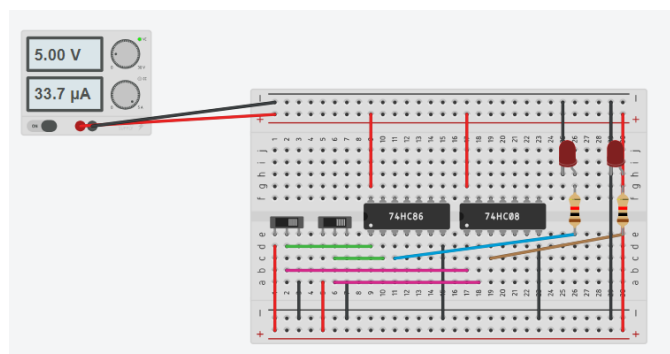
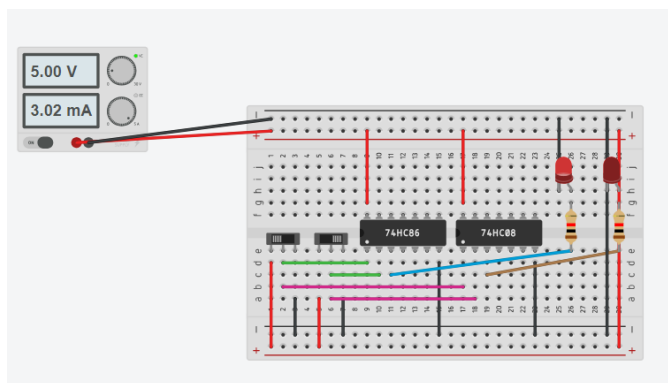
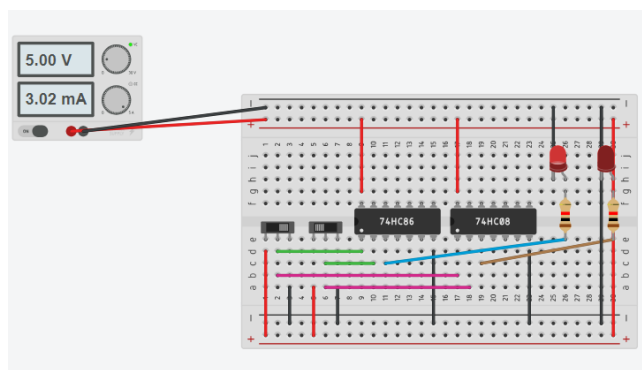
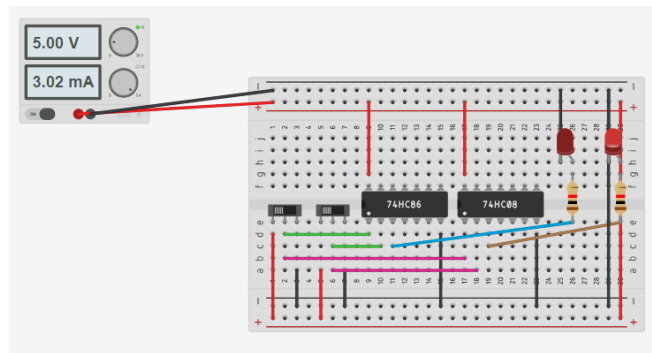
**Group#B**

**Lab 4**

**Submitted to Sir Shoaib**



## Half Adder



# FULL ADDER

