





**Computer architecture and logic design**

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**2022-BSE-067**

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**2022-BSE-064**

**Hafsah tahir**

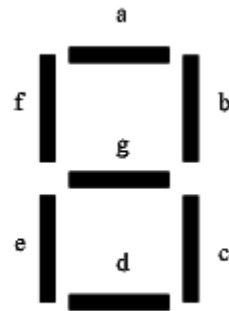
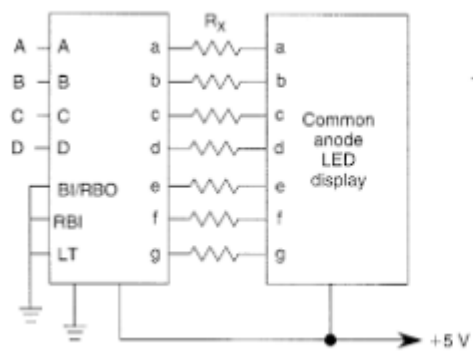
**2022-BSE-052**

**Group#B**

**LAB#05**

**Submitted to Sir Shoaib**

**Understand .the basic concept of BCD to Seven segment decode**



INPUT				Output
1	2	4	8	
0	0	0	0	0
1	0	0	0	1
0	1	0	0	2
1	1	0	0	3
0	0	1	0	4
1	0	1	0	5
0	1	1	0	6
1	1	1	0	7
0	0	0	1	8
1	0	0	1	9

