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Group#B

Computer architecture and logic design

LAB#03

Submitted to Sir Shoaib

NAND to XOR:

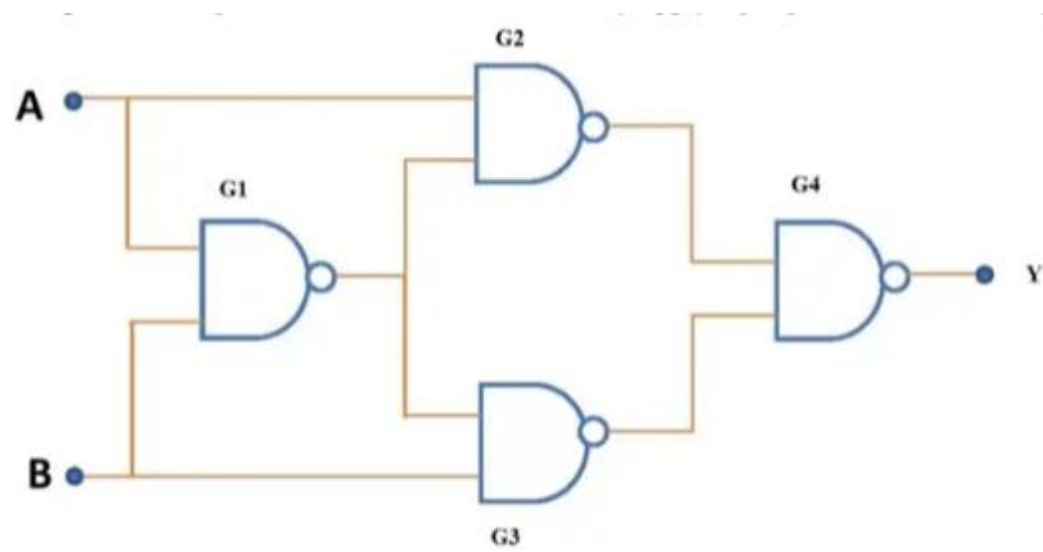
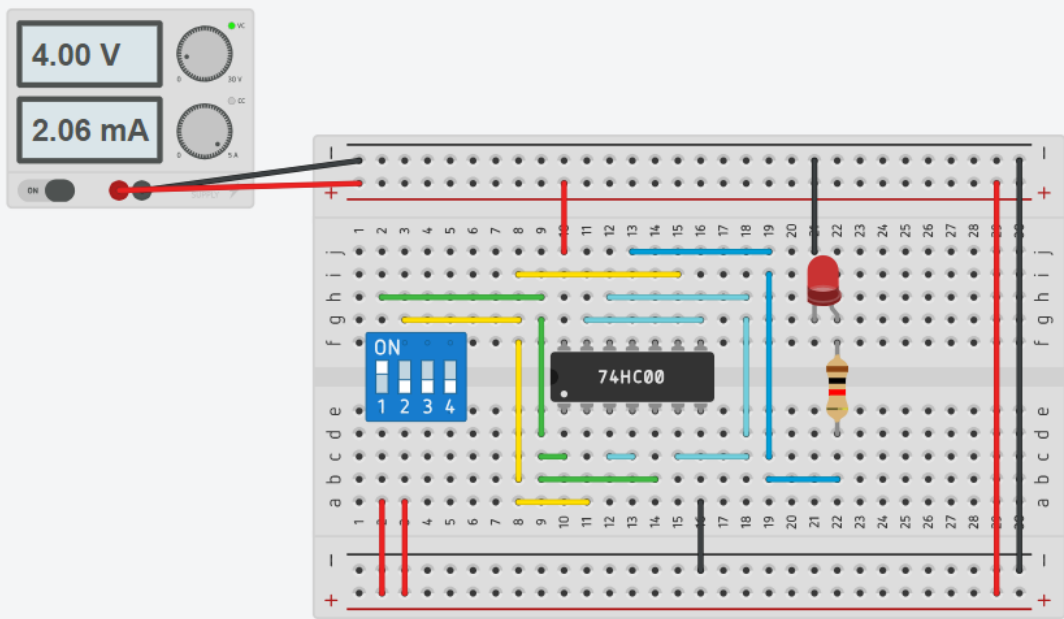
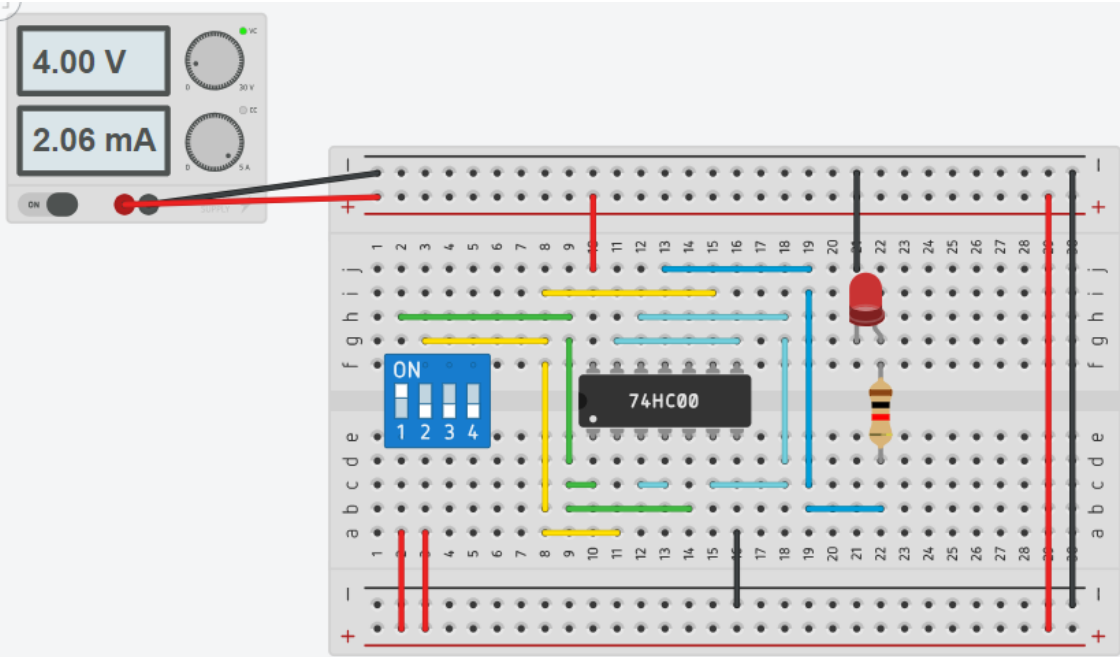
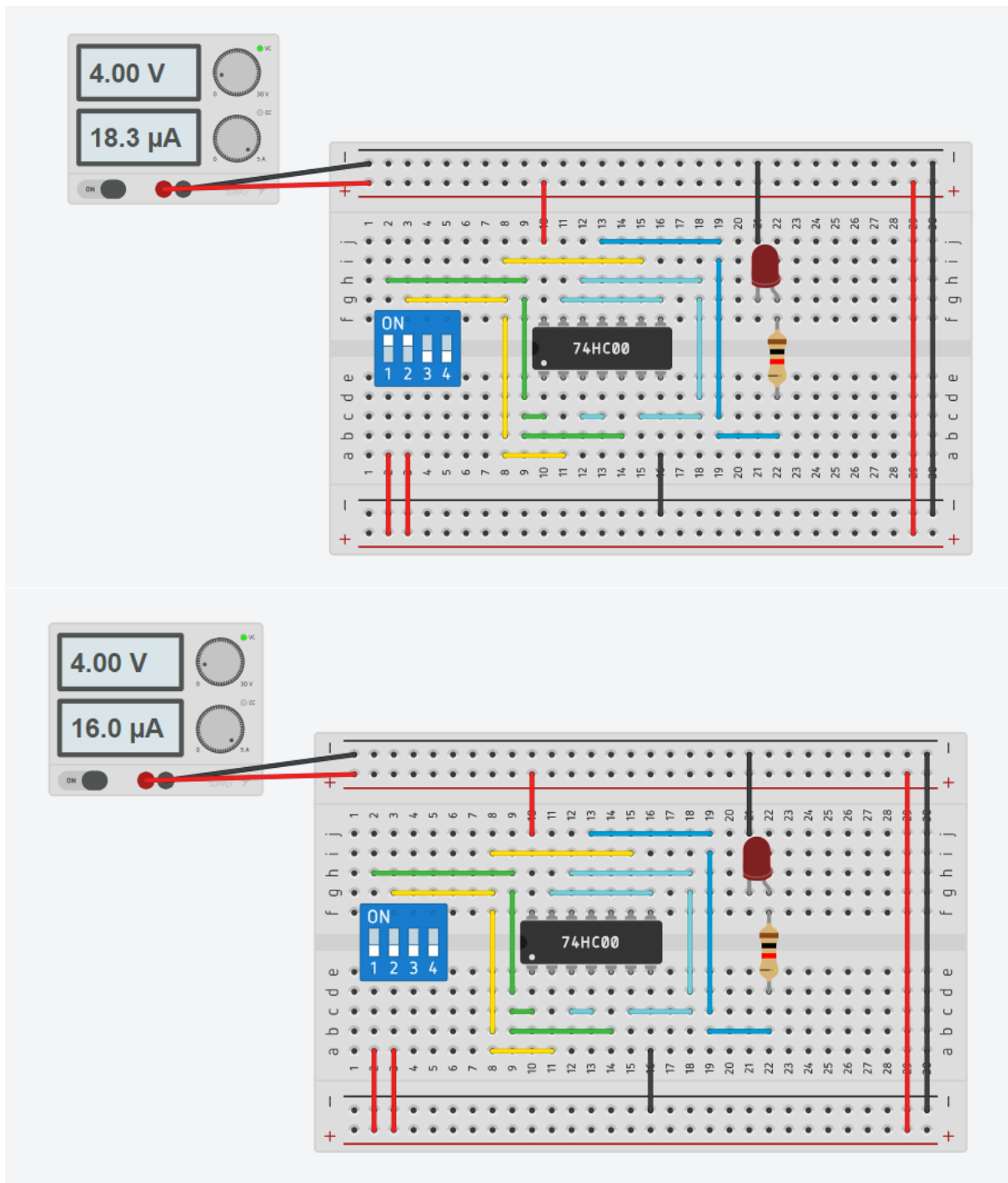


Figure 4.4: NAND implementation of XOR gate





NAND to XNOR:

NAND Gates as EX-NOR Gate

