



Sawaira saeed

2022-BSE-067

Rabia Batool

2022-BSE-064

Hafsah tahir

2022-BSE-052

Group#B

Computer architecture and logic design

LAB#03

Submitted to Sir Shoaib

NAND to XOR:

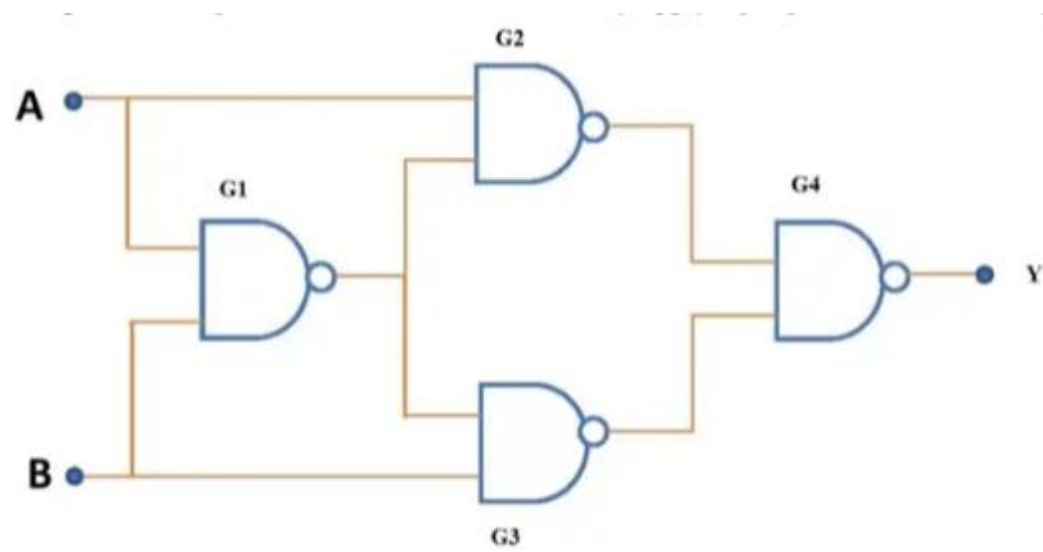
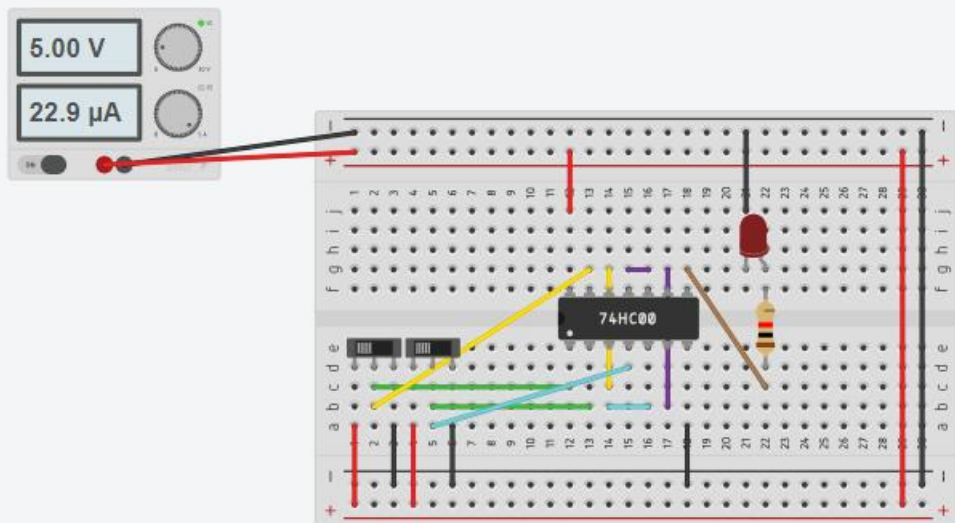
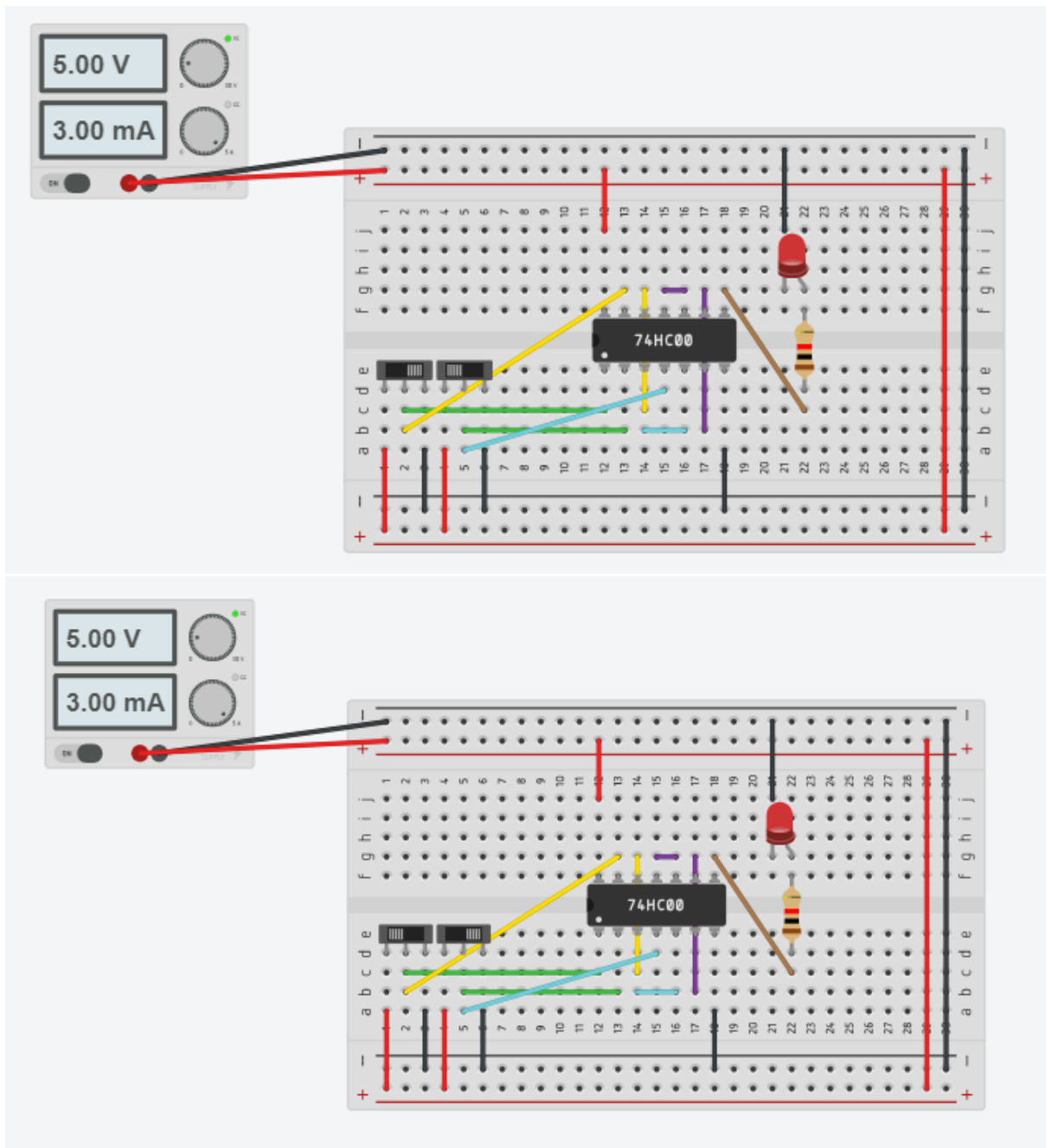


Figure 4.4: NAND implementation of XOR gate





NAND to XNOR:

NAND Gates as EX-NOR Gate

