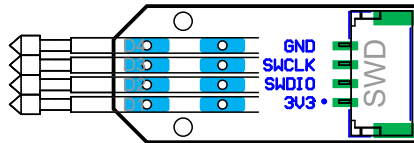


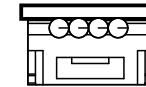


Version 1

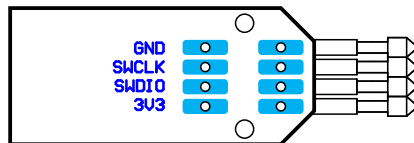
TOP SIDE



GND  
SWCLK  
SWDIO  
3V3



BOTTOM SIDE



GND  
SWCLK  
SWDIO  
3V3

1. Max input voltage 5.3 V
2. SWD pinout:  
GND  
SWCLK  
SWDIO  
3V3



Designator	Quantity	MF	MP	Value	Package	Description	Instructions	Type	HelpURL	ChipDip_Link	Name
D1, D2, D3, D4	4		P75-B1			Pogo pin P75-E2, Pogo pin P75-B1	see picture to install				P75-E2, P75-B1
SWD	1	JST Sales	SM04B-GHS-TB_LF__SN_-		None	Connector Header Surface Mount, Right Angle 4 position 0.049_1.25mm_			<a href="http://www.farnell.com/datasheets/2082975.pdf">http://www.farnell.com/datasheets/2082975.pdf</a>	<a href="https://www.chipdip.ru/product/sm04b-ghs-tb-lf-sn-gh-1.25mm-header-side-ent">https://www.chipdip.ru/product/sm04b-ghs-tb-lf-sn-gh-1.25mm-header-side-ent</a>	SM04B-GHS-TB(LF)(SN)-