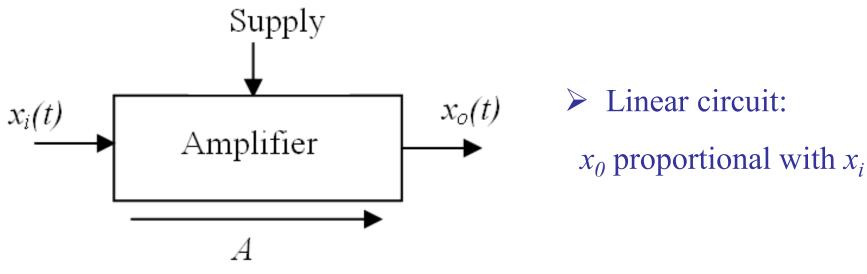
# Electronic Amplifiers

# **Electronic Amplifiers**

Amplifier: activ three-port network that delivers to the output a signal  $x_o(t)$  (voltage or current) with the same shape as the input signal  $x_i(t)$  and can provide greater power on an adequated load.



A – amplification, gain

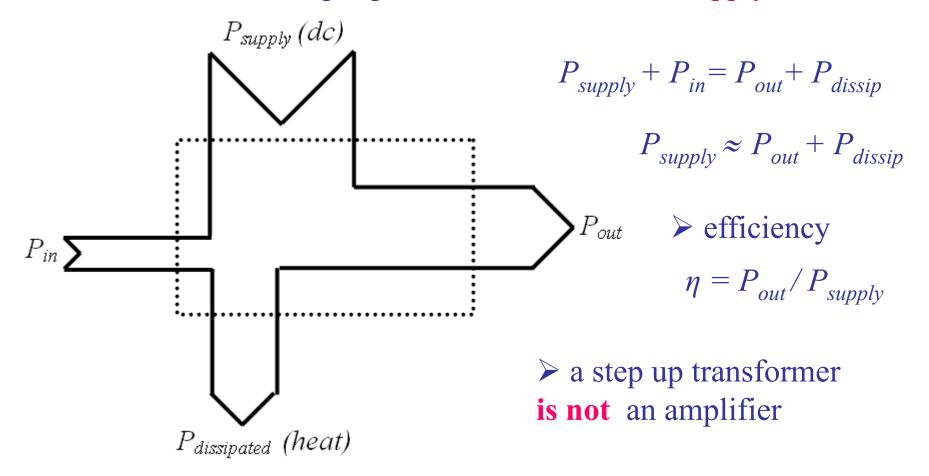
$$x_o(t) = Ax_i(t)$$

A < 0 inverting

A > 0 non-inverting

#### Power transfer and power balance

- $\triangleright$  the average power of the output signal  $P_{out}$  is greater than the average power of the input signal  $P_{in}$ .
- > the excess of the output power is taken from the supply sources



#### **Amplifier models**

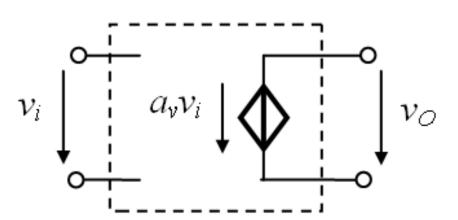
- ➤ two-port network: it consider explicitly only the behavior to the input and output ports and input-output transfer for the signal
- > valid, irrespective of the internal complexity of the amplifiers
- > valid in the bandpass frequency domain

#### Linear controlled sources

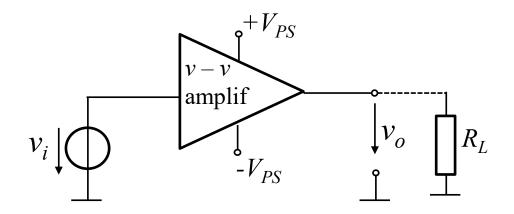
- ➤ active two-port network only one finite, non-zero parameter: forward transfer parameter (gain)
- The output signal (voltage  $v_o$ ) is **controlled** by the input signal (voltage  $v_i$ )
- > pseudo-sources

**Example: VCVS** 

$$v_0 = a_v v_i$$



### Noninverting amplifier, symmetric differential supply



$$A_{v} > 0$$

$$v_{O} = A_{v}v_{i}$$

$$v_O = A_{\nu} v_i$$

> general-purpose op-amp

$$v_O \in (-V_{PS} + 1V...2V; +V_{PS} - 1V...2V)$$

rail-to-rail op amp:

$$v_0 \in (-V_{PS}; +V_{PS})$$

# VTC for the voltage-to-voltage, noninverting amplifier, symmetric differential supply

> amplification (active) region:

$$v_{I} \in \left(\frac{V_{OL}}{A_{v}}; \frac{V_{OH}}{A_{v}}\right);$$

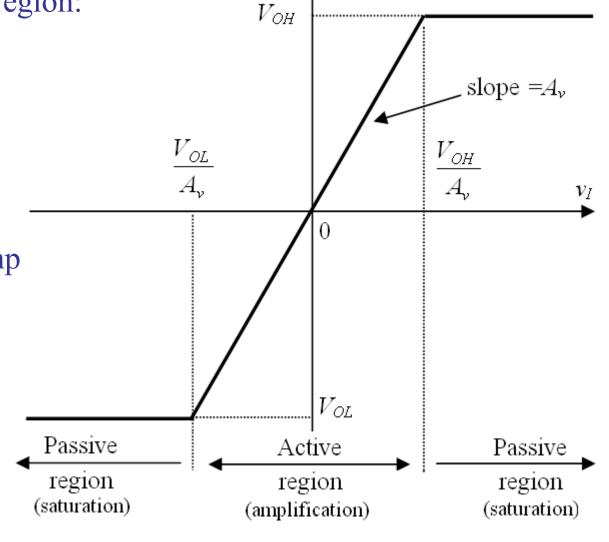
$$v_{O} \in \left(V_{OL}; V_{OH}\right)$$

general-purpose op-amp

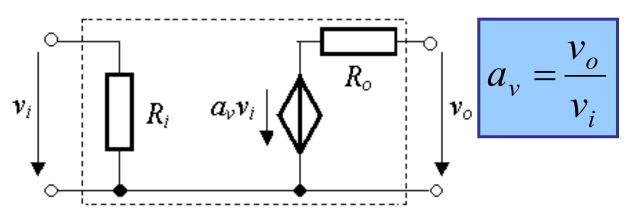
$$v_O \in (-V_{PS} + 1V...2V; + V_{PS} - 1V...2V)$$

> rail-to-rail OA:

$$v_O \in \left(-V_{PS}; +V_{PS}\right)$$

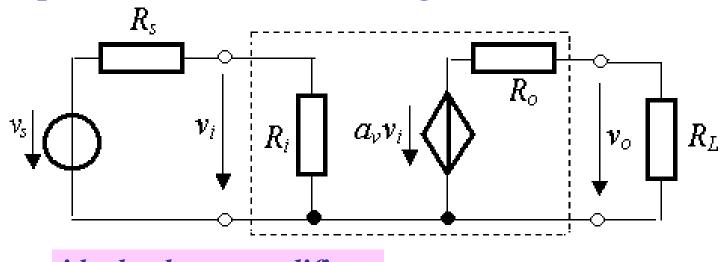


#### Modeling the voltage amplifier



 $a_{v} = \frac{v_{o}}{v_{i}}$   $R_{i} - \text{draws current from the input signal source}$   $R_{o} - \text{deteriorates the output voltage in the presence of load (voltage divider)}$ 

#### Amplifier connected with a signal source and a load resistance



ideal voltage amplifier

$$R_i = \infty; \qquad R_o = 0$$

$$v_i = v_s$$

$$v_o = a_v v_i$$

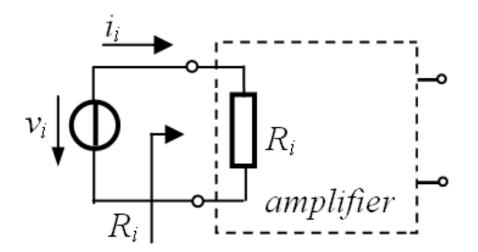
## **Determining the amplifier performances**

- gain (forward transfer factor)
- > input resistance
- > output resistance

#### Gain

- analysis of the circuit using theorems and electrical circuit relations (Kirchhoff, Ohm, etc.) and equations describing the operation of the active devices
- express the output signal as a function of the input signal and compute the gain

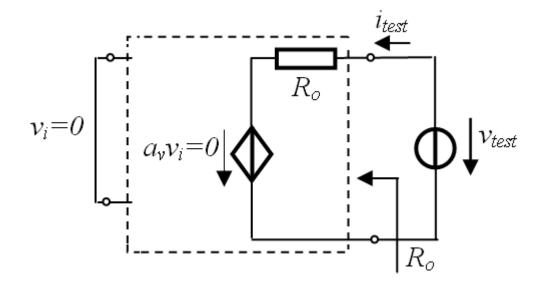
#### Input resistance



$$R_i = \frac{v_i}{i_i}$$

➤ The resistance seen by the signal source when it looks to the circuit

#### **Output resistance**



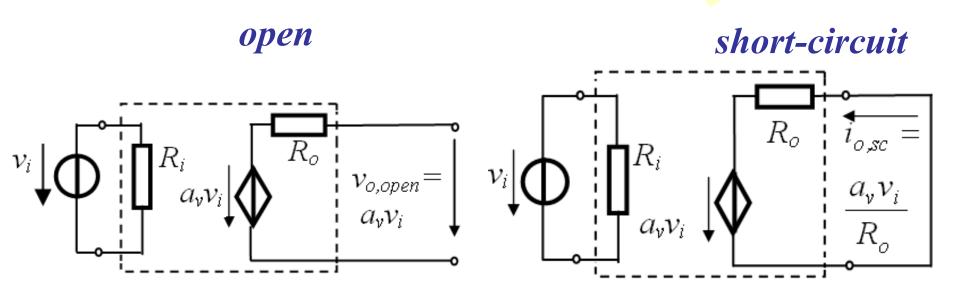
- > Set the input signal source to zero
- Connect to the output a test source

$$R_o = \frac{v_{test}}{i_{test}}$$

➤ The resistance seen by the load when it looks back to the circuit and input signal source is set to zero

#### Output resistance – alternative method

OPtional



$$R_o = \frac{v_{o,open}}{i_{o,sc}}$$