

Lecture 16: Channel Coding

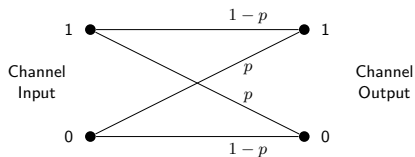
Prof. Deniz Gunduz

Department of Electrical & Electronic Engineering
Imperial College London

- Channel coding
- Linear block codes and generator matrix
- Hamming weight and distance
- Error detection and correction
- Reference
 - ✓ [Haykin] Chapter 10

- Noise can corrupt the information during transmission
- Corruption of a signal should be avoided if possible
- Different systems will generally require different levels of protection against errors
- Consequently, a number of different **channel coding** techniques have been developed to detect and correct different types and number of errors

- Binary Symmetric Channel (BSC):



- Error probabilities are symmetric, errors are stationary and statistically independent
- p is presumed to be less than $1/2$, or $p < 1 - p$

- Two numbers: 0, 1
- Addition $+$: $0 + 0 = 1 + 1 = 0$; $0 + 1 = 1 + 0 = 1$
- Multiplication \times : $0 \times 0 = 0 \times 1 = 1 \times 0 = 0$; $1 \times 1 = 1$
- Calculation order: same as regular number calculation (multiplication first, from left to right), for example,

$$1 \times 1 + 1 \times 0 + 0 = (1 \times 1) + (1 \times 0) + 0 = 1 + 0 + 0 = 1$$

- Algebraic in Modulo 2:

$$\text{vector } \mathbf{a} = \begin{bmatrix} 1 \\ 0 \\ 1 \end{bmatrix}, \quad \text{matrix } \mathbf{B} = \begin{bmatrix} 1 & 1 & 0 \\ 1 & 0 & 1 \end{bmatrix}$$

$$\mathbf{B}\mathbf{a} = \begin{bmatrix} 1 & 1 & 0 \\ 1 & 0 & 1 \end{bmatrix} \begin{bmatrix} 1 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \times 1 + 1 \times 0 + 0 \times 1 \\ 1 \times 1 + 0 \times 0 + 1 \times 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

If the error probability is small and information is fairly fault-tolerant, it is possible to use simple methods to detect errors (e.g., repetition, parity bits)

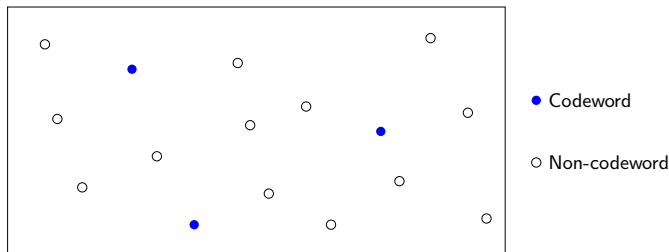
- **Repetition** – Repeating each bit in the message
 - ✓ If two symbols in an adjacent pair are different, it is likely that an error has occurred
 - ✓ However, this is not very efficient (bit rate is halved)
 - ✓ One repetition provides a means for error **detection**, but not for error correction
 - ✓ More repetitions are needed for error **correction**

Simple Error Checks: Adding a “Parity Bit”

- **Parity bit** – Use of a “parity bit” at the end of the message
 - ✓ A parity bit is a single bit that corresponds to the sum of the other message bits (modulo 2)
 - ✓ $\mathbf{u} = [u_1 \quad \dots \quad u_k] \Rightarrow \mathbf{c} = [c_1 \quad \dots \quad c_k \quad p], p = c_1 + c_2 + \dots + c_k$
 - ✓ For example, 011 \rightarrow 0110; 010 \rightarrow 0101
 - ✓ This allows any odd number of errors to be detected, but not even numbers
 - ✓ A single parity bit only allows error **detection**, not error **correction**
 - ✓ More efficient than simple repetition

An important class of codes that can detect and correct some errors are **block codes**

- Encode a series of symbols from the source, a “block”, into a longer string: codeword or code block
- **Error detection:** if the received coded block is not a valid codeword
- **Error correction:** “decode” and associate a corrupted block to a valid coded block by its proximity (as measured by the “Hamming distance”)



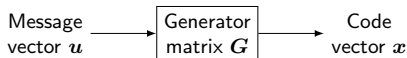
An (n, k) **binary linear block code** takes a block of k bits of source data and encodes them using n bits.

$$\text{code rate} = \frac{\text{information bits}}{\text{codeword bits}} = \frac{k}{n}$$

- **Linearity:** the *Boolean sum* of any two codewords *must* be another codeword, e.g., if $\mathbf{a} = [1 \ 0 \ 0]$ and $\mathbf{b} = [1 \ 0 \ 1]$ are codewords, then $\mathbf{c} = \mathbf{a} + \mathbf{b} = [1 + 1 \ 0 + 0 \ 0 + 1] = [0 \ 0 \ 1]$ is, too
- The set of codewords forms a vector space, within which mathematical operations can be defined and performed

- To construct a linear block code we define a matrix, the **generator matrix G** , which converts blocks of source symbols into longer blocks corresponding to codewords
- G is a $k \times n$ matrix (k rows, n columns) that takes a source block u (a binary vector of length k), to a codeword x (a binary vector of length n)

$$x = u \cdot G$$



$$u = [1 \quad 0], \quad G = \begin{bmatrix} 1 & 0 & 1 \\ 0 & 1 & 1 \end{bmatrix}, \quad x = uG = [1 \quad 0] \begin{bmatrix} 1 & 0 & 1 \\ 0 & 1 & 1 \end{bmatrix} = [1 \quad 0 \quad 1]$$

- Linearity: Summation of two codewords is another codeword.
 - ✓ If $x_1 = u_1 G$, $x_2 = u_2 G$ are two codewords, then $x_1 + x_2 = u_1 G + u_2 G = (u_1 + u_2)G$ is another codeword!

Hamming Weight

Richard Hamming (1915 – 1998) established code theory and method when he worked at AT&T Bell Labs, New Jersey, USA.



- **Hamming weight** of a binary vector \mathbf{a} (written as $w_H(\mathbf{a})$), is the number of non-zero elements it contains. For example:
 - ✓ 001110011 has a Hamming weight of 5
 - ✓ 000000000 has a Hamming weight of 0

- **Hamming Distance** between two binary vectors, \mathbf{a} and \mathbf{b} , is written as $d_H(\mathbf{a}, \mathbf{b})$, and is equal to the Hamming weight of their (Boolean) sum

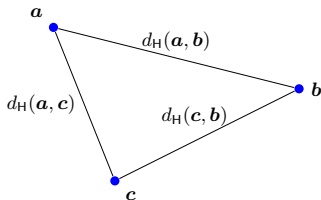
$$d_H(\mathbf{a}, \mathbf{b}) = w_H(\mathbf{a} + \mathbf{b})$$

For example, 01110011 and 10001011 have a Hamming distance of

$$d_H(01110011, 10001011) = w_H(01110011 + 10001011) = w_H(11111000) = 5$$

- **Triangle inequality**

$$d_H(\mathbf{a}, \mathbf{b}) \leq d_H(\mathbf{a}, \mathbf{c}) + d_H(\mathbf{c}, \mathbf{b})$$



(7,4) Hamming Code

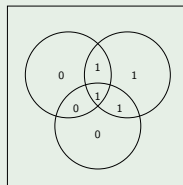
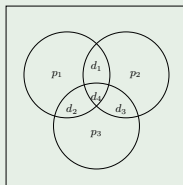
- Data bits: $\mathbf{u} = [d_1 \ d_2 \ d_3 \ d_4]$
- Generator matrix:

$$\mathbf{G} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 \end{bmatrix} = [\mathbf{I} \ \mathbf{P}]$$

- Codeword:

$$\mathbf{x} = \mathbf{u}\mathbf{G} = [d_1 \ d_2 \ d_3 \ d_4 \ d_2+d_3+d_4 \ d_1+d_3+d_4 \ d_1+d_2+d_4]$$

- Parity bits: $p_1 = d_1+d_2+d_4$, $p_2 = d_1+d_3+d_4$, $p_3 = d_2+d_3+d_4$



(7,4) Hamming Code

$$G = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 \end{bmatrix}$$

- Data bits: $\mathbf{u}_1 = [1 \ 0 \ 1 \ 0] \Rightarrow$ codeword $\mathbf{x}_1 = \mathbf{u}_1 \mathbf{G} = [1 \ 0 \ 1 \ 0 \ 1 \ 0 \ 1]$
- Data bits: $\mathbf{u}_2 = [1 \ 1 \ 0 \ 1] \Rightarrow$ codeword $\mathbf{x}_2 = \mathbf{u}_2 \mathbf{G} = [1 \ 1 \ 0 \ 1 \ 0 \ 0 \ 1]$
- Data bits: $\mathbf{u}_3 = [0 \ 0 \ 1 \ 0] \Rightarrow$ codeword $\mathbf{x}_3 = \mathbf{u}_3 \mathbf{G} = [0 \ 0 \ 1 \ 0 \ 1 \ 1 \ 0]$
- Calculate the Hamming distances between each pair of codewords $\mathbf{x}_1, \mathbf{x}_2, \mathbf{x}_3$ and compare them to the Hamming distances between each pair of data bits $\mathbf{u}_1, \mathbf{u}_2, \mathbf{u}_3$

$$d_H(\mathbf{x}_1, \mathbf{x}_2) = 4 > d_H(\mathbf{u}_1, \mathbf{u}_2) = 3$$

$$d_H(\mathbf{x}_1, \mathbf{x}_3) = 3 > d_H(\mathbf{u}_1, \mathbf{u}_3) = 1$$

$$d_H(\mathbf{x}_2, \mathbf{x}_3) = 7 > d_H(\mathbf{u}_2, \mathbf{u}_3) = 4$$

$$\mathbf{x} = \mathbf{uG} = \mathbf{u} \begin{bmatrix} \mathbf{I} & \mathbf{P} \end{bmatrix} = \begin{bmatrix} \mathbf{u} & \mathbf{uP} \end{bmatrix}$$

$$d_H(\mathbf{x}_i, \mathbf{x}_j) = d_H(\mathbf{u}_i, \mathbf{u}_j) + d_H(\mathbf{u}_i \mathbf{P}, \mathbf{u}_j \mathbf{P})$$

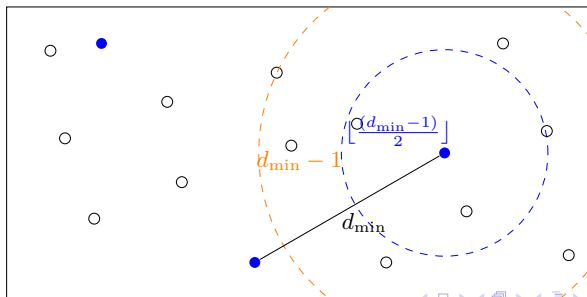
- To determine the number of errors a particular code can *detect* and *correct*, we look at the **minimum Hamming distance** between any two codewords.
- From linearity, the zero vector must be a codeword. The minimum Hamming distance of a code is the same as minimum weight of non-zero codewords.
- We define the minimum distance between any two codewords to be

$$d_{\min} = \min_{\substack{\mathbf{a}, \mathbf{b} \in \mathcal{C} \\ \mathbf{a} \neq \mathbf{b}}} d_H(\mathbf{a}, \mathbf{b}) = \min_{\substack{\mathbf{a}, \mathbf{b} \in \mathcal{C} \\ \mathbf{a} \neq \mathbf{b}}} d_H(0, \mathbf{a} + \mathbf{b}) = \min_{\mathbf{c} \in \mathcal{C}, \mathbf{c} \neq \mathbf{0}} w_H(\mathbf{c})$$

where \mathcal{C} is the set of codewords.

Error Detection and Correction

- The number of errors that can be **detected** is then $d_{\min} - 1$ since d_{\min} errors may turn an input codeword into a different valid codeword. Less than d_{\min} errors will turn an input codeword into a vector that is not a valid codeword.
- Number t of errors that can be **corrected** is $t = \lfloor (d_{\min} - 1)/2 \rfloor$, simply the number of errors that can be detected divided by two and rounded down to the nearest integer since any output vector with less than this number of errors will be “nearer” to the input codeword.
- (7,4) Hamming code has $d_{\min} = 3$. It can detect one or two bit errors, and correct any single bit error.



- The first success was the application of convolutional codes in deep space probes 1960's-70's.
 - ✓ Mariner Mars, Viking, Pioneer missions by NASA
- Voyager, Galileo missions were further enhanced by concatenated codes (RS + convolutional).
- The next chapter was trellis coded modulation (TCM) for voice-band modems in 1980's.
- 1990's saw turbo codes approached capacity limit (now used in 3G).
- Followed by another breakthrough – space-time codes in 2000's (used in WiMax, 4G)
- The current frontier: LDPC, fountain codes, network coding, polar codes in 5G

