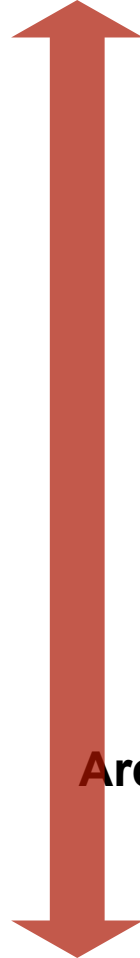MATLAB/Python Algorithm
or
C++ Instruction Set
Simulator

C++ Computer Architecture
Simulation Framework
(Object-Oriented)

Verilog or VHDL Design with
EDA Toolflow
(Concurrent-Structural)

Functional Level

**Algorithm and ISA
Development**

Cycle Level

**Design Space
Exploration**

Register Transfer Level

**Area/Energy/Timing Validation
and
Prototype Development**