F1: 3bits

1. No action
2. Rsrc-out
3. Rdest\_out
4. SOURCEout
5. DESTout
6. PCout
7. MDRout
8. Zout

F2: 2bits

1. No action
2. Yout
3. Rd
4. Wr

F3: 3bits

1. No action
2. Add
3. IncA
4. DecA
5. Set flag status ‘01’
6. Set flag status ‘10’
7. Set flag status ‘11’
8. End

F4: 3bits

1. No action
2. Zin
3. SOURCEin
4. DESTin
5. Yin
6. PCin
7. Rsrc-in
8. Rdest-in

F5: 3bits

1. No action
2. IncPC
3. PCinc-in
4. Operation
5. SOURCE/DEST in
6. SOURCE/DEST out
7. Rsrc/dest in
8. Rsrc/dest out

F6: 3bits

1. No action
2. Set flag status ‘00’
3. MARin
4. MDRin
5. IRin
6. Decoder offset-out
7. Bit src mode ‘bit or’
8. HLT

F7: 3bits

1. No action
2. Bit Dir mode ‘bit or’
3. Bit Reg Dir Indie ‘bit or’
4. Bit Inst Src Dst ‘bit or’
5. Bit Dst mode ‘bit or’
6. Bit Src Dst ‘bit or’
7. Bit save ‘bit or’
8. Wide branch ‘bit or’