

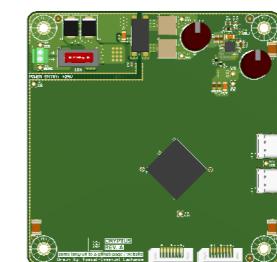
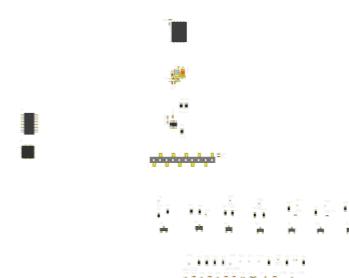
# CRYPTUS

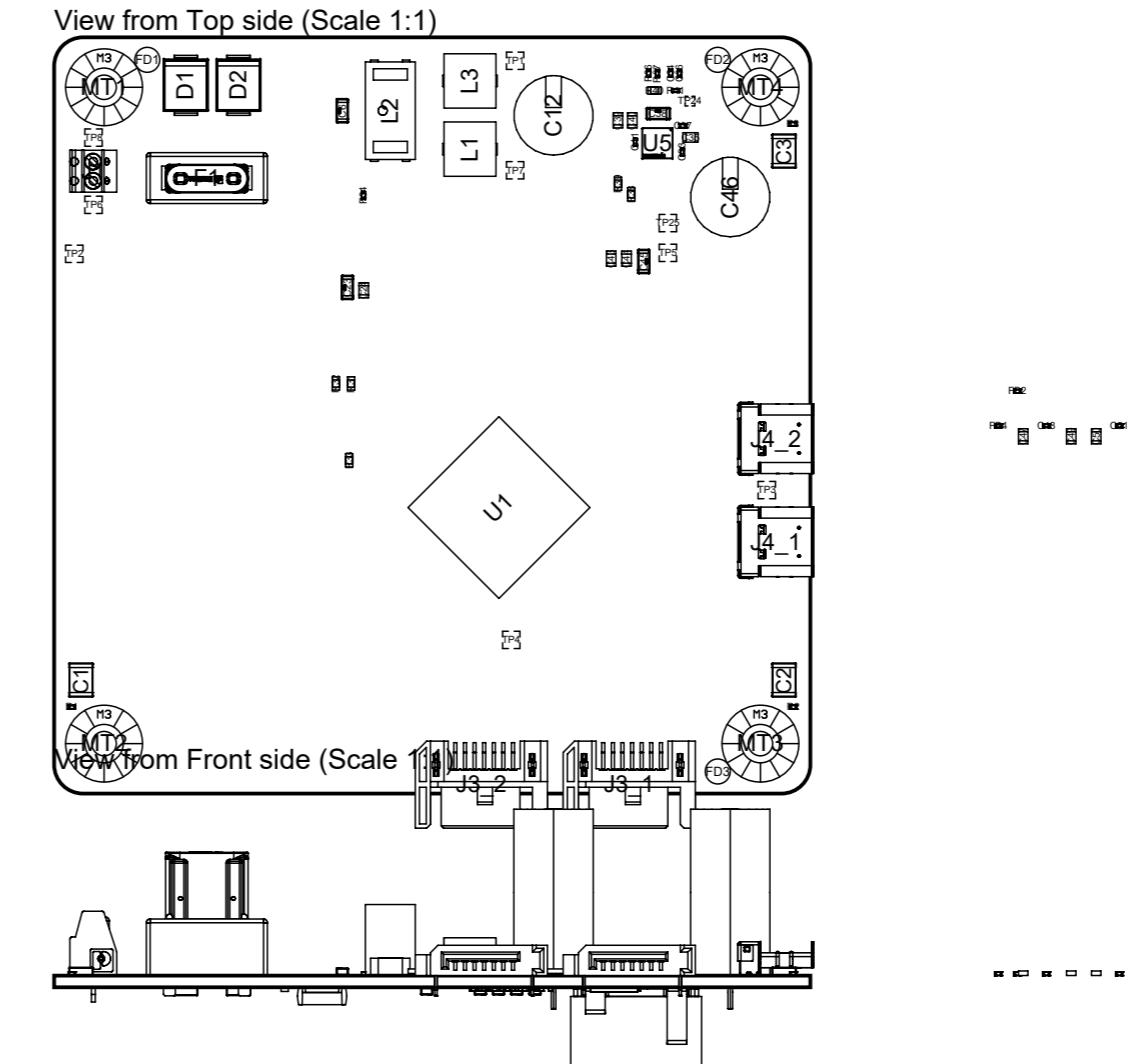
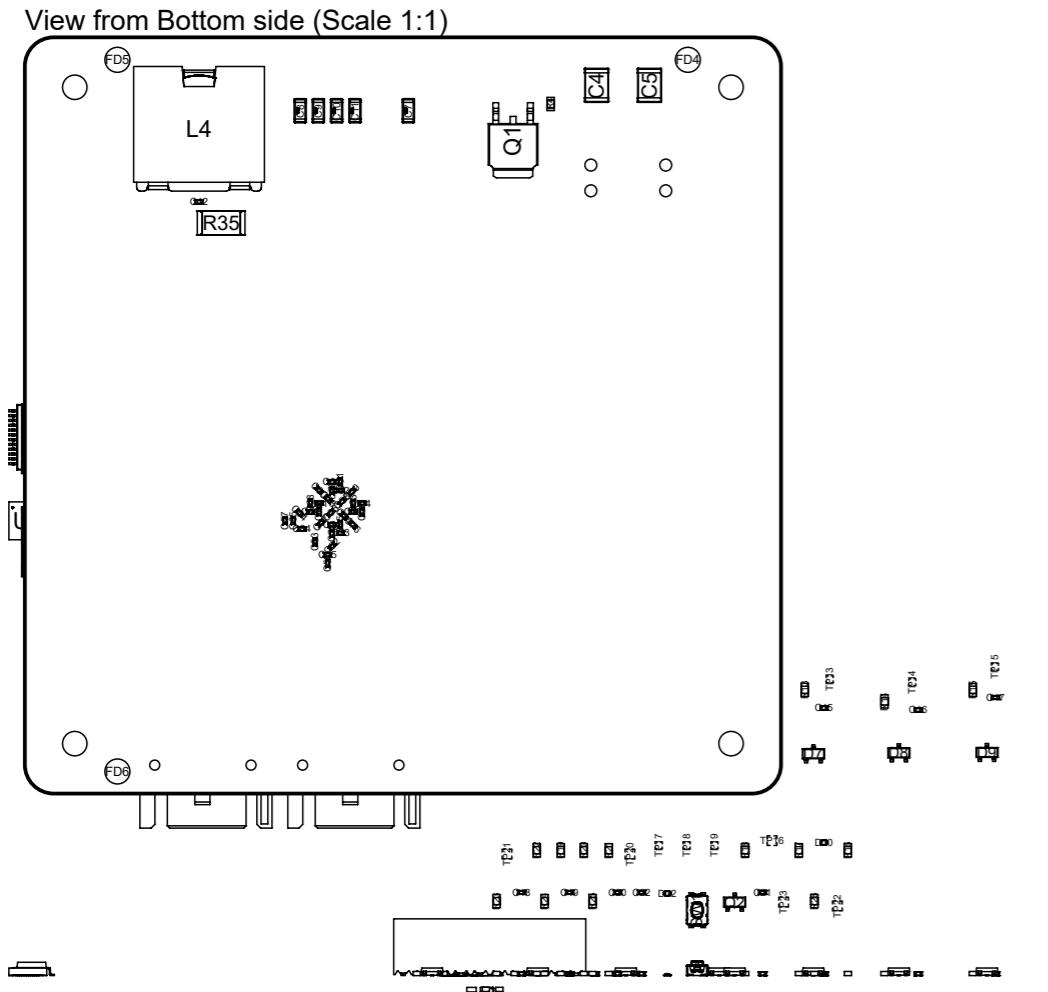
## REV B

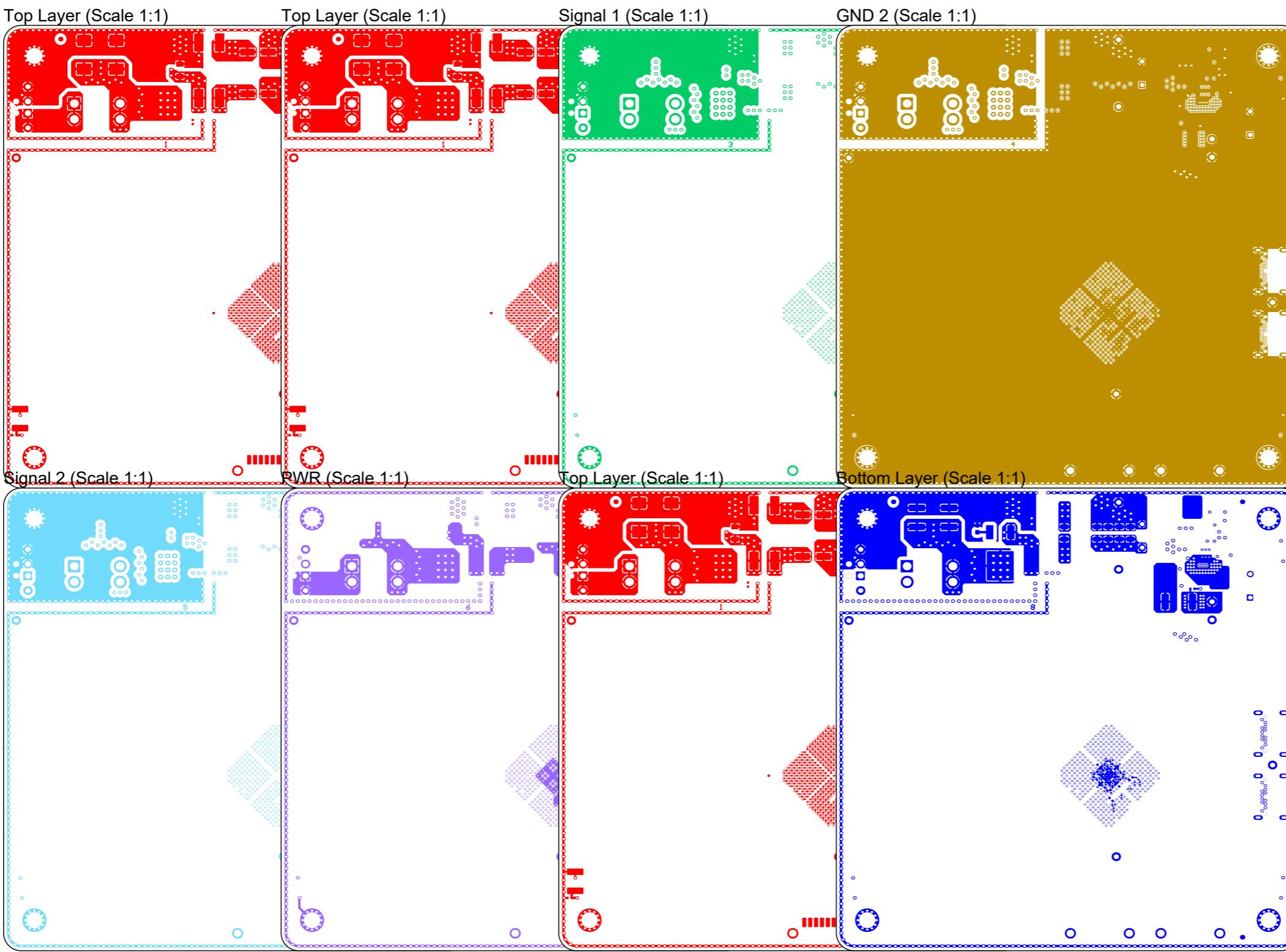
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PAGE	TITLE	DESCRIPTION
1	TITLE	COVER PAGE
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3	PCB PRINTS	TOP VIEW OF ALL LAYERS
4	LAYER STACK	VIEW OF LAYER STACK DIMENSIONS
5	DRILL	DRILL DRAWING & DRILL TABLE
6	FAB NOTES	FABRICATION NOTES & CONTROLLED IMPEDANCES
7	BOM	BILL OF MATERIAL

Realistic View

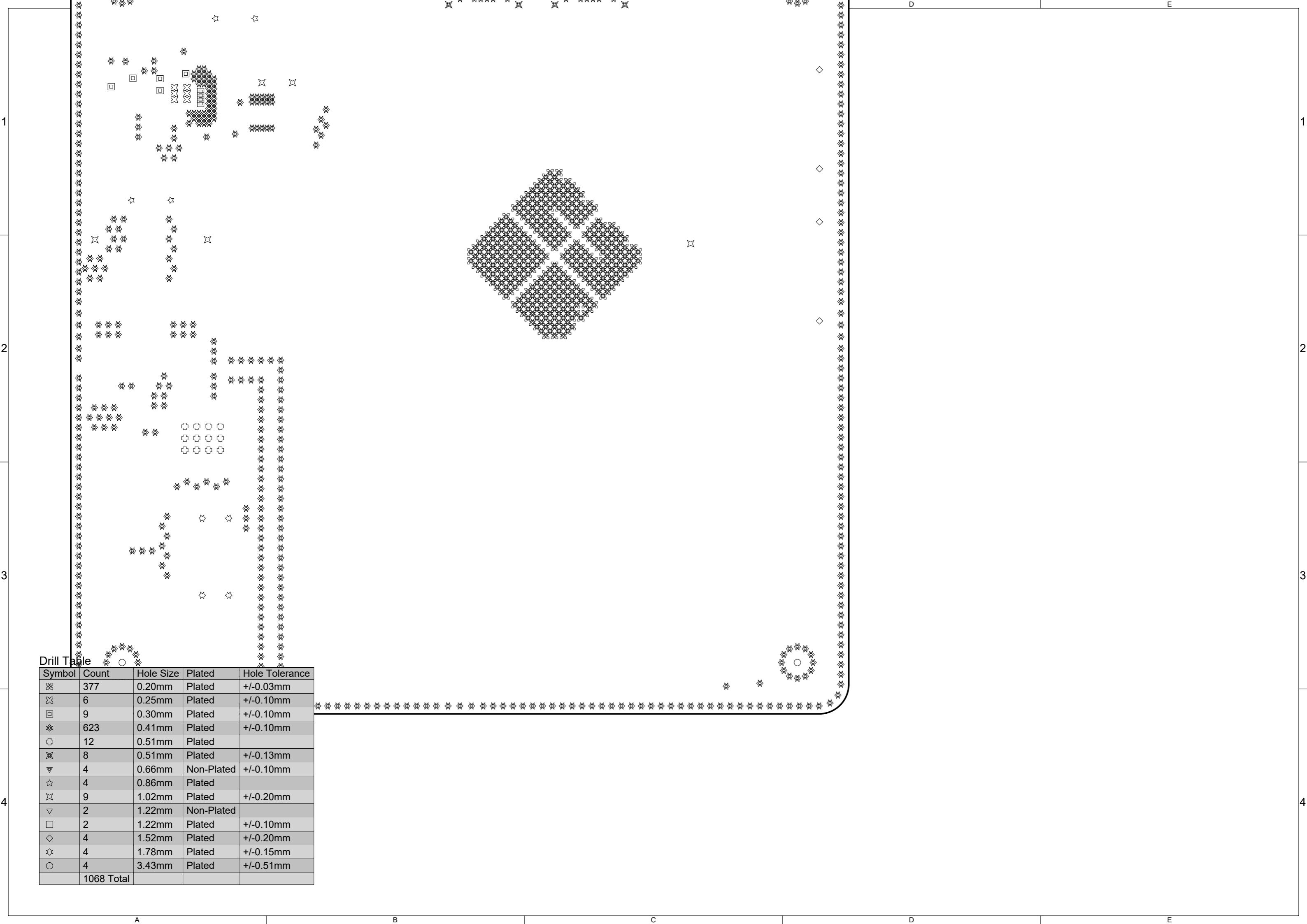






A	B	C	D	E																																																																																																												
1	<p>Layer Stack Legend</p> <table> <thead> <tr> <th>Material</th> <th>Layer</th> <th>Thickness</th> <th>Dielectric Material</th> <th>Type</th> <th>Gerber</th> </tr> </thead> <tbody> <tr> <td>Top Overlay</td> <td>Top Overlay</td> <td>0.40mil(0.010mm)</td> <td>Solder Resist</td> <td>Legend</td> <td>GTO</td> </tr> <tr> <td>Surface Material</td> <td>Top Solder</td> <td>0.40mil(0.010mm)</td> <td>Solder Mask</td> <td>GTS</td> </tr> <tr> <td>Copper</td> <td>Top Layer</td> <td><b>0.69mil(0.018mm)</b></td> <td>Signal</td> <td>GTL</td> </tr> <tr> <td>Prepreg</td> <td></td> <td>4.00mil(0.102mm)</td> <td>2116</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>GND 1</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G1</td> </tr> <tr> <td>Core</td> <td></td> <td>9.40mil(0.239mm)</td> <td>FR4-TG150</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>Signal 1</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G2</td> </tr> <tr> <td>Prepreg</td> <td></td> <td>7.00mil(0.178mm)</td> <td>7628</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>GND 2</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G3</td> </tr> <tr> <td>Core</td> <td></td> <td>9.40mil(0.239mm)</td> <td>FR4-TG150</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>Signal 2</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G4</td> </tr> <tr> <td>Prepreg</td> <td></td> <td>7.00mil(0.178mm)</td> <td>7628</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>PWR</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G5</td> </tr> <tr> <td>Core</td> <td></td> <td>9.40mil(0.239mm)</td> <td>FR4-TG150</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>GND 3</td> <td><b>1.40mil(0.036mm)</b></td> <td>Signal</td> <td>G6</td> </tr> <tr> <td>Prepreg</td> <td></td> <td>4.00mil(0.102mm)</td> <td>2116</td> <td>Dielectric</td> <td></td> </tr> <tr> <td>Copper</td> <td>Bottom Layer</td> <td><b>0.69mil(0.018mm)</b></td> <td>Signal</td> <td>GBL</td> </tr> <tr> <td>Surface Material</td> <td>Bottom Solder</td> <td>0.40mil(0.010mm)</td> <td>Solder Resist</td> <td>Solder Mask</td> <td>GBS</td> </tr> <tr> <td></td> <td>Bottom Overlay</td> <td></td> <td>Legend</td> <td>GBO</td> </tr> </tbody> </table> <p>Total thickness: 60.78mil(1.544mm)</p>	Material	Layer	Thickness	Dielectric Material	Type	Gerber	Top Overlay	Top Overlay	0.40mil(0.010mm)	Solder Resist	Legend	GTO	Surface Material	Top Solder	0.40mil(0.010mm)	Solder Mask	GTS	Copper	Top Layer	<b>0.69mil(0.018mm)</b>	Signal	GTL	Prepreg		4.00mil(0.102mm)	2116	Dielectric		Copper	GND 1	<b>1.40mil(0.036mm)</b>	Signal	G1	Core		9.40mil(0.239mm)	FR4-TG150	Dielectric		Copper	Signal 1	<b>1.40mil(0.036mm)</b>	Signal	G2	Prepreg		7.00mil(0.178mm)	7628	Dielectric		Copper	GND 2	<b>1.40mil(0.036mm)</b>	Signal	G3	Core		9.40mil(0.239mm)	FR4-TG150	Dielectric		Copper	Signal 2	<b>1.40mil(0.036mm)</b>	Signal	G4	Prepreg		7.00mil(0.178mm)	7628	Dielectric		Copper	PWR	<b>1.40mil(0.036mm)</b>	Signal	G5	Core		9.40mil(0.239mm)	FR4-TG150	Dielectric		Copper	GND 3	<b>1.40mil(0.036mm)</b>	Signal	G6	Prepreg		4.00mil(0.102mm)	2116	Dielectric		Copper	Bottom Layer	<b>0.69mil(0.018mm)</b>	Signal	GBL	Surface Material	Bottom Solder	0.40mil(0.010mm)	Solder Resist	Solder Mask	GBS		Bottom Overlay		Legend	GBO	1
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Symbol	Count	Hole Size	Plated	Hole Tolerance
☒	377	0.20mm	Plated	+/-0.03mm
☒	6	0.25mm	Plated	+/-0.10mm
☐	9	0.30mm	Plated	+/-0.10mm
☒	623	0.41mm	Plated	+/-0.10mm
✚	12	0.51mm	Plated	
☒	8	0.51mm	Plated	+/-0.13mm
▼	4	0.66mm	Non-Plated	+/-0.10mm
☆	4	0.86mm	Plated	
☒	9	1.02mm	Plated	+/-0.20mm
▽	2	1.22mm	Non-Plated	
□	2	1.22mm	Plated	+/-0.10mm
◇	4	1.52mm	Plated	+/-0.20mm
☒	4	1.78mm	Plated	+/-0.15mm
○	4	3.43mm	Plated	+/-0.51mm
1068 Total				



A	B	C	D	E
1	Transmission Line Structure Table			
1	Impedance Id	Transmission Line	Target Impedance	Calculated Impedance
1	1	Coated Microstrip	50	50.00
1	2	Offset Stripline	50	50.00
1	3	Offset Stripline	50	50.00
1	4	Offset Stripline	50	50.00
1	5	Offset Stripline	50	50.00
1	6	Coated Microstrip	50	50.00
2		Trace layer	Wide Trace Width	Narrow Trace Width
2	1	Top Layer	0.19mm	0.19mm
2	2	Signal 1	0.14mm	0.14mm
2	3	GND 2	0.14mm	0.14mm
2	4	Signal 2	0.14mm	0.14mm
2	5	PWR	0.14mm	0.14mm
2	6	Bottom Layer	0.19mm	0.19mm
3		Reference layers	Substack	Clearance
3	1	GND 1	Board Layer Stack	0.13mm
3	2	GND 1,GND 2	Board Layer Stack	0.13mm
3	3	Signal 1,Signal 2	Board Layer Stack	0.13mm
3	4	GND 2,PWR	Board Layer Stack	0.13mm
3	5	Signal 2,GND 3	Board Layer Stack	0.13mm
3	6	GND 3	Board Layer Stack	0.13mm
4		Target Tolerance		
4	1	10%		
4	2	10%		
4	3	10%		
4	4	10%		
4	5	10%		
4	6	10%		



1 2 3 4 5 6 7 8

A

A

B

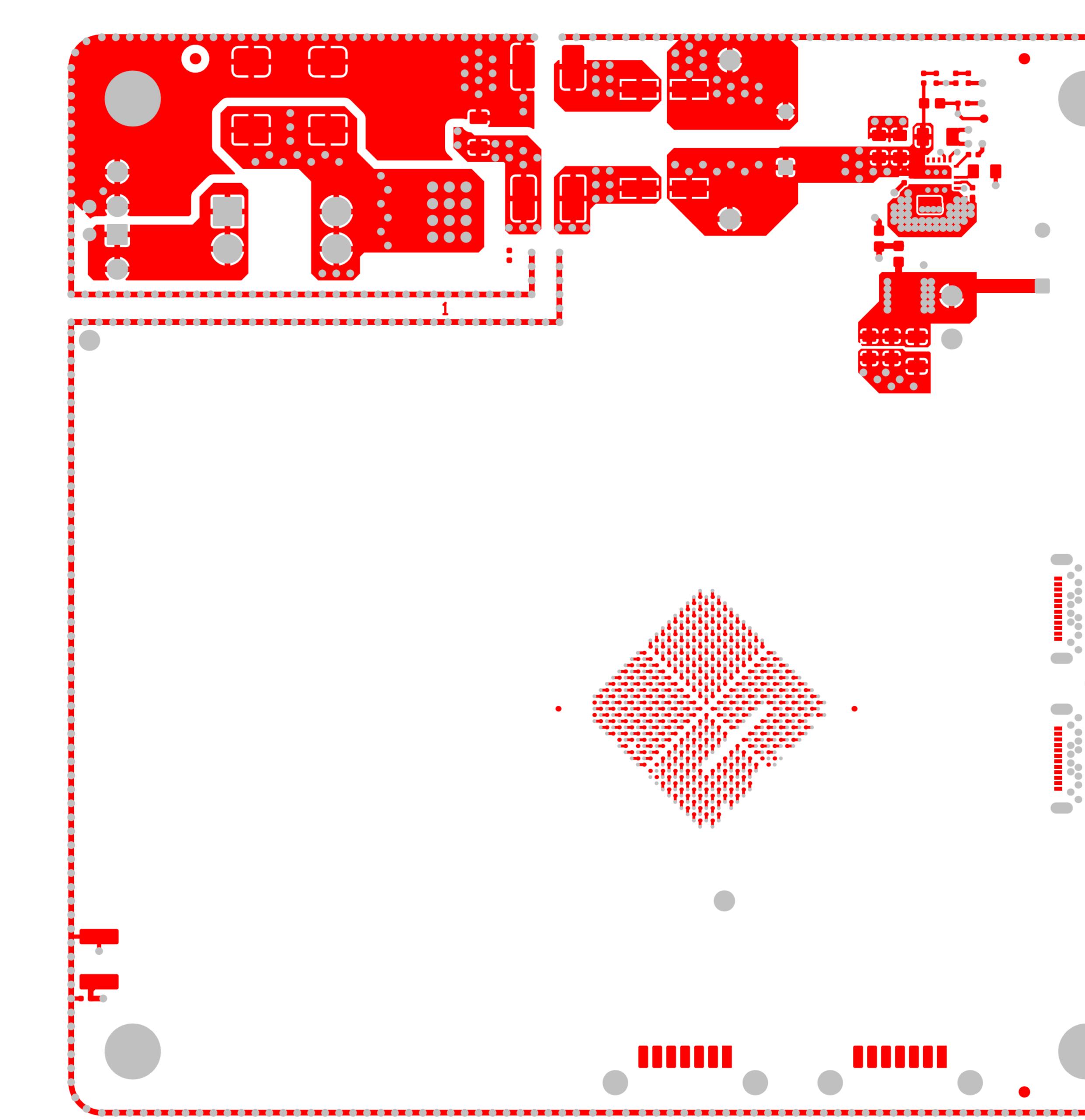
B

C

C

D

D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)			
PASCAL-EMMANUEL LACHANCE		TITLE: CRYPTUS DAUGHTERBOARD	
DATE: 2025-05-12	PART NO.: 1_A	REV. 1_A	SCALE:
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:		

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

A

A

B

B

C

C

D

D

ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

A

A

B

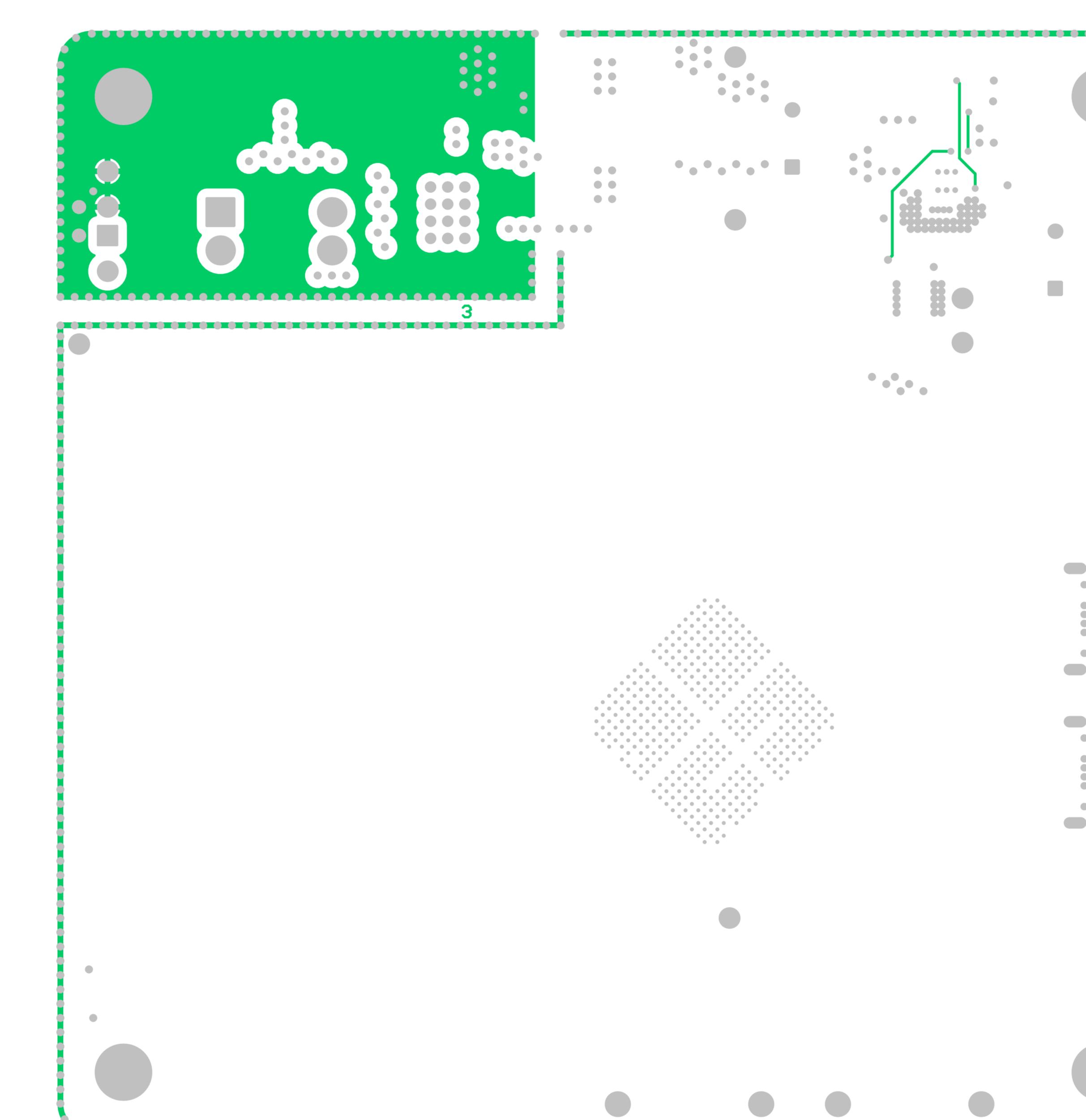
B

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D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

A

A

B

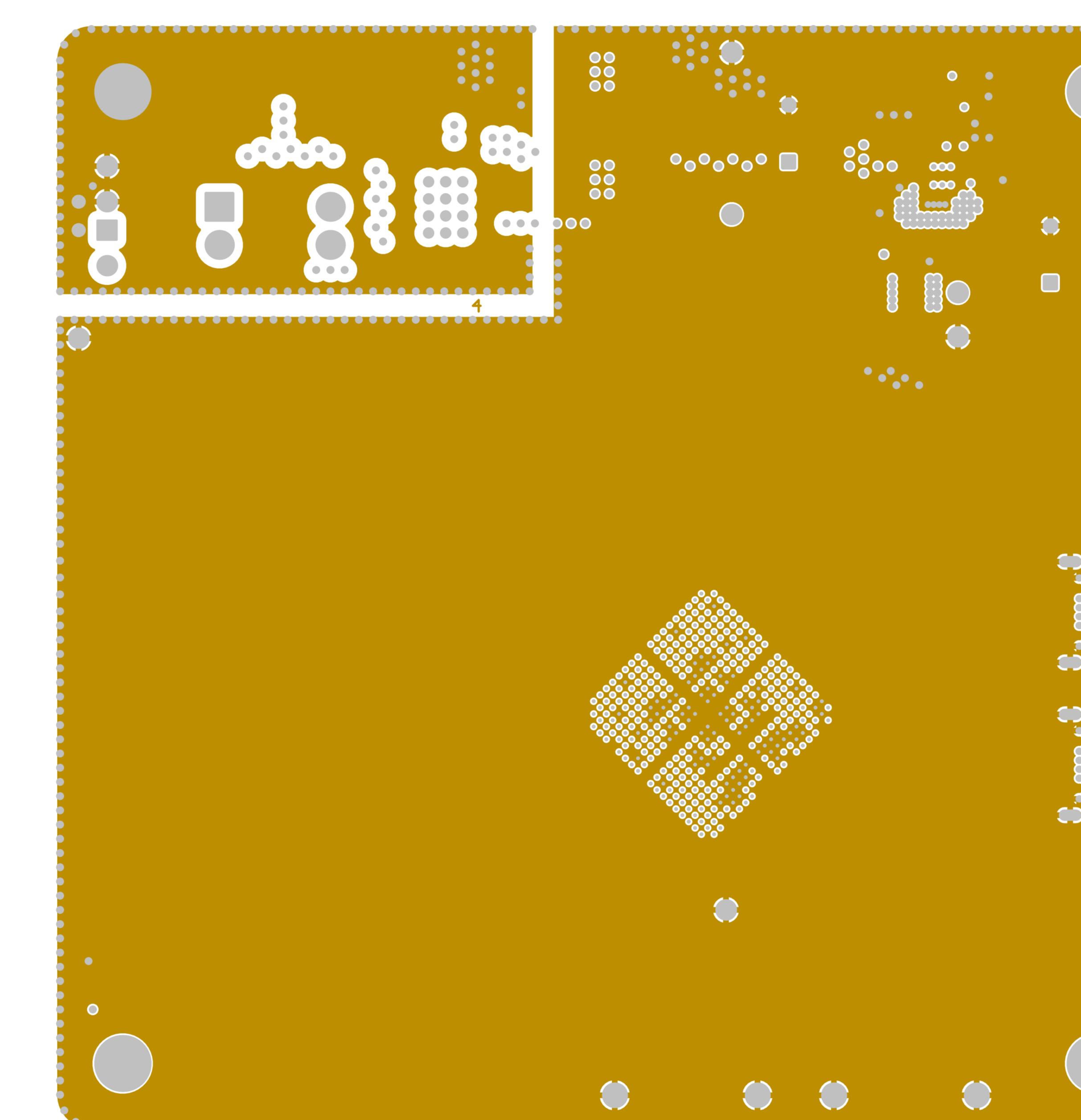
B

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D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

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1 2 3 4 5 6 7 8

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A

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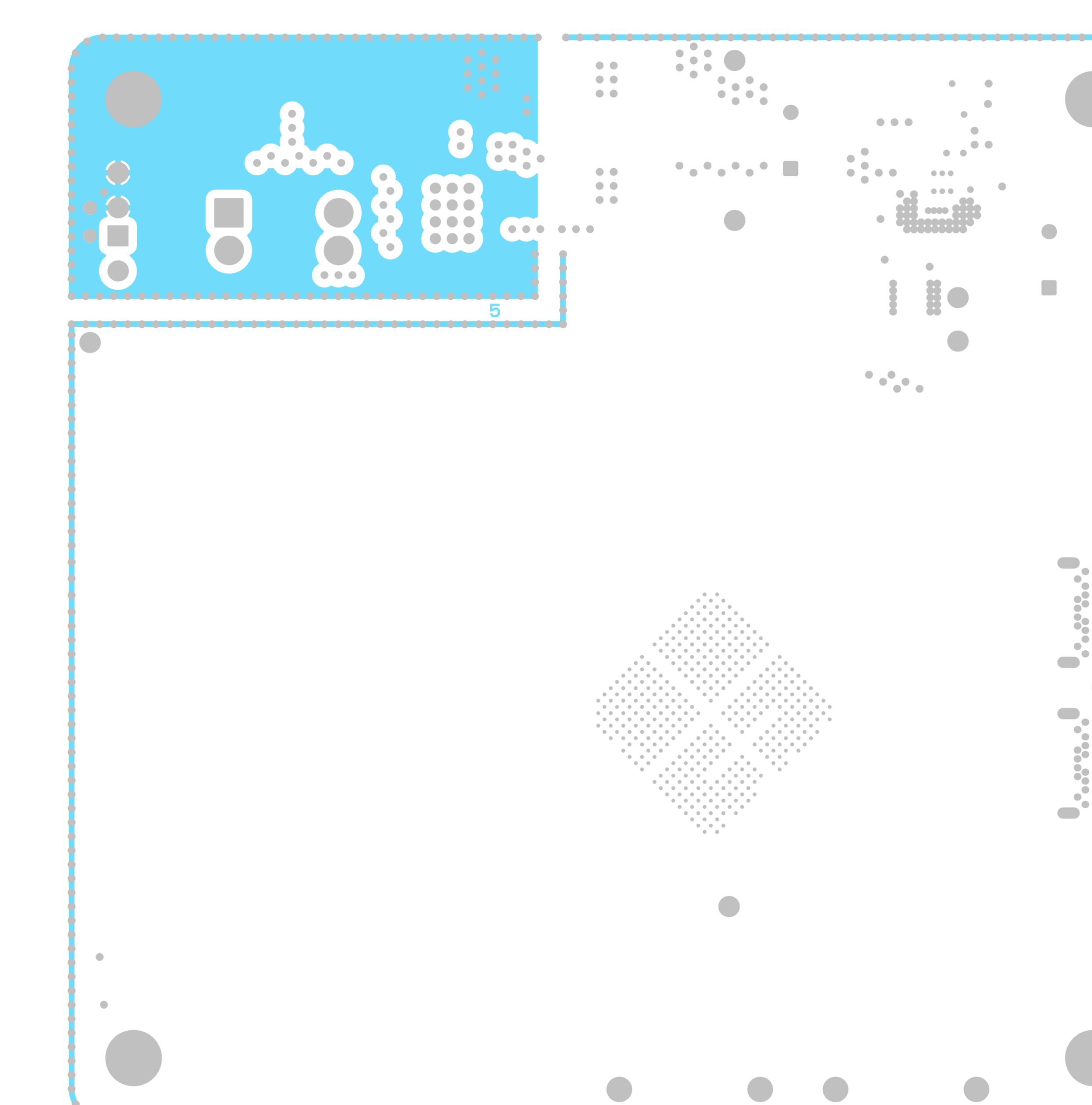
C

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ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)			
PASCAL-EMMANUEL LACHANCE	TITLE:	<b>CRYPTUS DAUGHTERBOARD</b>	
DATE: 2025-05-12	PART NO.:	1_A	
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:	

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

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A

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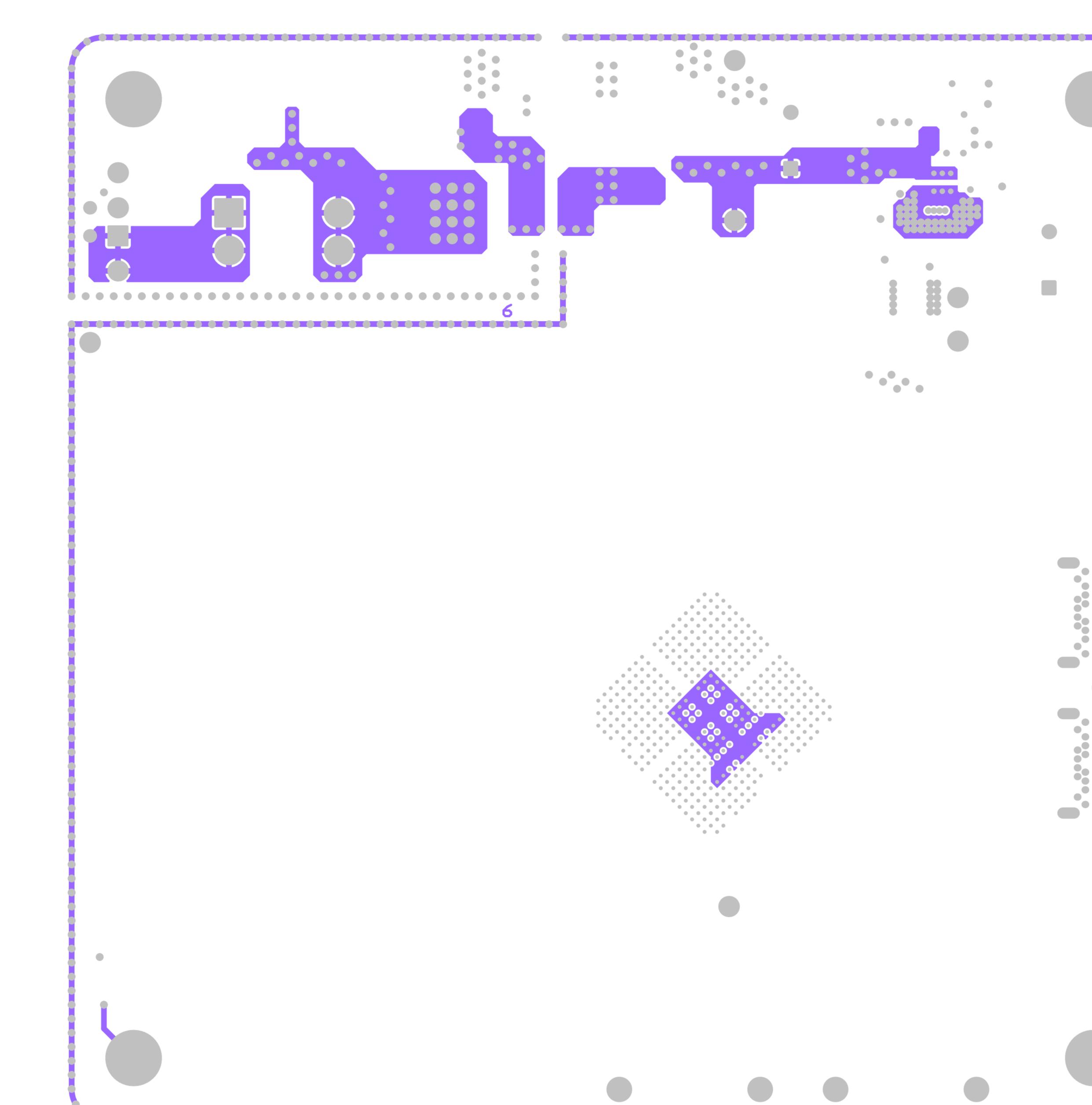
C

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ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)			
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DATE: 2025-05-12	PART NO.:		REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:		SCALE:

1 2 3 4 5 6 7 8

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ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1 2 3 4 5 6 7 8

1 2 3 4 5 6 7 8

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A

B

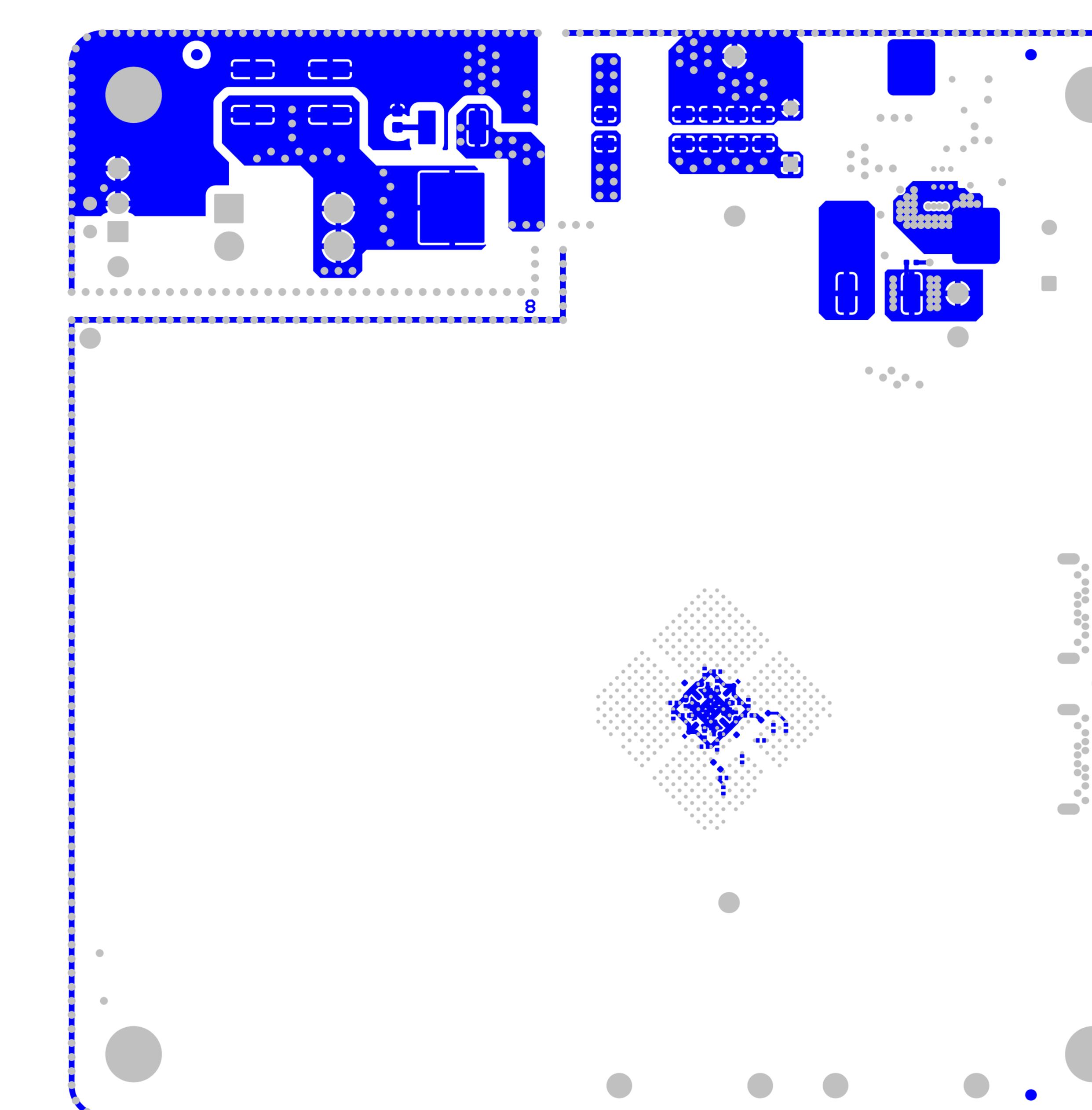
B

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ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1 2 3 4 5 6 7 8

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ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		TITLE: CRYPTUS DAUGHTERBOARD	
		PASCAL-EMMANUEL LACHANCE	
DATE:	2025-05-12	PART NO.:	REV. 1_A
FILE NAME:	CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

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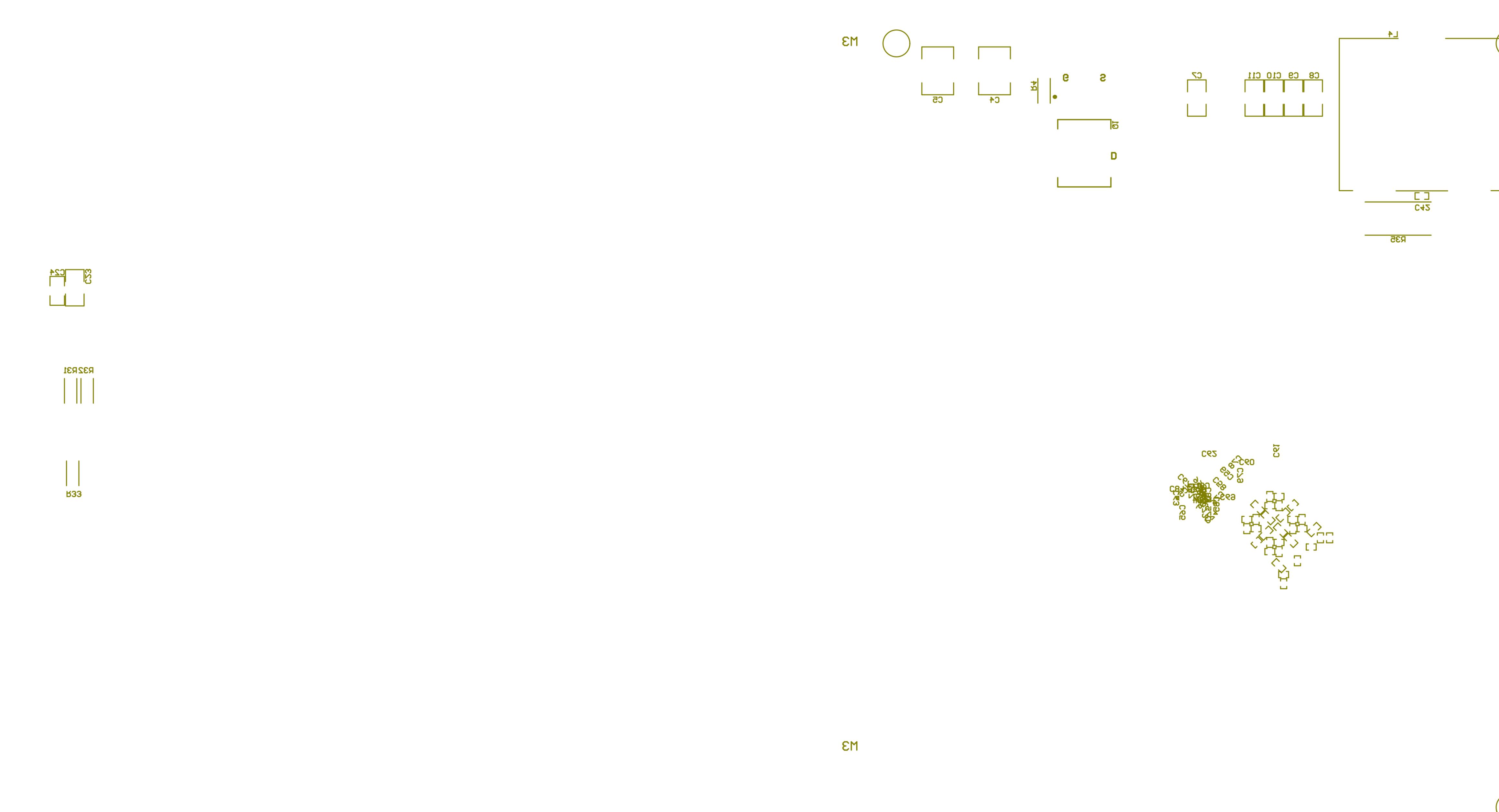
1 2 3 4 5 6 7 8

A A

B B

C C

D D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)			
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS	
		DAUGHTERBOARD	
DATE: 2025-05-12	PART NO.:		REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:		SCALE: 1:1

1 2 3 4 5 6 7 8

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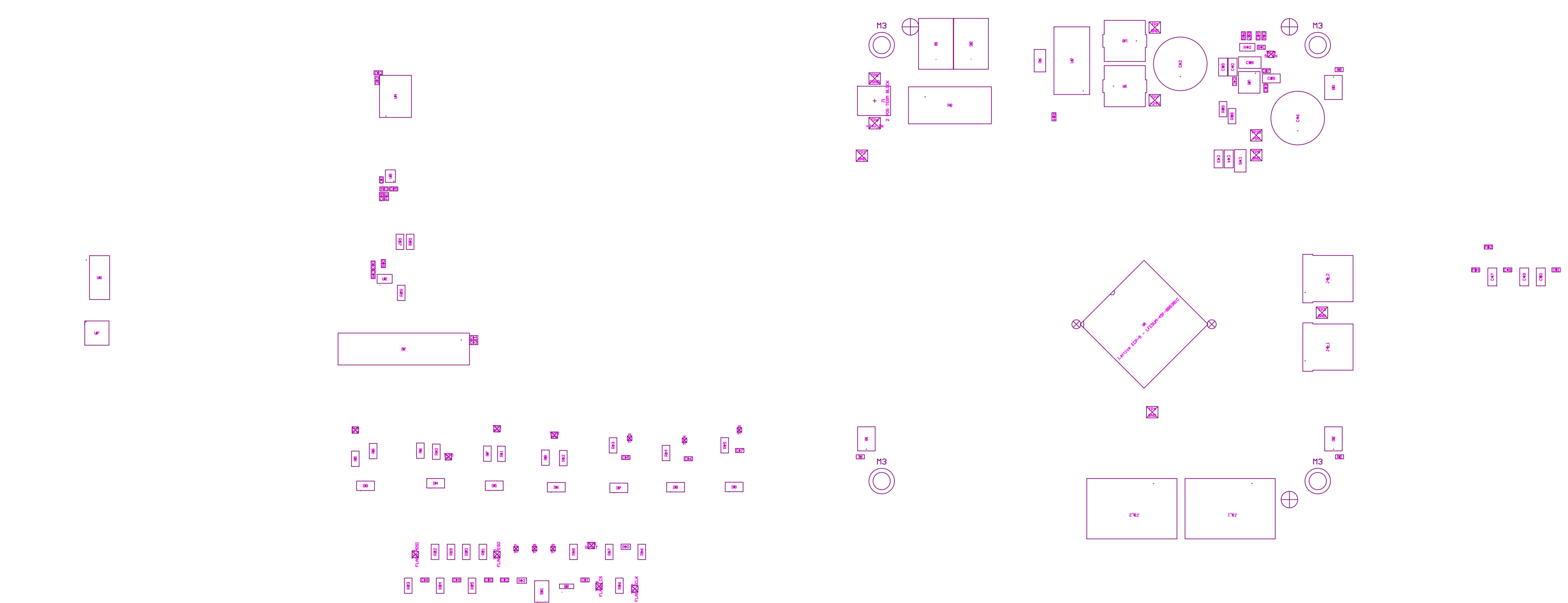
B

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D

D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1

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1 2 3 4 5 6 7 8

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A

B

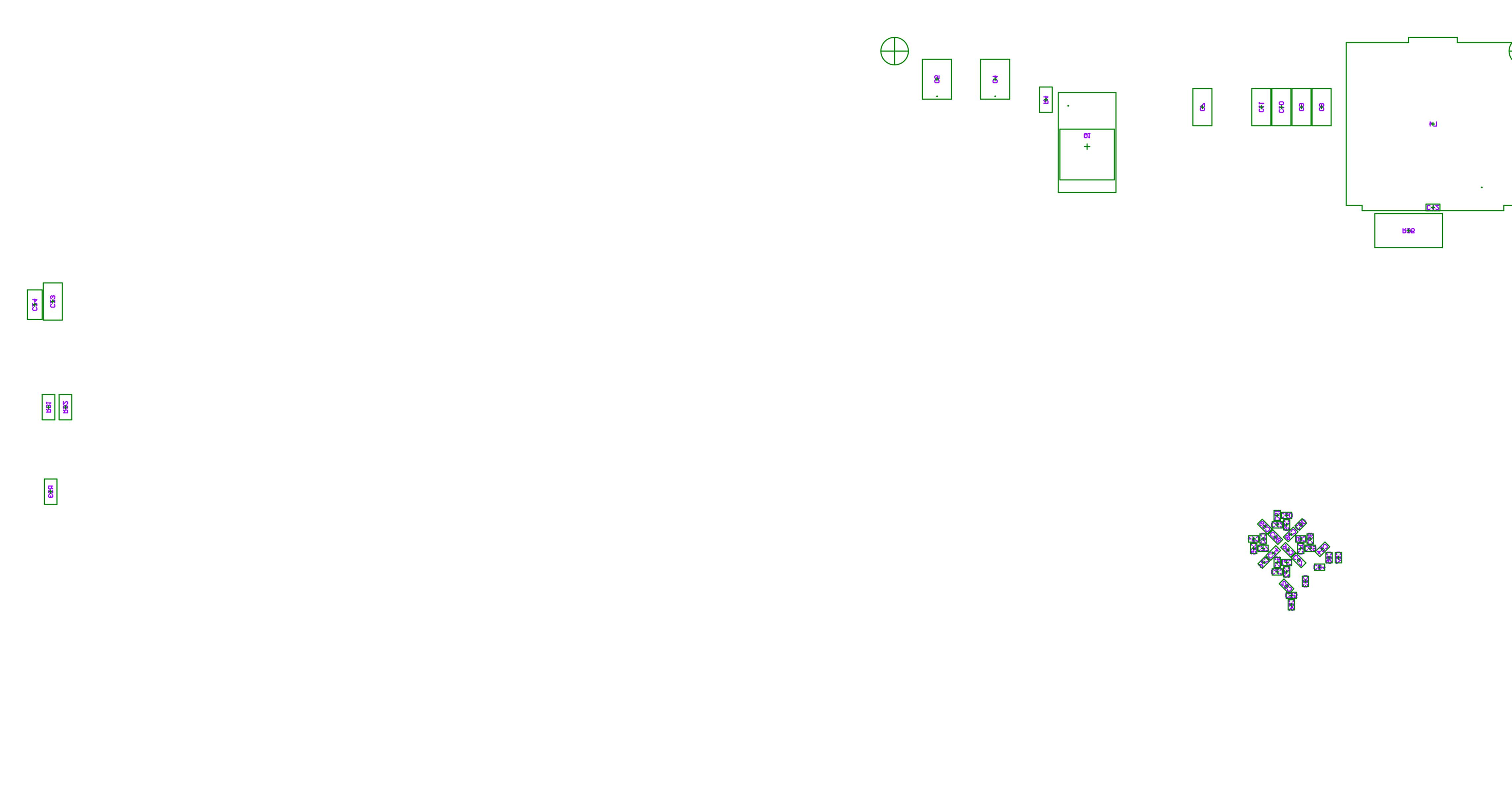
B

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D



ALL DIMENSIONS ARE IN THOUSANDS OF INCHES (MILS)		
PASCAL-EMMANUEL LACHANCE	TITLE:	CRYPTUS DAUGHTERBOARD
DATE: 2025-05-12	PART NO.:	REV. 1_A
FILE NAME: CryptUS_RevB_PCB.PcbDoc	DWG NO.:	SCALE:

1 2 3 4 5 6 7 8





Symbol	Count	Hole Size	Plated	Hole Tolerance
✖	377	7.87mil <0.200mm)	PTH	+/-1.00mil
◇	6	10.00mil (0.254mm)	PTH	+/-4.00mil
▣	9	12.00mil (0.305mm)	PTH	+/-4.00mil
✳	623	16.00mil (0.406mm)	PTH	+/-4.00mil
○	12	20.00mil (0.508mm)	PTH	+/-5.00mil
◎	8	20.00mil (0.508mm)	PTH	+/-5.00mil
▼	4	26.00mil (0.660mm)	NPTH	+/-4.00mil
☒	4	34.00mil (0.864mm)	PTH	+/-4.00mil
❖	9	40.00mil (1.016mm)	PTH	+/-8.00mil
▽	2	48.00mil (1.219mm)	NPTH	+/-4.00mil
□	2	48.00mil (1.219mm)	PTH	+/-4.00mil
✖	4	60.00mil (1.524mm)	PTH	+/-8.00mil
✳	4	70.00mil (1.778mm)	PTH	+/-6.00mil
○	4	135.00mil (3.429mm)	PTH	+/-20.00mil
	1068 Total			

Slot definitions : Routed Path Length = Calculated from tool start centre point to end centre point  
Hole Length = Routed Path Length + Tool Size = Slot length + Tool diameter

