



RT58x Thread SDK User Guide

V1.1

Table of Contents

1.	Introdu	uction	3
2.	Systen	n architecture	3
3.	Thread	SDK introduction	4
4.	Thread	d Application introduction	5
	4.1	Initaial	5
	4.2	Command register	5
	4.3	application process action	5
	4.4	Software timer	6
	4.5	Memory Manage	7
	4.6	Openthread API	7
5.	Comm	only commands	7
	5.1	Openthread Commands	7
	5.2	Rafael User Commands	10
6.	Start T	hread network	11
7.	Config	uration	11
	7.1	Project Configuration	12
	7.2	Main Configuration	12
8.	Sniffer		13
	8.1	Download	13
	8.2	Download sniffer bin	13
	8.3	Installation	14
	8.4	Install pyspinel package	14
	8.5	Interface in wireshark	15
	8.6	Options setting in wireshark	16
	8.7	Thread configure setting in wireshark	

Security Level < Confidential >



9.	OTA upgrade		
	9.1	Process	20
	9.2	OTA download tool	21
	9.3	OTA Command	24
Rev	ision l	History	25

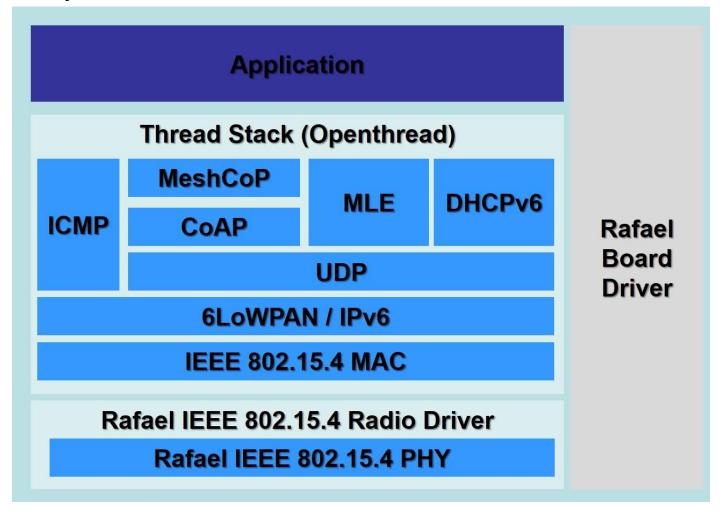


1. Introduction

This document describes the interface provided by the Rafael Thread Stack Library. It includes a reference software and supporting library for the Rafael Thread SDK, which combines the open-source project Openthread with Rafael IEEE 802.15.4 and Rafael Driver.

The Rafael Thread Library is easy to integrate with Rafael RT58x series SDK.

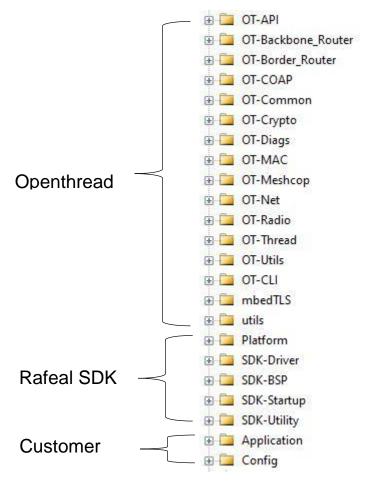
2. System architecture





3. Thread SDK introduction

This section provides an overview of the file distribution within the Rafael Thread SDK specifically related to Keil operations.





4. Thread Application introduction

This section introduces the examples and APIs included in the Rafael Thread SDK.

Pro	ject\Application\Thread\ftd
	app.c
	app.h
	main_ftd.c
	uart.cpp

4.1 Initaial

This function initializes the parameters of a thread network.

static void _Set_Network_Configuration()

This function initializes the parameters of the sleep node.

void _Sleep_Init()

This function configures the UDP settings.

void _Udp_Init()

This function initializes the OTA.

ota_init(g_app_instance)

4.2 Command register

This function registers the command

- static const otCliCommand kCommands[] = {name, callback}
 - 1. name: registers command name
 - 2. callback: command callback

4.3 application process action

This function includes the process of OpenThread tasks and the process of Rafael's driver.

- void _app_process_action()
 - otTaskletsProcess(g_app_instance)
 - otSysProcessDrivers(g app instance)

Customers can independently add their own task-processing procedures.



4.4 Software timer

Software timer create

- sw_timer_t *sw_timer_create(const char *name, uint32_t period, uint32_t auto_reload, uint32_t execute_mode, void *cb_param, sw_timer_cb cb_function)
- 1. name: A human readable text name that is assigned to the timer.
- 2. period: The period of the timer.
- 3. *auto_reload*: If auto_reload is set to TRUE, then the timer will expire repeatedly with a frequency set by the period parameter. If auto_reload is set to FALSE, then the timer will be a one-shot and enter the dormant state after it expires.
- 4. *execute_mode*: The execute mode of the timer.
- 5. *cb_param*: The input parameter of the call back function.
- 6. *cb_function*: The function to call when the timer expires

Software timer start

- sw_timer_err_t sw_timer_start(sw_timer_t *timer)
- 1. *timer*: The handle of the timer being started/restarted.

Software timer stop

- sw_timer_err_t sw_timer_stop(sw_timer_t *timer)
- 1. timer: The handle of the timer being stopped.

Software timer reset

- sw timer err t sw timer reset(sw timer t *timer)
- 1. *timer*: The handle of the timer being reset/started/restarted.

Software timer change period

- sw_timer_err_t sw_timer_change_period(sw_timer_t *timer, uint32_t period)
- 1. timer: The handle of the timer that is having its period changed.
- 2. *period*: The new period for the timer.

Software timer change execute mode

- sw_timer_err_t sw_timer_change_execute_mode(sw_timer_t *timer, uint32_t execute_mode)
- 1. *timer*: The handle of the timer that has its execute mode.
- execute_mode: The new execute mode for the timer.



Software timer delete

- sw_timer_err_t sw_timer_delete(sw_timer_t *timer)
- 1. *timer*: The handle of the timer being deleted.

Software timer get running

- bool sw_timer_get_running(sw_timer_t *timer)
- 1. *timer*: The timer beging queried.
- 2. return: ture= is active. false = is dormant.

4.5 Memory Manage

Memory allocation

- void * mem malloc (uint32 t u32 size)
- 1. *u32_size* : memory size in bytes.
- 2. return: NULL=allocate fail, memory pointer=allocate success.

Memory Free

- void mem_free (void * ptr)
- 1. *ptr*: allocate memory pointer.

Memory Copy

- void mem_memcpy (void *dest_ptr, void* src_ptr, uint32_t lens)
- 1. dest_ptr: target copy memory pointer.
- 2. src_ptr: original memory pointer that is being copied.
- 3. lens: copy size.

4.6 Openthread API

Please refer to the OpenThread API.

5. Commonly commands

5.1 Openthread Commands

state

Return state of current state.

> state

offline, disabled, detached, child, router or leader

Done



channel

Get the IEEE 802.15.4 Channel value.

Note: SUG-G 915M(1-10), 868M(1-14), 433M(1-4), 470M (1-20), 2.4G(11-26)

> channel

11 Done

SUG-G					
915M		868M	433M	470M	
1.	920000 KHz	1. 863000 KHz	1. 433000 KHz	1. 470000 KHz	
2.	920500 KHz	2. 863500 KHz	2. 433500 KHz	2. 472000 KHz	
3.	921000 KHz	3. 864000 KHz	3. 434000 KHz	3. 474000 KHz	
4.	921500 KHz	4. 864500 KHz	4. 434500 KHz	4. 476000 KHz	
5.	922000 KHz	5. 865000 KHz		5. 478000 KHz	
6.	922500 KHz	6. 865500 KHz		6. 480000 KHz	
7.	923000 KHz	7. 866000 KHz		7. 482000 KHz	
8.	923500 KHz	8. 866500 KHz		8. 484000 KHz	
9.	924000 KHz	9. 867000 KHz		9. 486000 KHz	
10.	924500 KHz	10. 867500 KHz		10. 488000 KHz	
		11. 868000 KHz		11. 490000 KHz	
		12. 868500 KHz		12. 492000 KHz	
		13. 869000 KHz		13. 494000 KHz	
		14. 869500 KHz		14. 496000 KHz	
				15. 498000 KHz	
				16. 500000 KHz	
				17. 502000 KHz	
				18. 504000 KHz	
				19. 506000 KHz	
				20. 508000 KHz	
	2.4G				
11.	2405 MHz	15. 2425 MHz	19. 2445 MHz	23. 2465 MHz	
12.	. 2410 MHz	16. 2430 MHz	20. 2450 MHz	24. 2470 MHz	
13.	. 2415 MHz	17. 2435 MHz	21. 2455 MHz	25. 2475 MHz	
14.	2420 MHz	18. 2440 MHz	22. 2460 MHz	26. 2480 MHZ	



panid

Get the IEEE 802.15.4 PAN ID value.

> panid

0xdead

Done

networkkey

Get the Thread Network Key value.

> networkkey

00112233445566778899aabbccddeeff

Done

thread start

Enable Thread protocol operation and attach to a Thread network.

> thread start

Done

thread stop

Disable Thread protocol operation and detach from a Thread network.

> thread stop

Done

ipaddr

List all IPv6 addresses assigned to the Thread interface.

> ipaddr

fdde:ad00:beef:0:0:ff:fe00:0

fdde:ad00:beef:0:558:f56b:d688:799

fe80:0:0:0:f3d9:2a82:c8d8:fe43

Done

ping [async] [-I source] <ipaddr> [size] [count] [interval] [hoplimit] [timeout] Send an ICMPv6 Echo Request.

async: Use the non-blocking mode. New commands are allowed before the ping process terminates.

source: The source IPv6 address of the echo request.

size: The number of data bytes to be sent; Limit size: 1280 bytes.



count: The number of ICMPv6 Echo Requests to be sent.

interval: The interval between two consecutive ICMPv6 Echo Requests in

seconds. The value may have fractional form, for example 0.5.

hoplimit: The hoplimit of ICMPv6 Echo Request to be sent.

timeout: Time in seconds to wait for the final ICMPv6 Echo Reply after sending out the request. The value may have fractional form.

- > ping fd00:db8:0:0:76b:6a05:3ae9:a61a
- > 16 bytes from fd00:db8:0:0:76b:6a05:3ae9:a61a: icmp_seq=5 hlim=64 time=0ms
- 1 packets transmitted, 1 packets received. Packet loss = 0.0%. Round-trip min/avg/max = 0/0.0/0 ms.

Done

- > ping -I fd00:db8:0:0:76b:6a05:3ae9:a61a ff02::1 100 1 1 1
- > 108 bytes from fd00:db8:0:0:f605:fb4b:d429:d59a: icmp_seq=4 hlim=64 time=7ms
- 1 packets transmitted, 1 packets received. Round-trip min/avg/max = 7/7.0/7 ms. Done

Udp send <ip> <port> <message>

Send a UDP message.

ip: the destination address.

port: the UDP destination port.

message: the message to send; Limit size: 640 characters.

> udp send fdde:ad00:beef:0:bb1:ebd6:ad10:f33 1234 hello

Done

For more details about OpenThread commands, please refer.

5.2 Rafael User Commands

euiset

• Set the device eui64 value.

> euiset 11223344556677

Done



6. Start Thread network

- 1. Prepare two boards with the flashed Example (Thread_2P4G.bin or Thread_SubG).
- 2. Open a terminal (Tera Term)
- 3. Connect to the used COM port with the following direct UART settings:
 - Baud rate: 115200
 - 8 data bits
 - 1 stop bit
 - No parity
 - Flow control: none
- 4. Check the board 1 role

> state

leader

Done

5. Check the board 2 role

> state

child

Done

6. Check the board 2 IP

> ipaddr

fdde:ad00:beef:0:0:ff:fe00:0

fdde:ad00:beef:0:558:f56b:d688:799

fe80:0:0:0:f3d9:2a82:c8d8:fe43

Done

- 7. Use board 1 to ping board 2
- > ping fdde:ad00:beef:0:558:f56b:d688:799
- > 16 bytes from ping fdde:ad00:beef:0:558:f56b:d688:799: icmp_seq=5 hlim=64 time=0ms
- 1 packets transmitted, 1 packets received. Packet loss = 0.0%. Round-trip min/avg/max = 0/0.0/0 ms.

Done

8. To modify Thread network parameters, you can either use code to programmatically modify the parameters or use commands through the command line or configuration interface.

7. Configuration

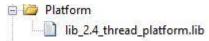


7.1 Project Configuration

PLAFFORM CONFIG ENABLE SUBG

If defined, enable SUG-G; otherwise, default to using 2.4GHz.

Note: Use the corresponding library for the platform.



7.2 Main Configuration

RFB_DATA_RATE

Set SUG-G data rate value; supported Value: [FSK_50K; FSK_100K; FSK_150K; FSK_200K; FSK_300K].

RFB CCA THRESHOLD

Set clear channel assessment (CCA) threshold value; Default: 75 (-75 dBm)

RFB SUBG FREQUENCY BAND

Set SubG frequency band value; Default: 0, (0: 915M, 1: 868M, 2: 433M, 3: 470M)



8. Sniffer

8.1 Download

Python

https://www.python.org/downloads/release/python-379/

Wireshark

https://www.wireshark.org/download.html

8.2 Download sniffer bin

The bin file in the path "pyspinel /RafaelMicroSinfferBin".



8.3 Installation

Automatic installation from source

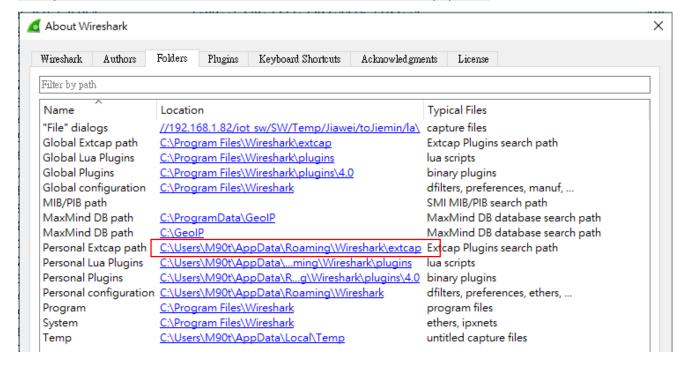
- \$ git clone https://github.com/RafaelMicro/pyspinel
- \$ cd pyspinel
- \$ python3 setup.py install --extcap-path=<extcap path>

Automatic install from PYPI

\$ pip3 install pyspinel --install-option="--extcap-path=<extcap_path>"

It is referred to as <extcap_path> in the following sections.

"Help" -> "About Wireshark" -> "Folders" -> "Extcap path"



8.4 Install pyspinel package

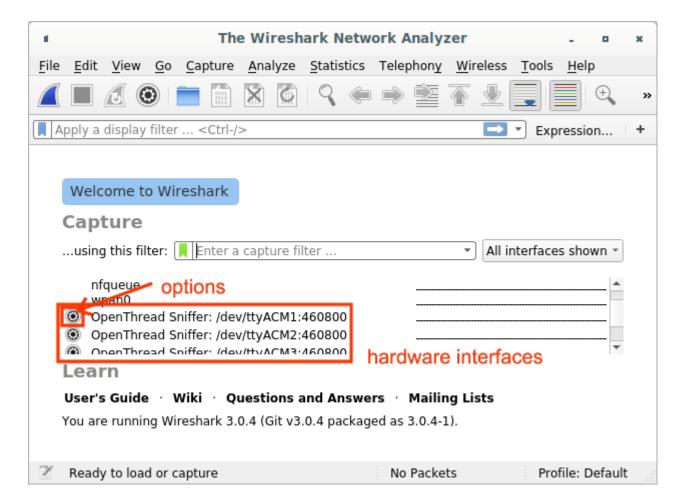
\$ pip3 install pyspinel

Copy the provided **extcap_ot.py** and **extcap_ot.bat** to the extcap directory.



8.5 Interface in wireshark

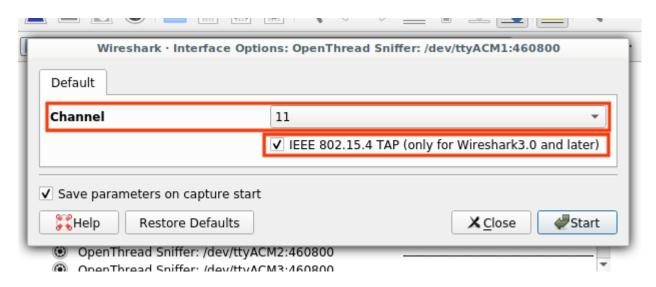
If this is your first time using an interface, click the **Options** button to the left of the interface. Otherwise, "capture"->"refresh interfaces" to find the interface.





8.6 Options setting in wireshark

- Set the **Channel** to the desired value.
- Check IEEE 802.15.4 TAP to ensure that the channel information is included in the pcap output and can be displayed in the Wireshark GUI.
- Check Save parameters on capture start to ensure that these parameters are saved after the start of the capture, to avoid having to set it again the next time you use the interface (unless you need to change the channel).
- Click Start.

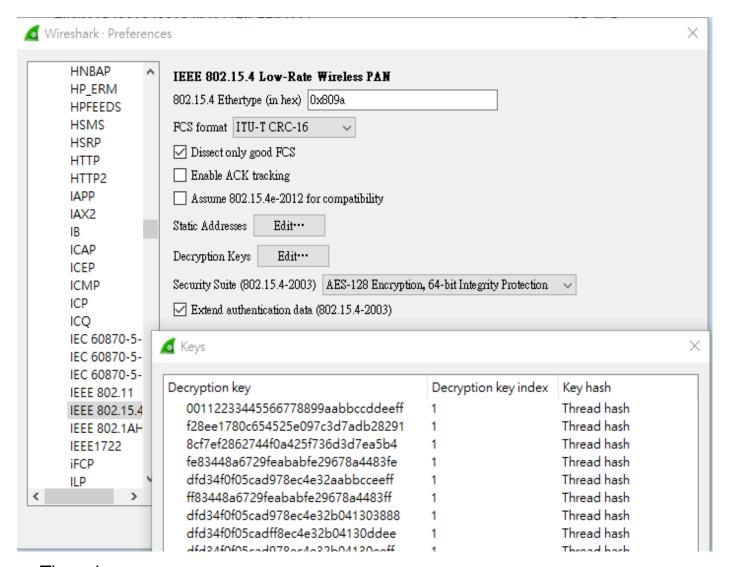


8.7 Thread configure setting in wireshark

To configure protocols, select Preferences... in Wireshark and expand the Protocols section.

- IEEE 802.15.4
 - 1. Click + to add a **Decryption key**.
 - 2. Enter the Thread network Master Key into the Decryption key column.
 - 3. Enter "1" as the Decryption key index.
 - 4. Select Thread hash from the **Key hash** column listbox.

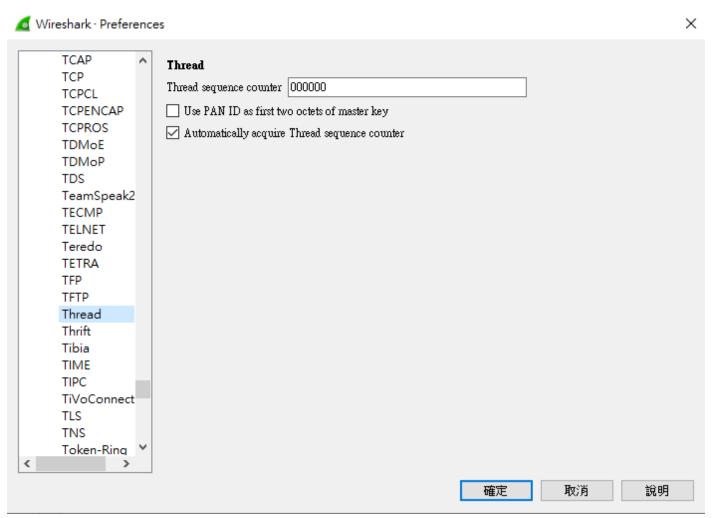




Thread

- 1. Enter "00000000" for the Thread sequence counter.
- 2. Uncheck Use PAN ID as first two octets of master key.
- 3. Check Automatically acquire Thread sequence counter.





- CoAp
 - 1. Enter UDP port "61631".
 - 2. TCP port "5683".



	×
CFLOW CFP Chargen CHDLC CIGI CIMD CIP (S) 5683 CIP (CIP I/O CIP Motion CISCO3 ERSF CLDAP CN/IP CMP CN/IP CoAP collectd CommunityIE Components COPS COROSYNC/COTP Couchbase CP2179	說明



9. OTA upgrade

9.1 Process

- 1. Use the IoT_EVALUATION_TOOL tool to compress the binary file.
- 2. Use the OTA download tool to transmit the updated binary file (compressed Bin file) to the RT58x via UART1.
- 3. Once the tool update is complete, use the OTA command to initiate the update process.
- 4. After the module update is finished, the RT58x will automatically reboot and load the new bin file.

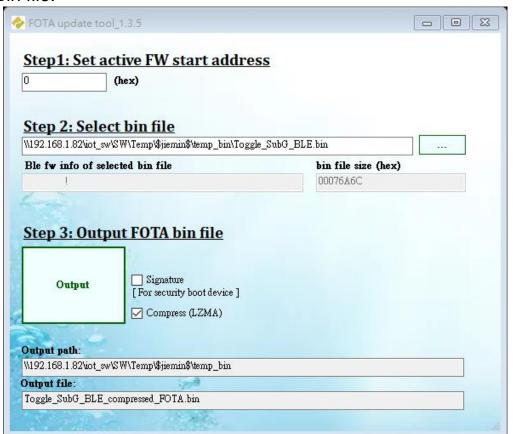


9.2 IoT_EVALUATION_TOOL tool

1. Open this tool and select "FOTA" from the options.



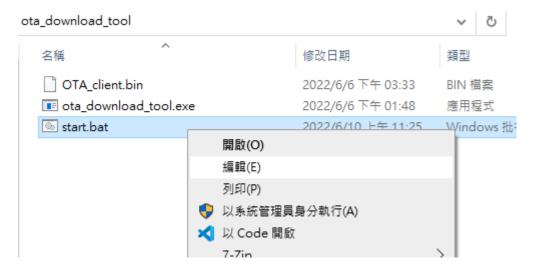
2. Set the FW start address to 0 and click the "Output" button to get the compressed Bin file.



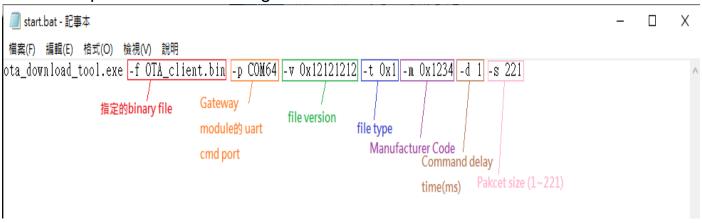


9.3 OTA download tool

1. Edit Start.bat



2. Set the parameters according to the actual situation.



3. Enable start.bat





4. Wait for the download to complete.

5. Download to complete.

```
C:\WINDOWS\system32\cmd.exe — X

C:\Users\M720t\Desktop\ota_download_tool>ota_download_tool.exe -f OTA_client.bin -p COM64 -v 0x12121212 -t 0x1 -m 0x1234

-d 1 -s 221

Select File : OTA_client.bin
Select Port : COM64
File Ver : 0x12121212

File Type : 0x1
Manufacturer : 0x1234
Delay Time : 1
Packet size : 221

file size 445824, ver 12121212, type 1 manufact 1234

[ ________ ] 100.00

C:\Users\M720t\Desktop\ota_download_tool>pause
請按任意鍵繼續 . . . ___
```



9.4 OTA Command

1. ota information command

> ota

ota image code: 1234

ota image version: 12121212

ota image size : XXXX ota image crc : XXXX

Done

- 2. ota start command (For 1 to many)
- segments_size : suggest using a 255-byte payload.
- interval: suggest a 2000- millisecond interval.
- > ota start <segments_size> <intervel>

Done

- 3. ota send command (For 1 to 1)
- ipaddr : destination IP address.
- > ota send <ipaddr>

Done



Revision History

Revision	Description	Owner	Date
V1.0	Initial version	Jiemin	2023/06/26
V1.1	Add SubG frequency band config	Jiemin	2024/02/20

© 2021 by Rafael Microelectronics, Inc.

All Rights Reserved.

Information in this document is provided in connection with **Rafael Microelectronics**, **Inc.** ("**Rafael Micro**") products. These materials are provided by **Rafael Micro** as a service to its customers and may be used for informational purposes only. **Rafael Micro** assumes no responsibility for errors or omissions in these materials. **Rafael Micro** may make changes to this document at any time, without notice. **Rafael Micro** advises all customers to ensure that they have the latest version of this document and to verify, before placing orders, that information being relied on is current and complete. **Rafael Micro** makes no commitment to update the information and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to its specifications and product descriptions.

THESE MATERIALS ARE PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESS OR IMPLIED, RELATING TO SALE AND/OR USE OF **RAFAEL MICRO** PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, CONSEQUENTIAL OR INCIDENTAL DAMAGES, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. **RAFAEL MICRO** FURTHER DOES NOT WARRANT THE ACCURACY OR COMPLETENESS OF THE INFORMATION, TEXT, GRAPHICS OR OTHER ITEMS CONTAINED WITHIN THESE MATERIALS. **RAFAEL MICRO** SHALL NOT BE LIABLE FOR ANY SPECIAL, INDIRECT, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, INCLUDING WITHOUT LIMITATION, LOST REVENUES OR LOST PROFITS, WHICH MAY RESULT FROM THE USE OF THESE MATERIALS.



Rafael Micro products are not intended for use in medical, lifesaving or life sustaining applications. Rafael Micro customers using or selling Rafael Micro products for use in such applications do so at their own risk and agree to fully indemnify Rafael Micro for any damages resulting from such improper use or sale. Rafael Micro, logos and RT568 are Trademarks of Rafael Microelectronics, Inc. Product names or services listed in this publication are for identification purposes only, and may be trademarks of third parties. Third-party brands and names are the property of their respective owners.