

The diagram illustrates two voltage regulation stages. The top stage, U2 (MCP73831/2 Lipo Charger), is configured with a USB input (UBUS) connected to its VDD pin. A red LED (RED) is connected to the CHG pin. The UBAT pin is connected to the battery output. The STAT pin is connected to a 1K resistor (R2) and a status pin (STAT). The PROG pin is connected to a 2K resistor (R5) and the VSS pin. The VBAT pin is connected to the battery output. The bottom stage, U3 (MIC5225-3.3YM5-T), is a 3.3V LDO regulator. Its IN pin is connected to the USB input (UBUS) through a 100K resistor (R1). The EN pin is connected to the IN pin. The OUT pin is connected to the 3.3V output. The GND pin is connected to ground. The output is filtered by a 10uF capacitor (C2) and a 1uF capacitor (C5). The input is filtered by a 10uF capacitor (C1). The output is also connected to a 10uF capacitor (C6) and a 10uF capacitor (C5).



Size: A4	R. R. Engineering				
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