

# Trabalho Pratico 2 - Cifra A5/1

## Grupo 27

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## Explicação do problema a)

Considerando a descrição da cifra A5/1, pretende-se definir e codificar, em Z3 e usando o tipo BitVec para modelar a informação, uma FSM que descreva o gerador de chaves.

```
from z3 import *
import random

def declare(i):
    return {
        'LFSR1': BitVec(f'LFSR1{i}', 19),
        'LFSR2': BitVec(f'LFSR2{i}', 22),
        'LFSR3': BitVec(f'LFSR3{i}', 23),
        'keys': BitVec(f'keys_{i}', 64), # Vetor de 64 bits
        'kepos': BitVec(f'kepos_{i}', 7)
    }

def init(state):
    z0 = random.getrandbits(19)
    z1 = random.getrandbits(22)
    z2 = random.getrandbits(23)

    return And(
        state['LFSR1'] == BitVecVal(z0, 19),
        state['LFSR2'] == BitVecVal(z1, 22),
        state['LFSR3'] == BitVecVal(z2, 23),
        state['keys'] == BitVecVal(0, 64), # Inicializado com zero
        state['kepos'] == BitVecVal(0, 7)
    )

def avanca(curr, bits_tapped):
    feed_bits = [Extract(i, i, curr) for i in bits_tapped]
    feedback = BitVecVal(0, 1)
```

```

    for bit in feed_bits:
        feedback ^= bit
    LFSR_deslocado = curr >> 1
    feed_deslocado = ZeroExt(curr.size() - 1, feedback) <<
    (curr.size() - 1)
    return LFSR_deslocado | feed_deslocado

def maioria_bit(control_1, control_2, control_3):
    return (control_1 & control_2) | (control_1 & control_3) |
    (control_2 & control_3)

def trans(curr, prox):
    control_1 = Extract(8, 8, curr['LFSR1'])
    control_2 = Extract(10, 10, curr['LFSR2'])
    control_3 = Extract(10, 10, curr['LFSR3'])
    maioria = maioria_bit(control_1, control_2, control_3)
    bits_tapped1 = [13, 16, 17, 18]
    bits_tapped2 = [20, 21]
    bits_tapped3 = [7, 20, 21, 22]

    next_LFSR1 = If(maioria == control_1, avanca(curr['LFSR1'],
bits_tapped1), curr['LFSR1'])
    next_LFSR2 = If(maioria == control_2, avanca(curr['LFSR2'],
bits_tapped2), curr['LFSR2'])
    next_LFSR3 = If(maioria == control_3, avanca(curr['LFSR3'],
bits_tapped3), curr['LFSR3'])

    new_bit = Extract(0, 0, next_LFSR1) ^ Extract(0, 0, next_LFSR2) ^
Extract(0, 0, next_LFSR3)
    updated_keys = (curr['keys'] << 1) | ZeroExt(63, new_bit)
    updated_pos = curr['kepos'] + 1

    return And(
        prox['LFSR1'] == next_LFSR1,
        prox['LFSR2'] == next_LFSR2,
        prox['LFSR3'] == next_LFSR3,
        prox['keys'] == updated_keys,
        prox['kepos'] == updated_pos,
    )

# (posições que estão a uma distância de no máximo 2 ** (t // 2) de i)

def print_state(i, model, state):
    LFSR1val = model[state['LFSR1']].as_long()
    LFSR2val = model[state['LFSR2']].as_long()
    LFSR3val = model[state['LFSR3']].as_long()

```





```
LFSR 0 - 1111100100010110111
LFSR 1 - 111111110011010001100
LFSR 2 - 1111111101101100101100
```

[illegible]

```
LFSR 0 - 1111110010001011011
LFSR 1 - 1111111110011010001100
LFSR 2 - 11111111110110110010110
```

[illegible]

```
LFSR 0 - 1111110010001011011
LFSR 1 - 1111111111001101000110
LFSR 2 - 11111111111011011001011
```

[illegible]

```
LFSR 0 - 1111111001000101101
LFSR 1 - 1111111111100110100011
LFSR 2 - 11111111111011011001011
```

[illegible]

```
LFSR 0 - 1111111100100010110
LFSR 1 - 1111111111110011010001
LFSR 2 - 11111111111011011001011
```

[illegible]

```
LFSR 0 - 1111111110010001011
LFSR 1 - 1111111111111001101000
LFSR 2 - 11111111111101101100101
```

[illegible]

```
LFSR 0 - 111111111001000101
LFSR 1 - 111111111111001101000
LFSR 2 - 1111111111110110110010
```

[illegible]





[illegible][illegible][illegible][illegible][illegible][illegible][illegible]

```
Estado 37:
LFSR 0 - 1111111111001000101
LFSR 1 - 11111111111111111111
```



LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 38:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 39:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 40:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 41:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 42:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 43:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0000000000000000000000000000111001001101001010010000111111111111

Estado 44:  
LFSR 0 - 111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111

Output -  
0000000000000000000011100100110100101001000011111111111111111111

Estado 45:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 46:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 47:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 48:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 49:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 50:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -

0000000000000000000011100100110100101001000011111111111111111111

Estado 51:

LFSR 0 - 111111111001000101

LFSR 1 - 11111111111111111111

LFSR 2 - 11111111111111111111

Output -



```
LFSR 0 - 1111111111001000101
LFSR 1 - 11111111111111111111
LFSR 2 - 11111111111111111111
Output - 
00000111001001101001010010000111111111111111111111111111111111
```

```
LFSR 0 - 1111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
000011100100110100101001000011111111111111111111111111111111
```

```
LFSR 0 - 1111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
000111001001101001010010000111111111111111111111111111111111
```

```
LFSR 0 - 1111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
001110010011010010100100001111111111111111111111111111111111
```

```
LFSR 0 - 1111111111001000101  
LFSR 1 - 11111111111111111111  
LFSR 2 - 11111111111111111111  
Output -  
0111001001101001010010000111111111111111111111111111111111111111
```

[illegible]

```

LFSR 0 - 1001000110101000100
LFSR 1 - 101001011001001111100
LFSR 2 - 11010100011110100010000
Output -

```

[illegible]

Estado 2:

LFSR 0 - 1100100011010100010

LFSR 1 - 1101001011001001111110

LFSR 2 - 11101010001111010001000

Output -

Estado 3:

```
LFSR 0 - 1110010001101010001
```

LFSR 1 - 1110100101100100111111

LFSR 2 - 11101010001111010001000

Output -

Estado 4:

```
LFSR 0 - 1111001000110101000
```

LFSR 1 - 1110100101100100111111

LFSR 2 - 11110101000111101000100

Output -

Estado 5:

LFSR 0 - 1111100100011010100

LFSR 1 - 1110100101100100111111

LFSR 2 - 11111010100011110100010

Output -

Estado 6:

LFSR 0 - 1111110010001101010

LFSR 1 - 1111010010110010011111

LFSR 2 - 11111010100011110100010

Output -

Estado 7:

```
LFSR 0 - 1111110010001101010
```

LFSR 1 - 1111101001011001001111

LFSR 2 - 11111101010001111010001

Output -

Estado 8:

LFSR 0 - 1111111001000110101

LFSR 1 - 1111101001011001001111

LFSR 2 - 11111110101000111101000

Output -

Estado 9:

LFSR 0 - 1111111100100011010

LFSR 1 - 1111101001011001001111

LFSR 2 - 11111111010100011110100

Output -

[illegible]