

 Design Properties

Name:

PROYECTO

Location:

N:\P2\PROYECTO

Working directory:

N:\P2\PROYECTO

Description:

Project Settings

Property Name	Value
Top-Level Source Type	HDL
Product Category	All
Family	Spartan3
Device	XC3S200
Package	FT256
Speed	-4
Synthesis Tool	XST (VHDL/Verilog)
Simulator	ISim (VHDL/Verilog)
Preferred Language	VHDL
Property Specification in Project File	Store all values
Manual Compile Order	<input type="checkbox"/>
VHDL Source Analysis Standard	VHDL-93
Enable Message Filtering	<input type="checkbox"/>

OK

Cancel

Help