## Medium ED: Low Vout 1 VDD VDD Circuit Diagram A: Medium Resistance: A÷B÷C÷D A≒C>B≒D C: Medium Resistance: C: High A: High Vout 2 Vout 2 Medium ... GND GND T Diaphragm Chip (Cross Section) Pressure Piezoresistive element Cross Piezoresistive element Diaphragm Vout1 VDD Chip (Top View) Vout2 GND 8