**PC** — PROGRAM_COUNTER

LP_BAR, EP, CP, CLK → LP_BAR, EP, CP, CLK
W0, W1, W2, W3, W4, W5, W6, W7 → W0–W7 [?]

**ACCUMULATOR** — ACCUMULATOR

EA, LA_BAR, R, RC, LALU_BAR, CLK, CF_ACC_OUT → EA, LA_BAR, R, RC, LALU_BAR, CLK, CF_IN
CF_OUT → CF_ACC_IN [1]
AC0_OUT, AC1_OUT, AC2_OUT, AC3_OUT [1]
W0, W1, W2, W3 → W0–W3
AC0_IN, AC1_IN, AC2_IN, AC3_IN [1]

**MAR** — MAR

LM_BAR, CLK → LM_BAR, CLK
RA0_IN–RA7_IN → R0–R7
W0–W7 → W0–W7

**ALU** — ALU

PCF, S0, S1, S2, S3, CF_ALU_OUT, M → PCF, S0, S1, S2, S3, CF_IN, M
AC0_OUT, AC1_OUT, AC2_OUT, AC3_OUT → AC0_OUT–AC3_OUT
T0, T1, T2, T3 → T0–T3
AC0_IN, AC1_IN, AC2_IN, AC3_IN [1]
SF → SF_ALU_IN [1]
CF_OUT → CF_ALU_IN [1]
ZF → ZF_ALU_IN [0]

**FLAG** — FLAG REGISTER

CLK, CLC, CLS, LF_BAR, ECF, L_ACC, CF_ACC_IN, CF_ALU_IN, SF_ALU_IN, ZF_ALU_IN
CF_OUT → CF_ACC_OUT [0]
SF_OUT → SF_OUT [0]
ZF_OUT → ZF_OUT [0]
CF_ALU_OUT → CF_ALU_OUT [0]

**RAM** — RAM

ER, LR_BAR, RA0_IN–RA7_IN, CLK, RP, CLOCK
RD0–RD7 → R0–R7
CLK [0], RP [1]

**TMP** — TMP REGISTER

LT_BAR, ET, CLK
T0–T3 → T0–T3 [0]
W0, W1, W2, W3 → W0–W3

**MDR** — CCT001

LMB_BAR, LMR_BAR, EMB, EMR, CLK
R0–R7 → R0–R7
W0–W7 → W0–W7 [?]

**B** — B REGISTER

LB_BAR, EB, CLK
W0, W1, W2, W3 → W0–W3
B0, B1, B2, B3 → B0–B3

**IR** — IR

LI_BAR, CLK
CS0–CS7 → CS0–CS7 [1,0,1,0,0,1,1,0]
W0–W7 → W0–W7 [?]

**SP** — STACK_POINTER

IS, DS, ES, CLK
W0–W7 → W0–W7 [?]

**OUT** — OUTPUT REGISTER

LO_BAR, CLK
W0, W1, W2, W3 → W0–W3
OUT0, OUT1, OUT2, OUT3 → OUT0–OUT3 [0]

**CS** — CONTROLLER

ZF_OUT → ZF
CS0–CS7 → CS0–CS7
CLK → CLK
CSA0–CSA7 → CSA0–CSA7 [1,0,1,0,0,0,0,0]
IC1, IC0 → IC1, IC0 [1,1]

Outputs:
CP → CP [0]
LP_BAR → LP_BAR [1]
EP → EP [0]
LM_BAR → LM_BAR [1]
ER → ER [0]
LR_BAR → LR_BAR [1]
LMB_BAR → LMB_BAR [1]
LMR_BAR → LMR_BAR [1]
EMB → EMB [0]
EMR → EMR [0]
LI_BAR → LI_BAR [1]
R → R [0]
RC → RC [0]
LA_BAR → LA_BAR [1]
EA → EA [0]
PCF → PCF [0]
S3 → S3 [0]
S2 → S2 [0]
S1 → S1 [0]
S0 → S0 [0]
M → M [0]
CIN → CIN [0]
LT_BAR → LT_BAR [1]
LB_BAR → LB_BAR [1]
EB → EB [0]
CLC → CLC [0]
CLS → CLS [0]
LF_BAR → LF_BAR [1]
ECF → ECF [0]
ES → ES [0]
IS → IS [0]
DS → DS [0]
LO_BAR → LO_BAR [1]
LALU_BAR → LALU_BAR [1]
ET → ET [0]
L_ACC → L_ACC [0]

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