

Prepared By

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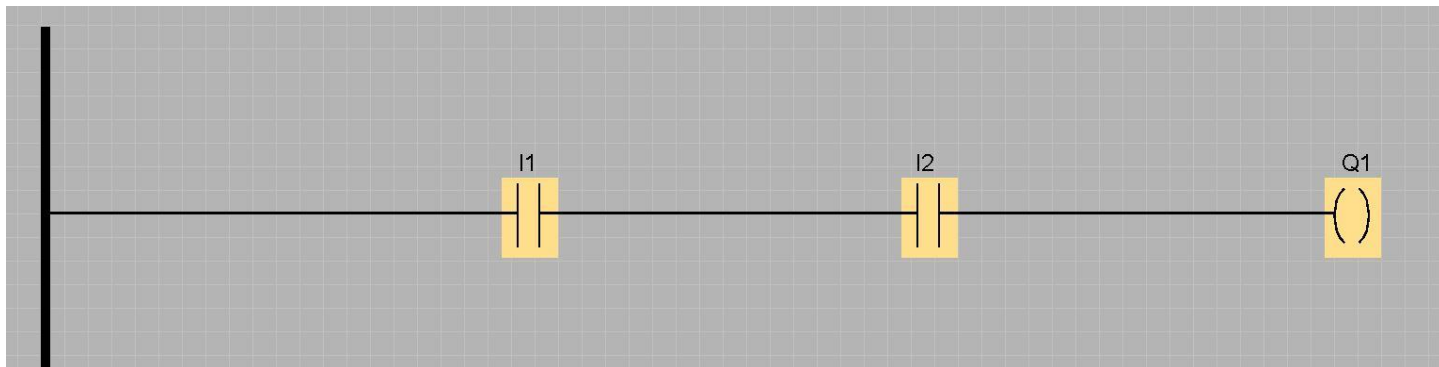


Fig: AND gate Ladder Diagram

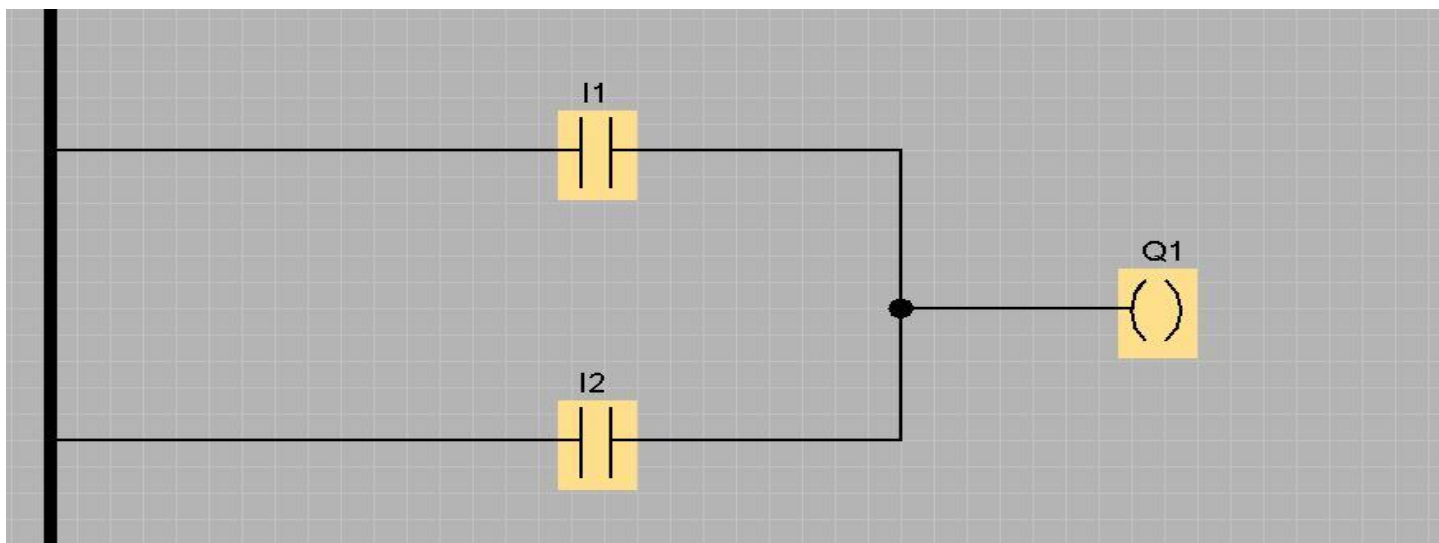


Fig: OR gate Ladder Diagram

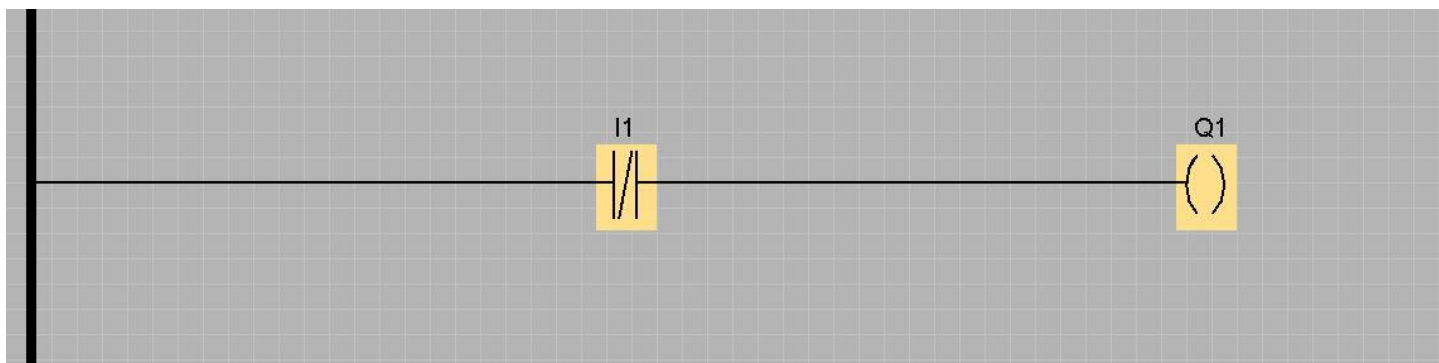


Fig: NOT gate Ladder Diagram

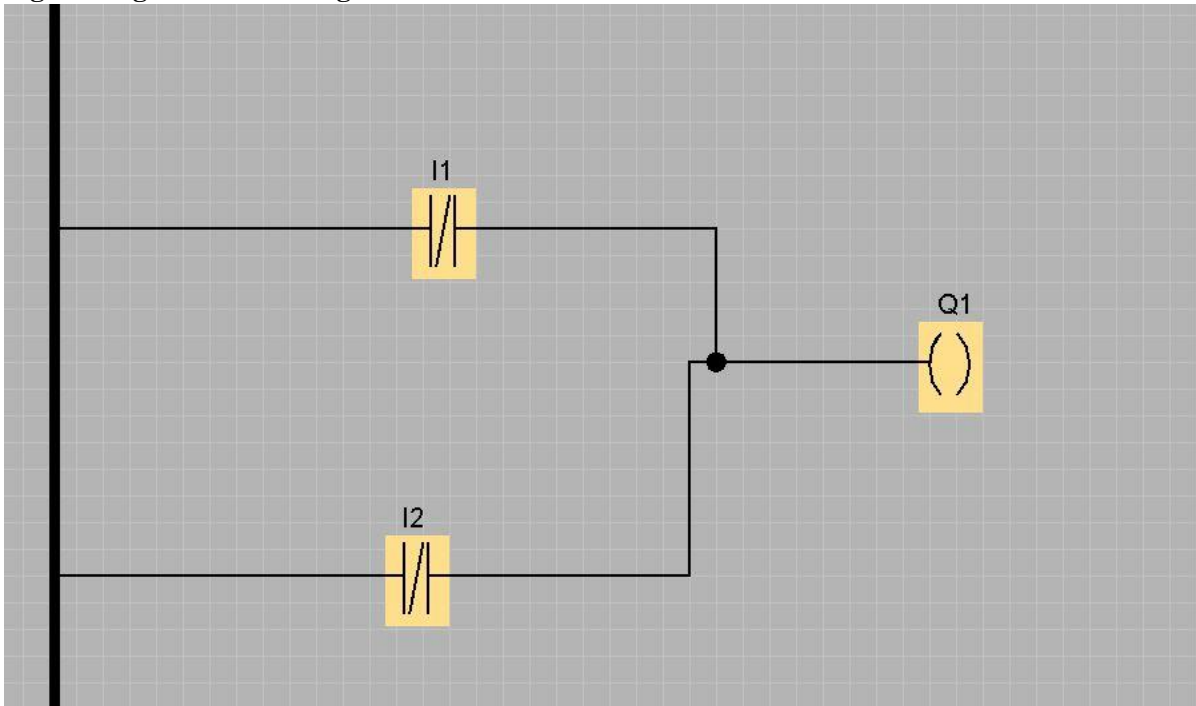


Fig: NAND gate Ladder Diagram

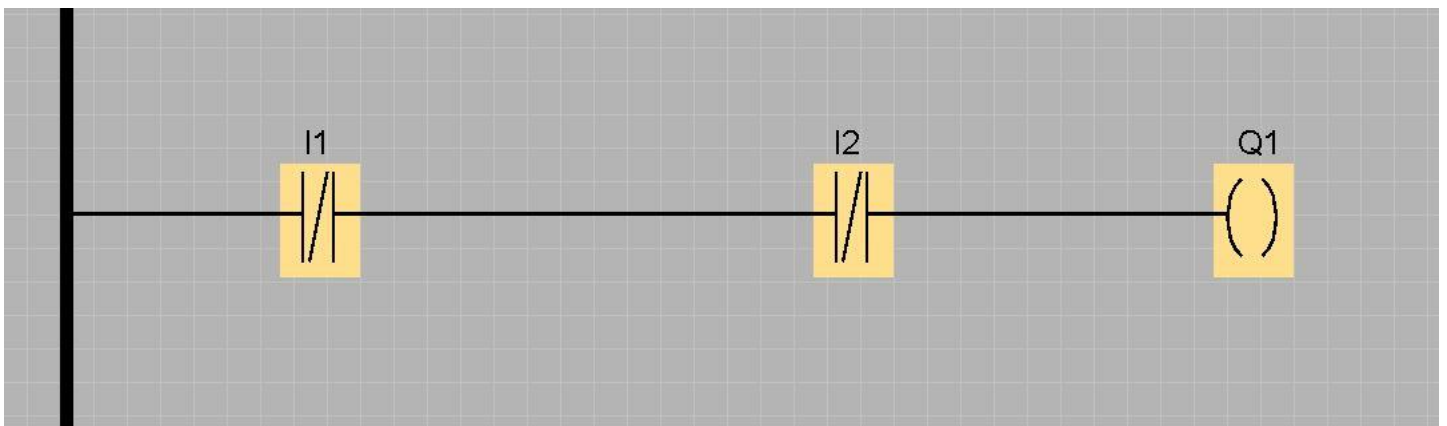


Fig: NOR gate Ladder Diagram

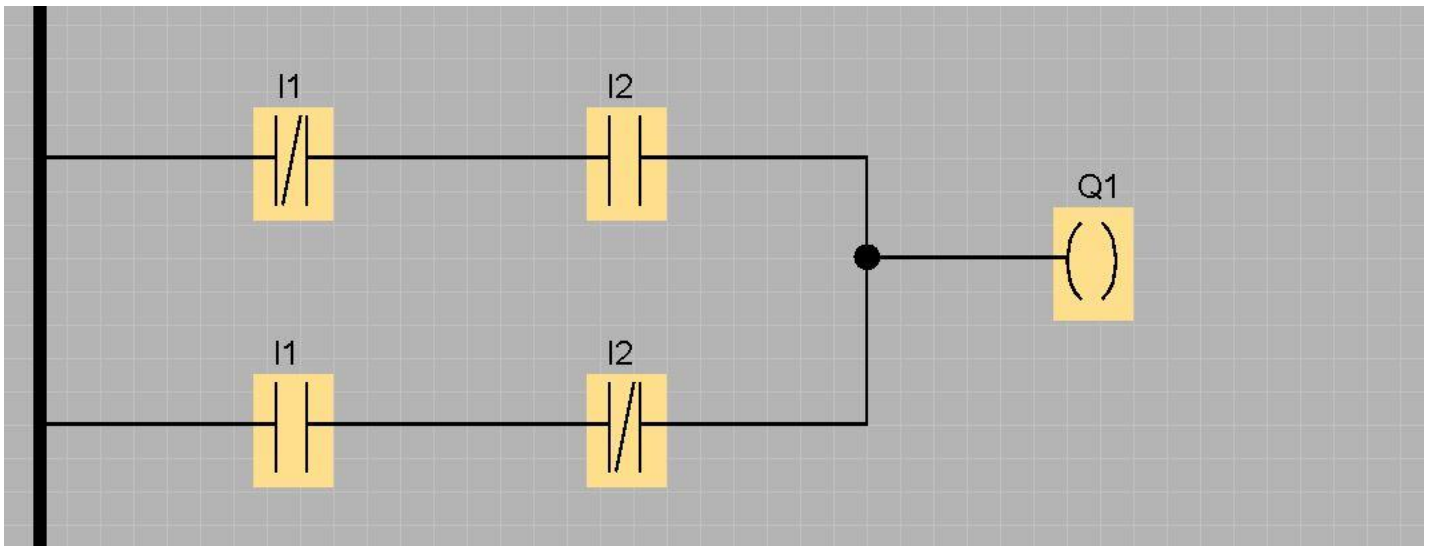


Fig: XOR gate Ladder Diagram

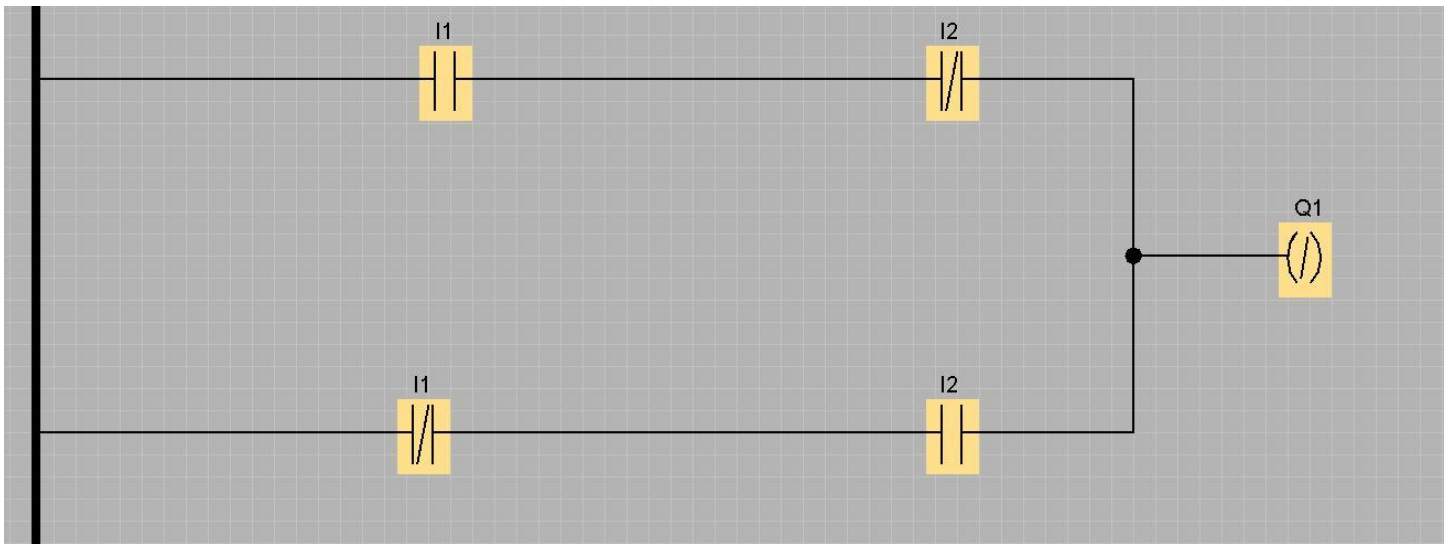


Fig: XNOR gate Ladder Diagram

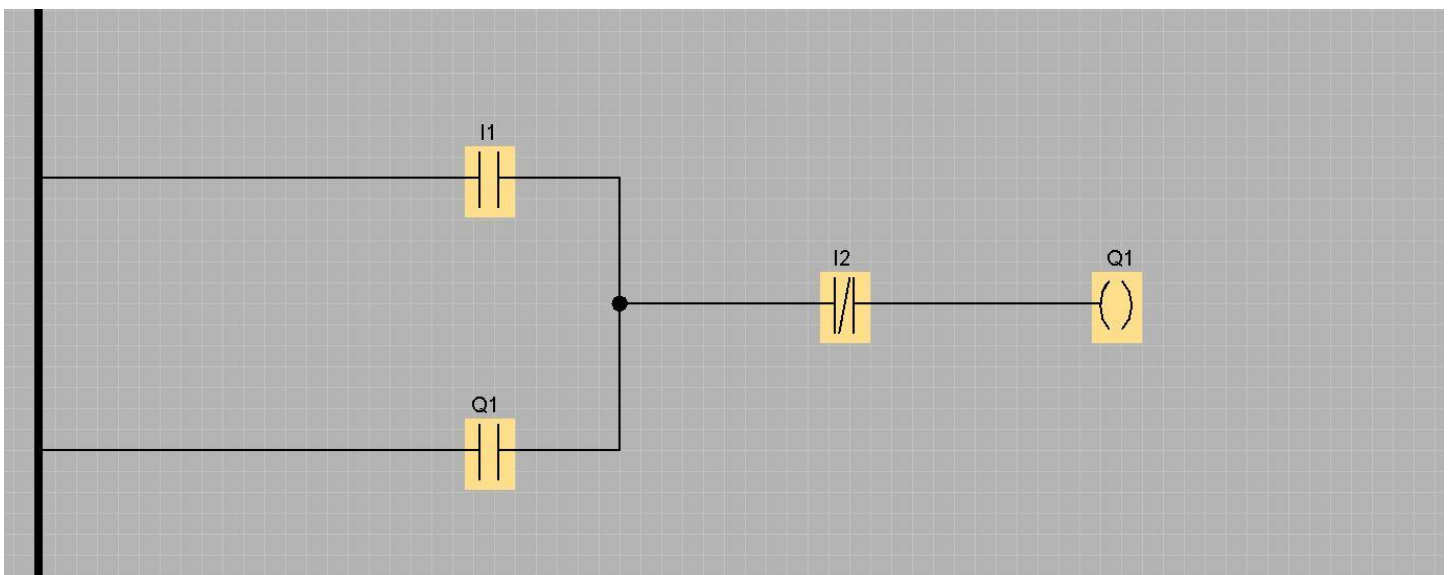


Fig: DOL Ladder Diagram

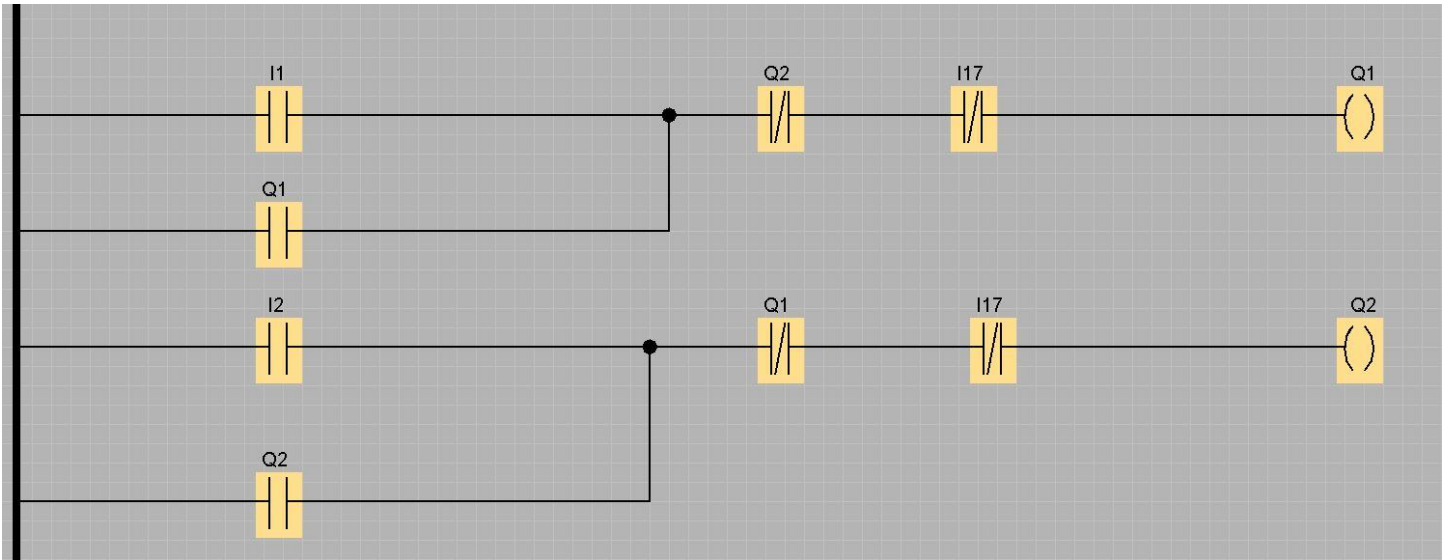


Fig: Reverse Forward Ladder Diagram

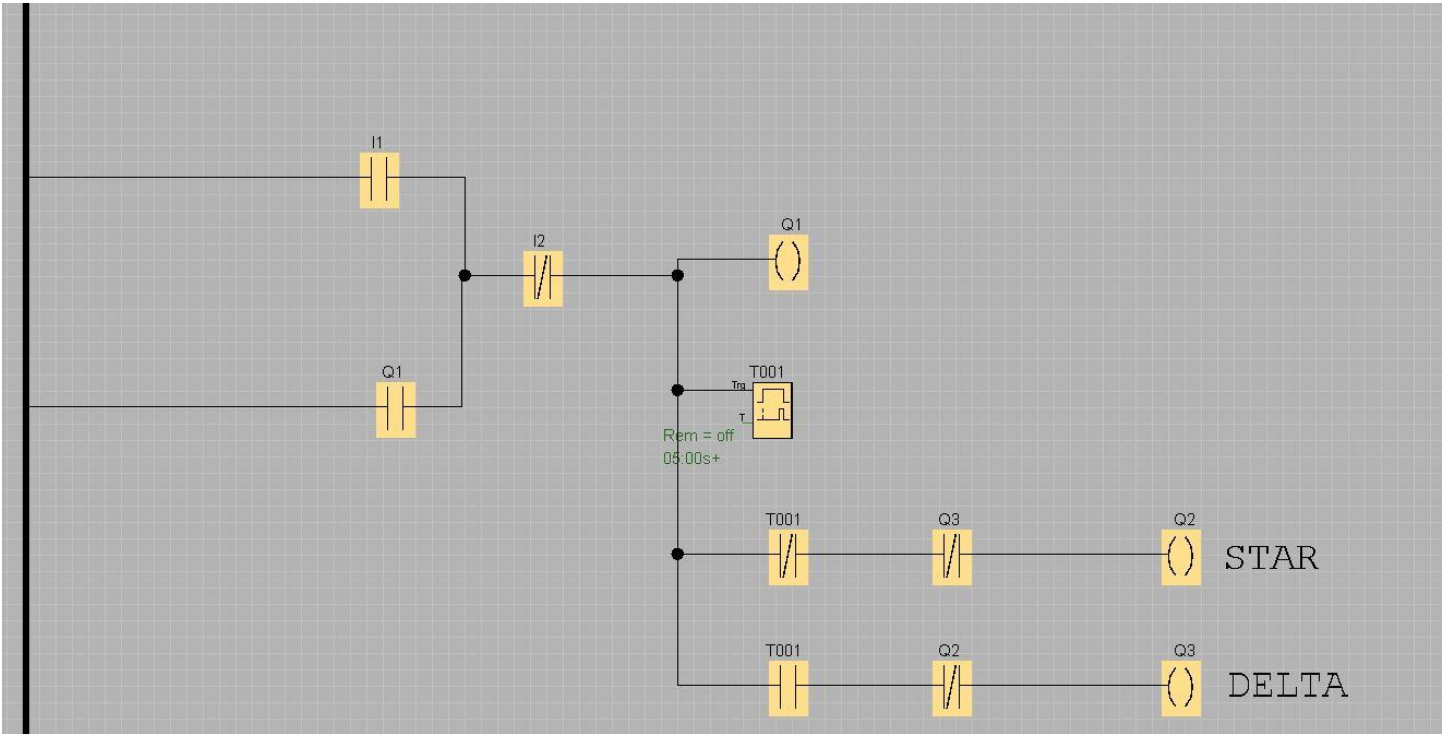


Fig: Star Delta Ladder Diagram

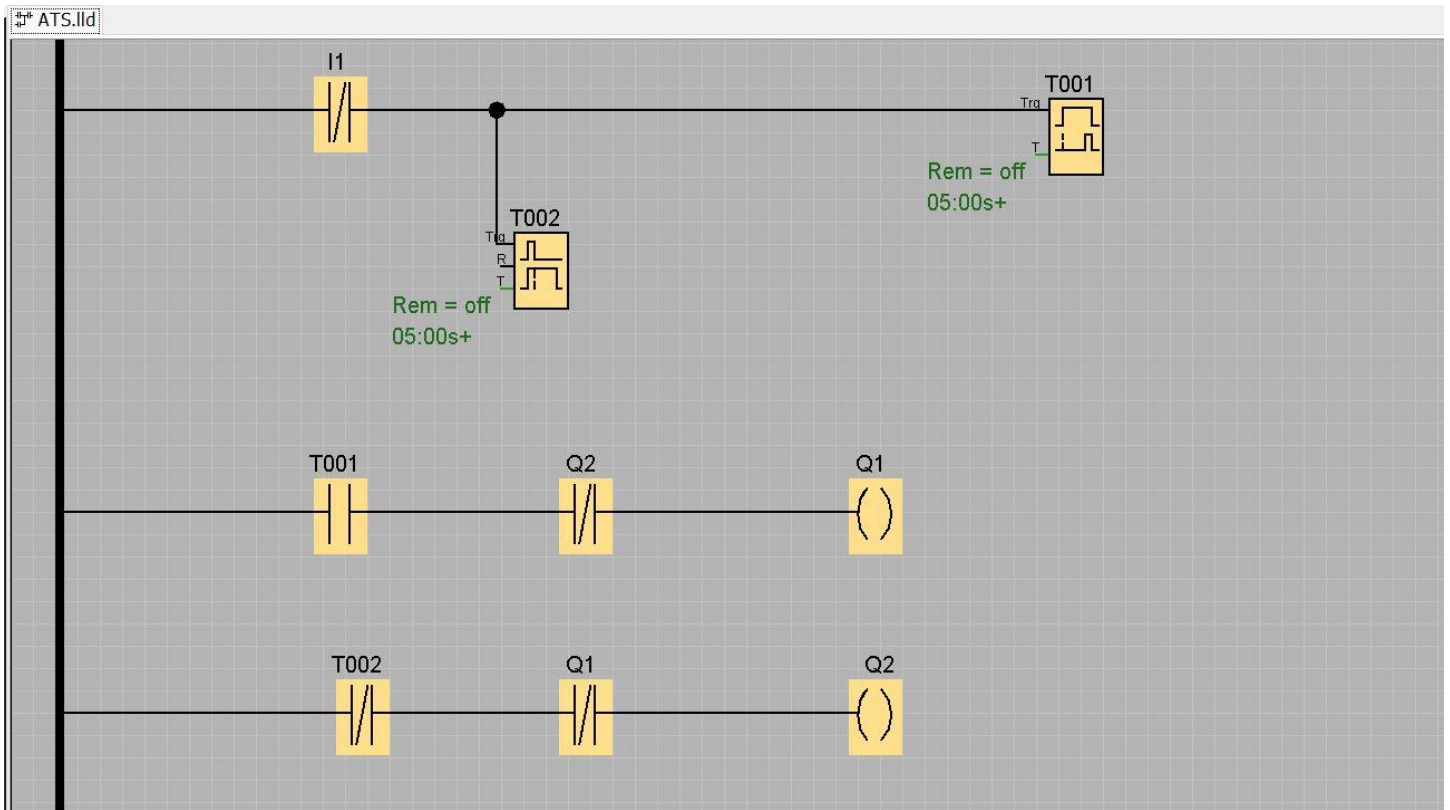


Fig: ATS Ladder Diagram

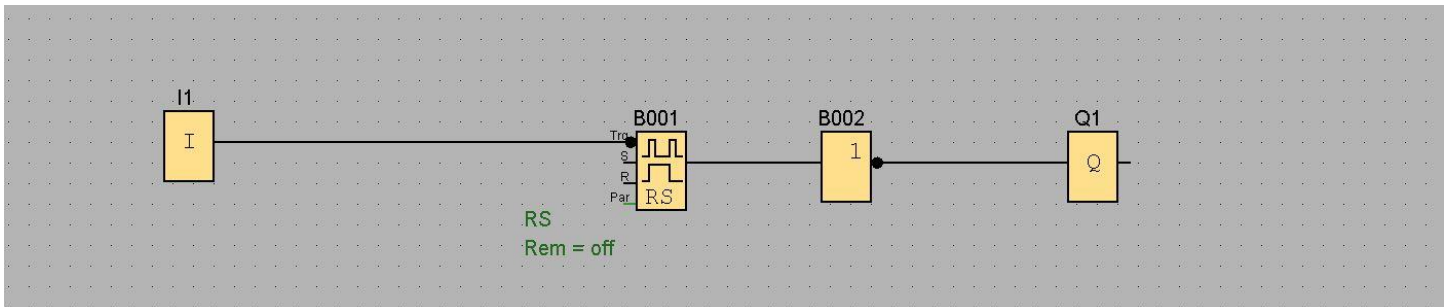


Fig: One push Button based Dol Ladder Diagram

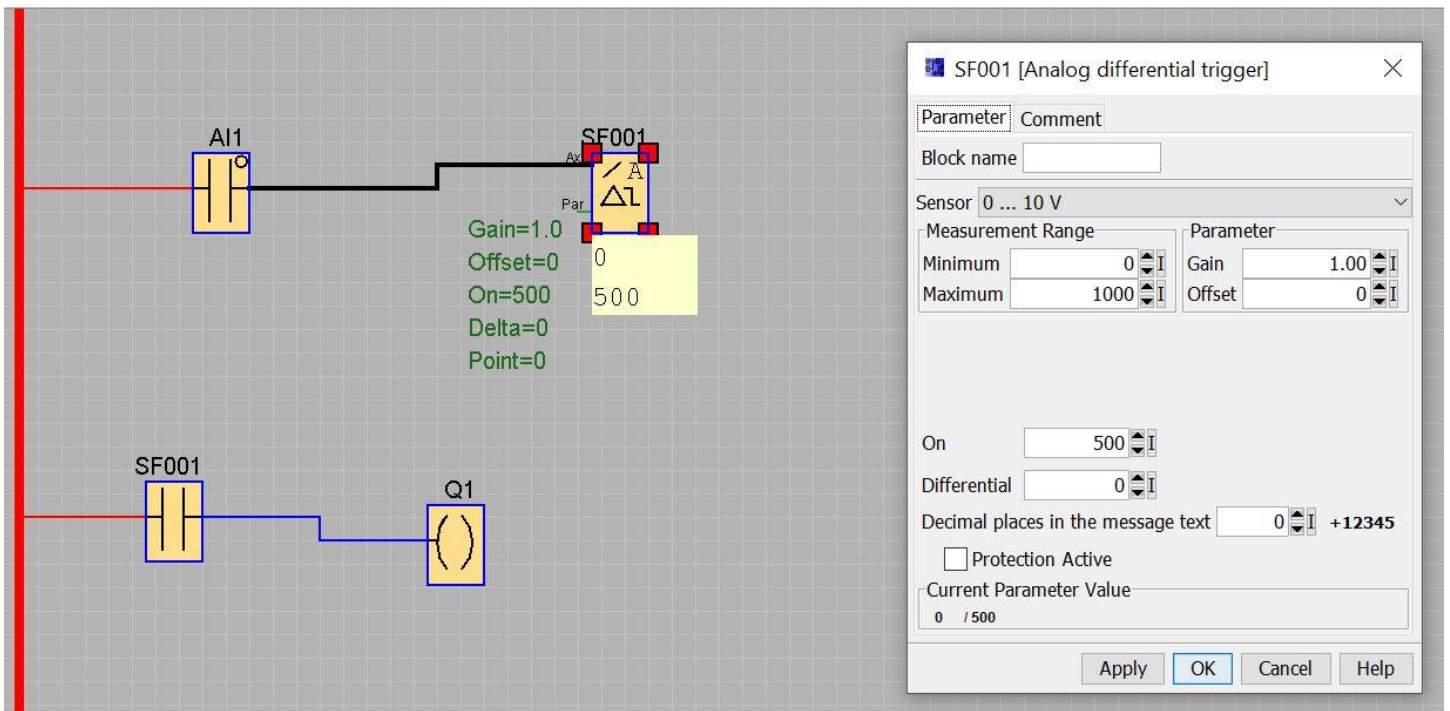


Fig: Analog Value based load control Ladder Diagram

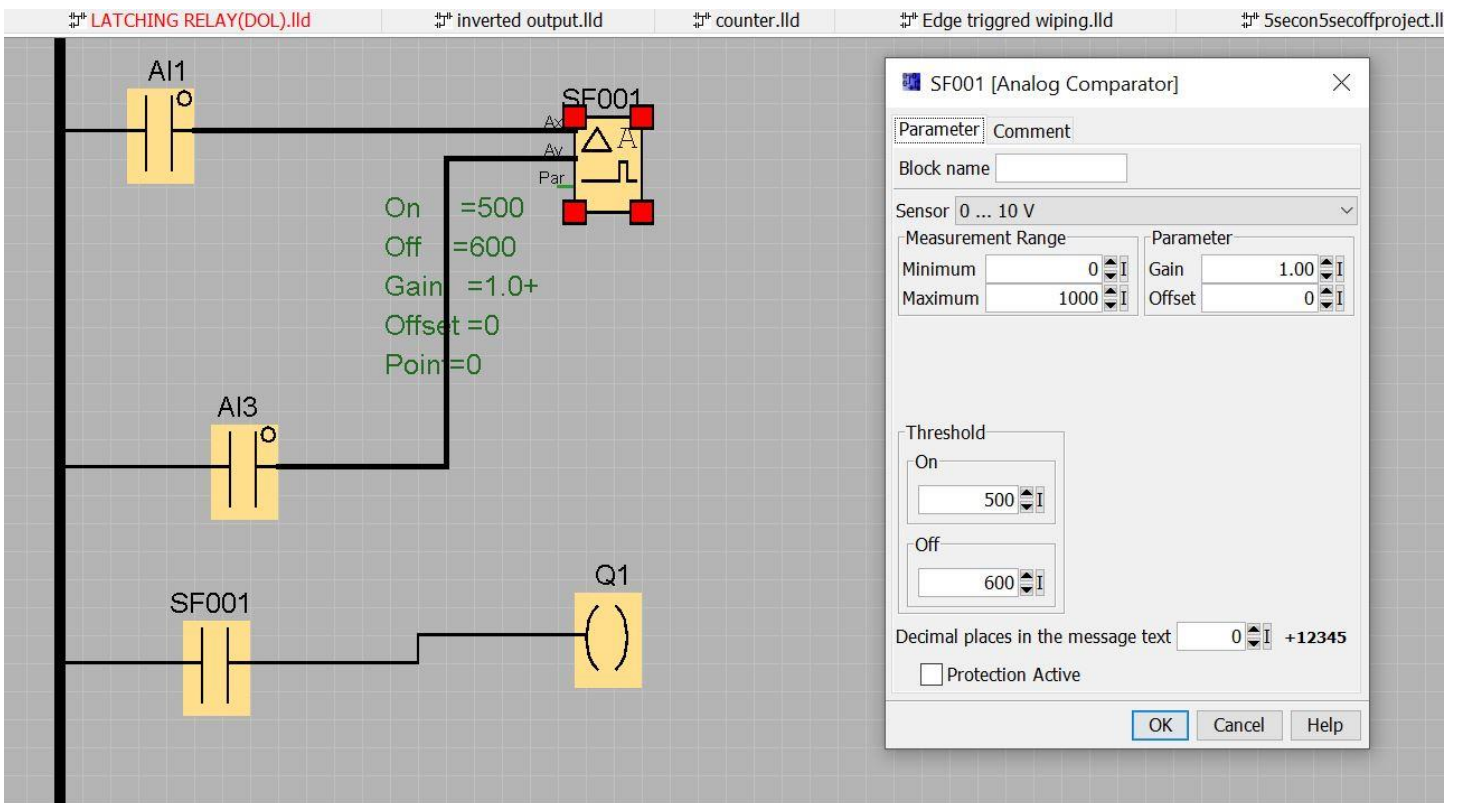


Fig: Analog Value Comparator based load control Ladder Diagram

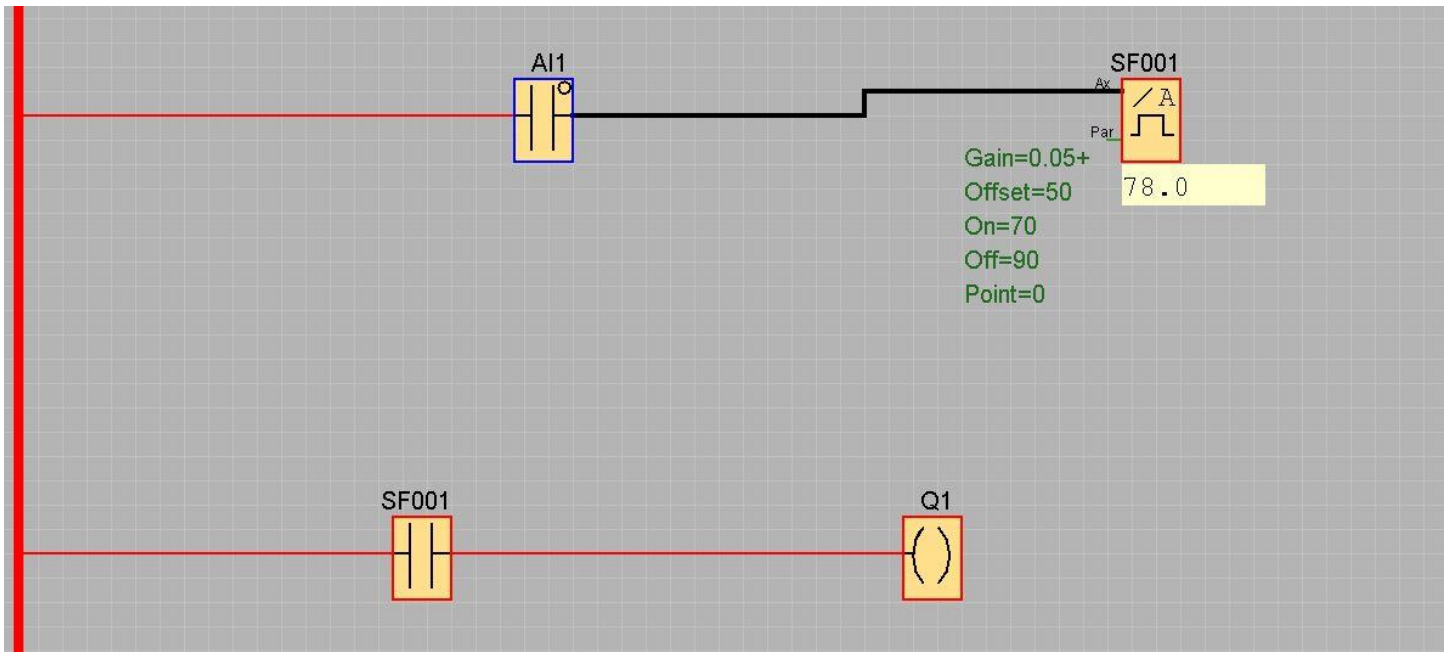


Fig: Analog Value input based load control Ladder Diagram

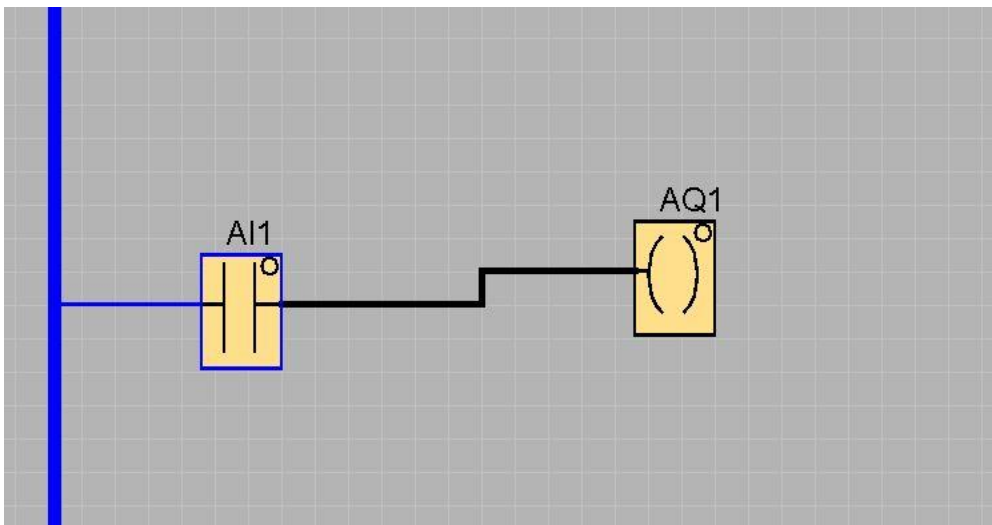


Fig: Analog Output Ladder Diagram

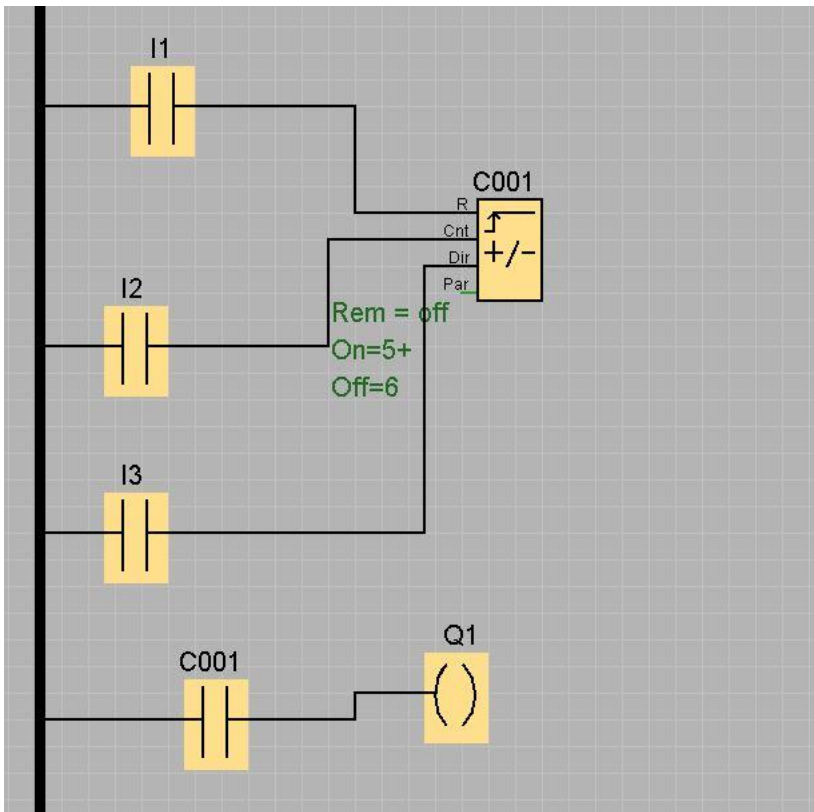


Fig: Counter based load Control Ladder Diagram

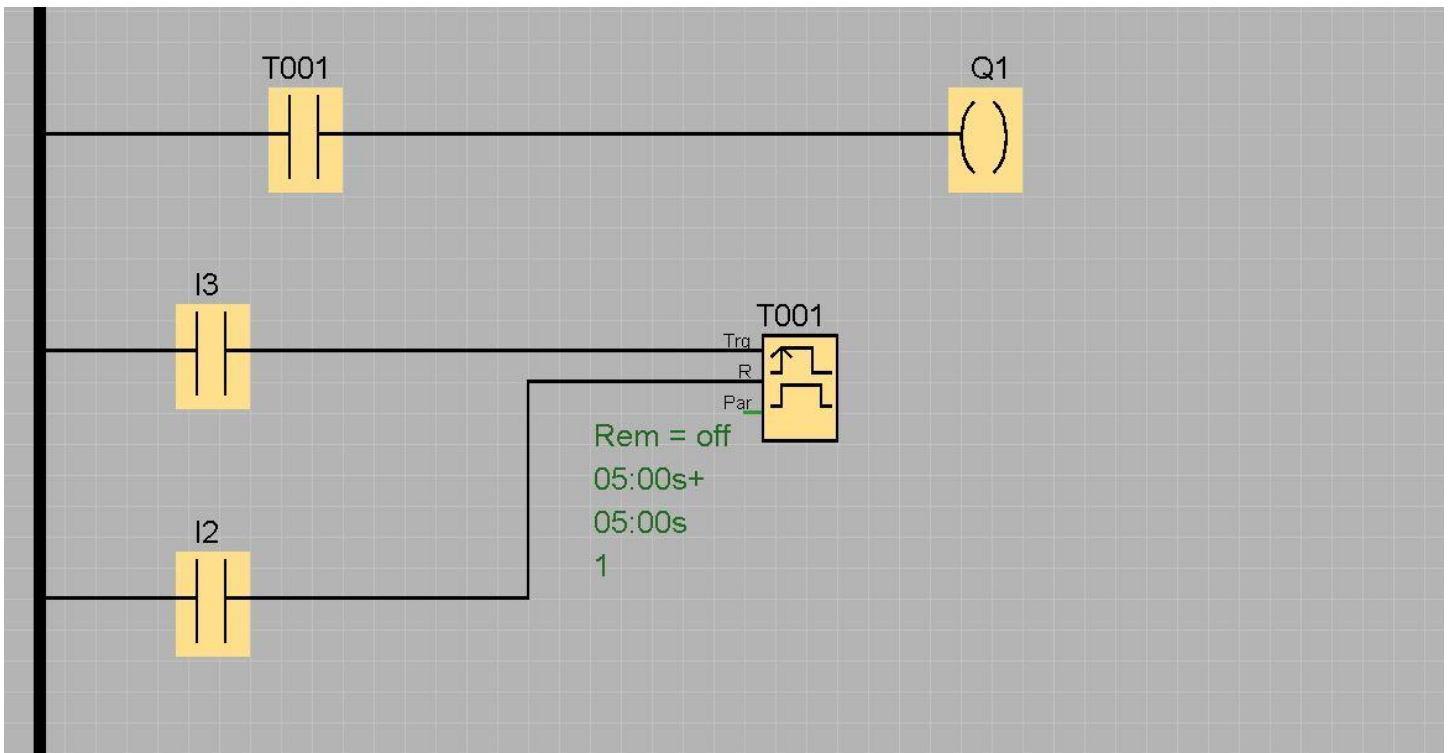


Fig: Edge trigger based load control Ladder Diagram

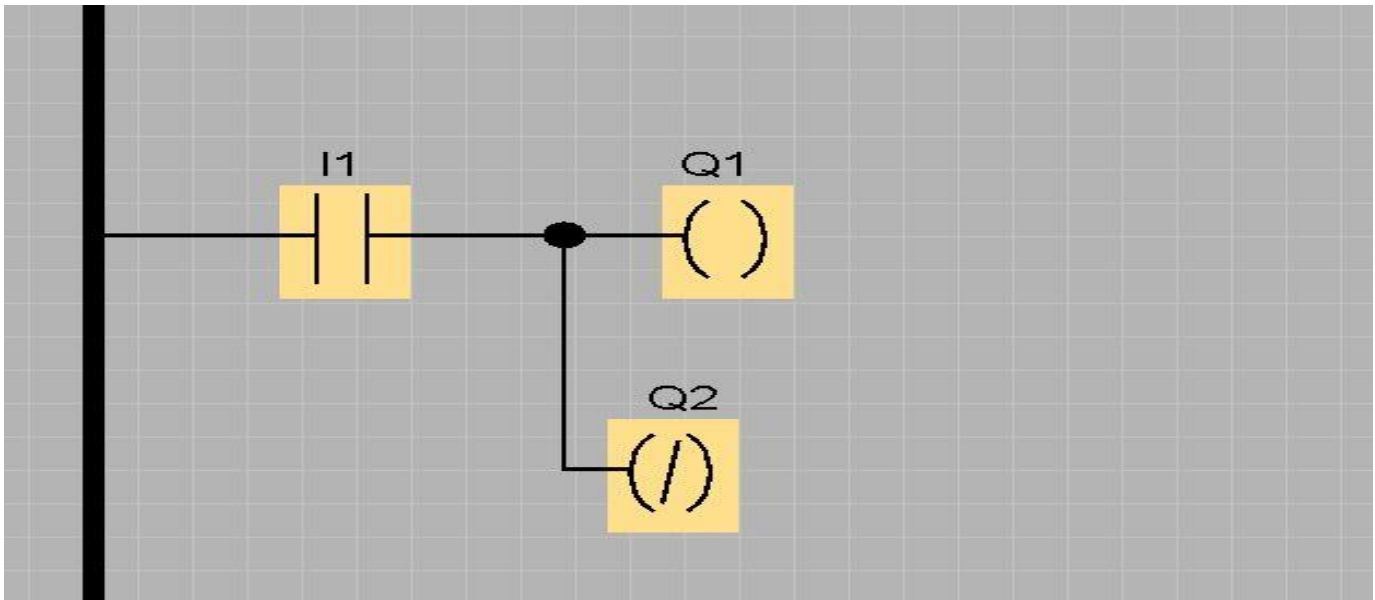


Fig: Inverted output Ladder Diagram

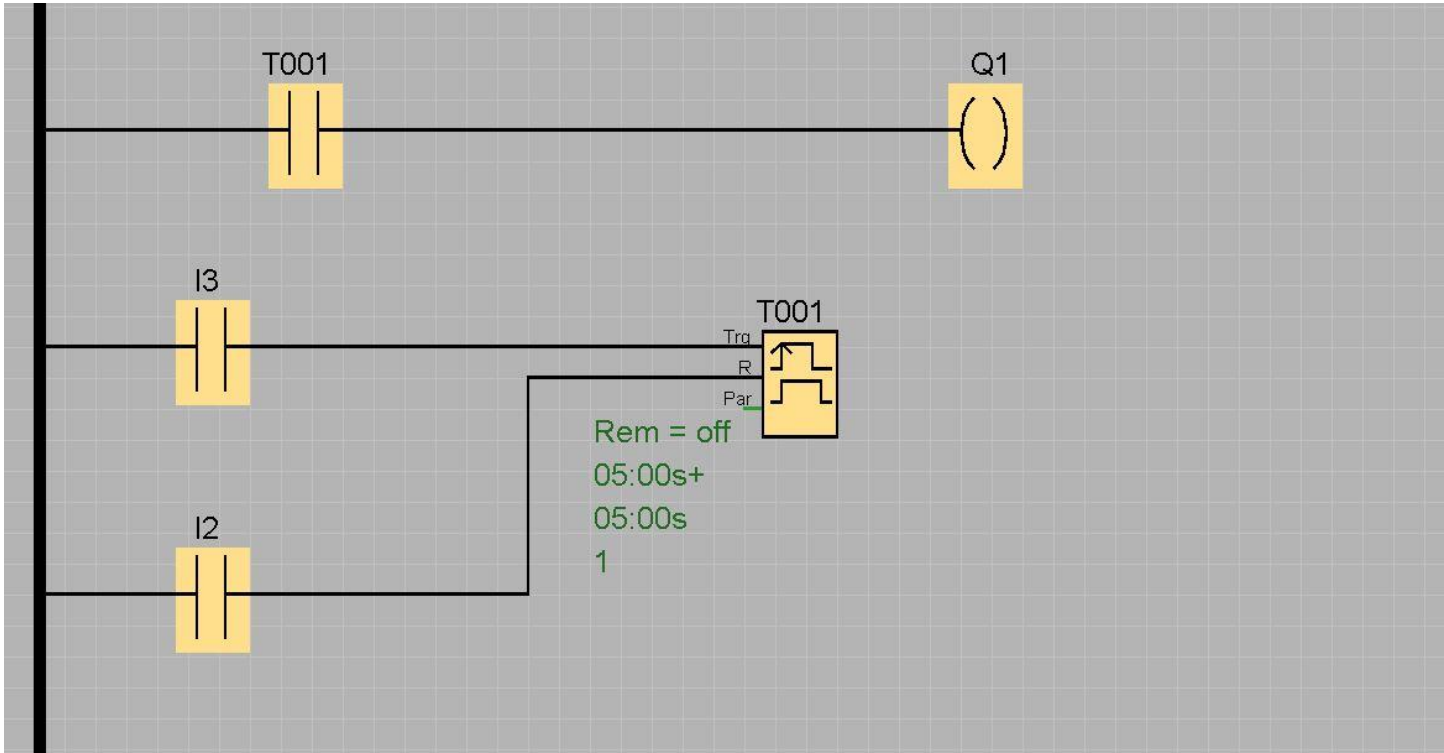


Fig: Edge trigger Ladder Diagram

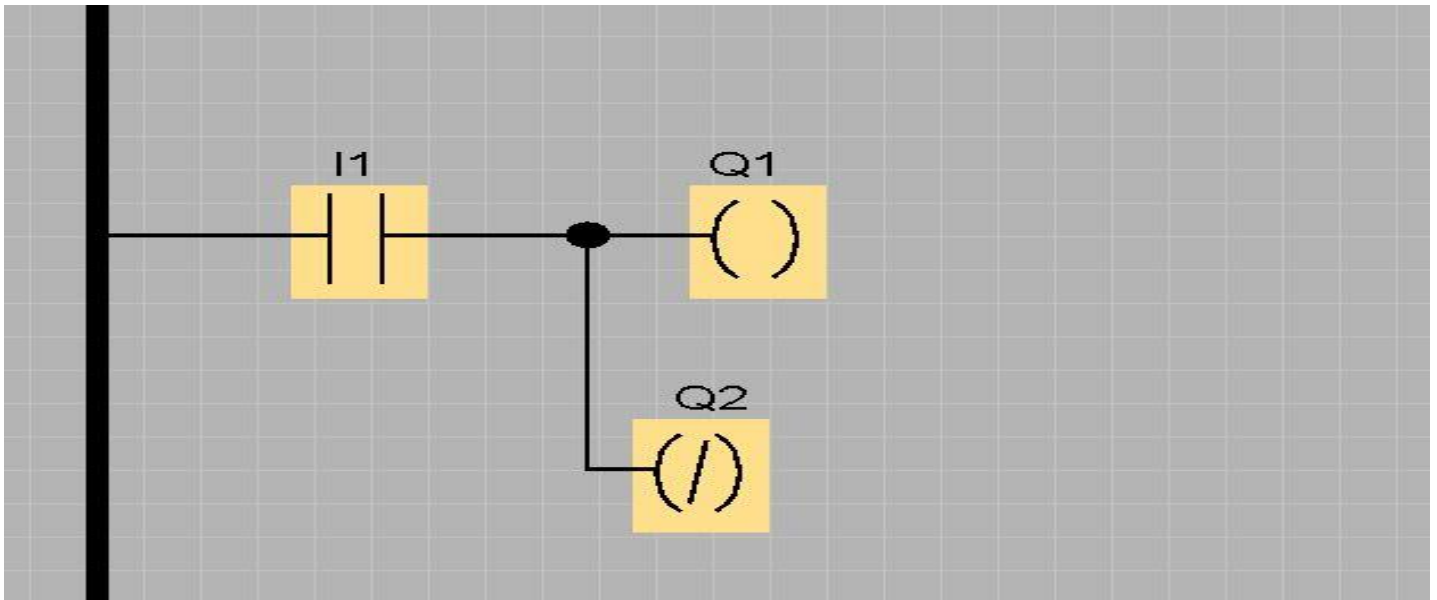


Fig: input based Inverted Output Ladder Diagram

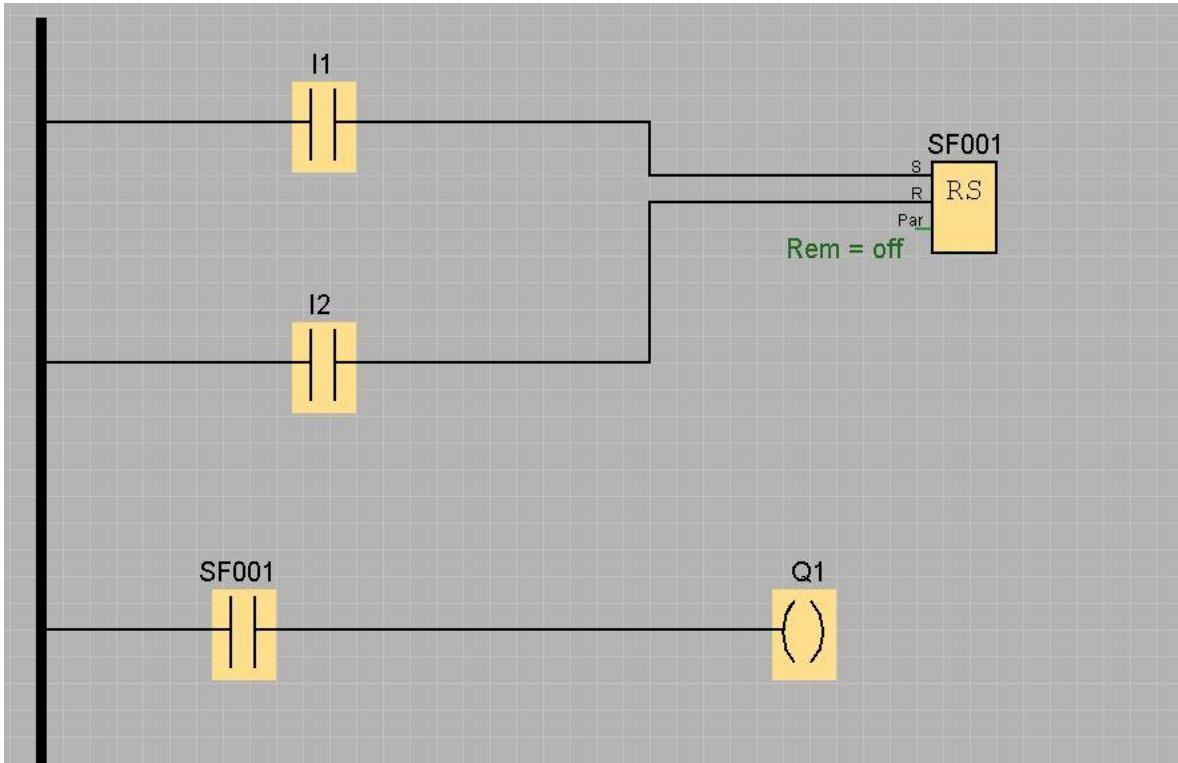


Fig: Latch Dol Ladder Diagram

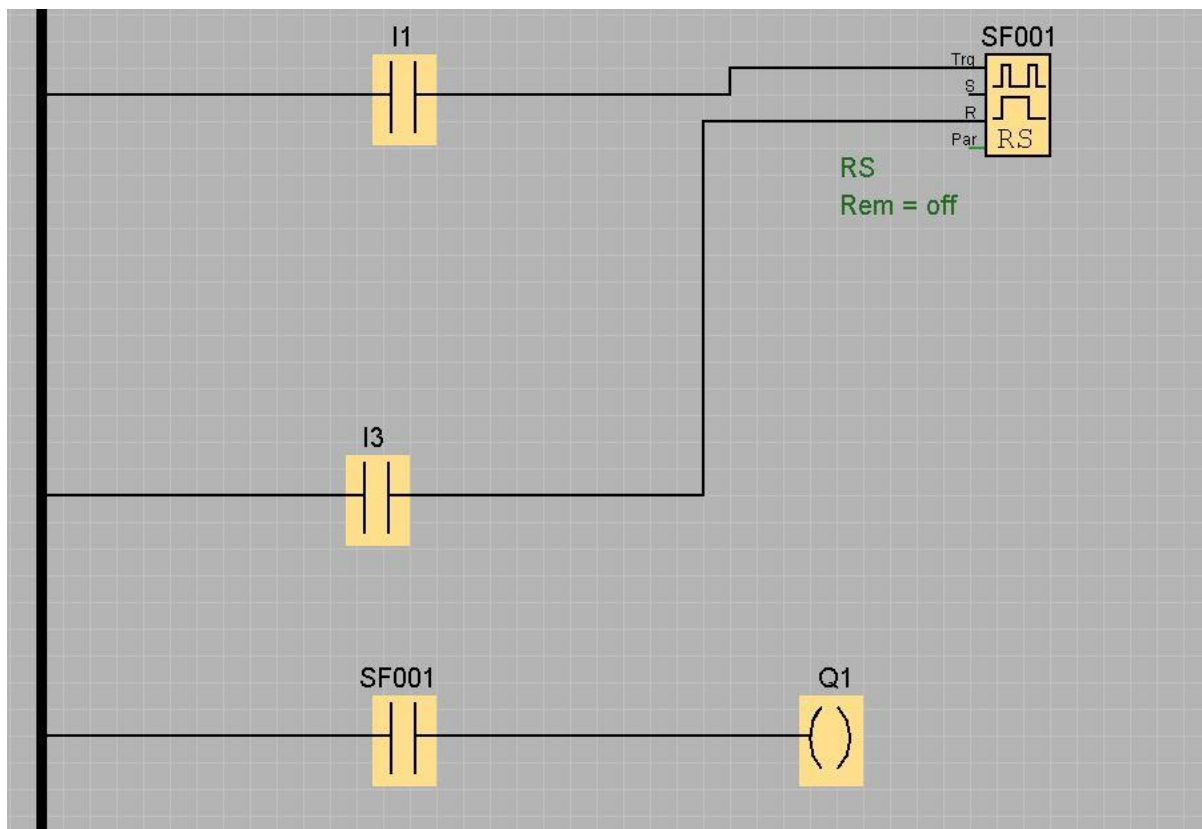


Fig: Latch Relay Ladder Diagram