

## CCCS217 Computer Organization and Architecture

# Course Project

**Students' names:**

# Project Description:

**Part1:** is related to mapping of memory to cache

## 1. For All caches (Direct Mapping)

Cache Comparison			
1. Direct Mapped Cache[16]		HIT 8% MISS 92%    ADD. RESOURCES 1328	NAND
Instruction Breakdown			
0011	0	0	
4 bit	4 bit	0 bit	
2. Direct Mapped Cache[32]		HIT 14% MISS 86%    ADD. RESOURCES 872	NAND
Instruction Breakdown			
001	10000	0	
3 bit	5 bit	0 bit	
3. Direct Mapped Cache[64]		HIT 16% MISS 84%    ADD. RESOURCES 496	NAND
Instruction Breakdown			
0	110000	0	
2 bit	6 bit	0 bit	

a. Set the size of Cache so that Direct Mapping has the maximum hit ratio and minimum miss ratio?

64 size of cache has maximum hit ratio: 16%, and minimum miss ratio: 84%.

16 size of cache has minimum hit ratio: 8%, and maximum miss ratio: 92%.

b. How can you increase the hit ratio in case of direct mapping by changing some of size of Caches?

We can increase the hit ratio in direct mapping by increasing the cache size.

c. In presentation, show the pictures from the simulator containing the values used and results obtained. Also, give reasons why the hit ratio increases when you change the size of caches?

When the size of the cache increases, then number of blocks will increase. Since there are many blocks, many pages from main memory will get accommodated in the cache memory. If more blocks are present in cache memory, then probability of finding a word in the cache will increase. Hence hit ratio increases.

If there are "n" blocks in cache initially, it will be "n+m" blocks after increase in the size.

d. Explain the reason for the number of bits being used for Tag and for the number of bits required for RAM address?

1 -To find the number of bits required for main memory address:

Simply by adding tag bits and bits of lines with the block offset

It's also known as "Physical Address Space"

The formula:

$PAS = \text{tag} + \text{line} + \text{block offset}$

As in the picture the PAS was 258 B  $\rightarrow$  number of bits =  $\lceil \log_2 256 \rceil = 8$  bits



Since the block offset is 0, line size = block size

the number of lines = cash size / line size

the number of lines =  $2^4 / 2^0 = 2^4$

number of bits =  $\lceil \log_2 2^4 \rceil = 4$  bits

2-To find tag bits :

Tag = PAS – bits of lines

Tag = 8 – 4  $\rightarrow$  Tag = 4 bits

## 2. For All caches (Fully Associative Mapping)

Cache Comparison	
1. Fully Associative Cache[16]	HIT 8% MISS 92%    ADD. RESOURCES 23088 NAND
Instruction Breakdown	
10010100	0
8 bit	0 bit
2. Fully Associative Cache[32]	HIT 10% MISS 90%    ADD. RESOURCES 32824 NAND
Instruction Breakdown	
10010100	0
8 bit	0 bit
3. Fully Associative Cache[64]	HIT 13% MISS 88%    ADD. RESOURCES 38440 NAND
Instruction Breakdown	
10010100	0
8 bit	0 bit

a. Set the size of Cache so that Fully Associative Mapping has the maximum hit ratio and minimum miss ratio?

64 size of cache has maximum hit ratio: 13%, and minimum miss ratio: 88%.

16 size of cache has minimum hit ratio: 8%, and maximum miss ratio: 92%.

b. How can you increase the hit ratio in case of Fully Associative mapping by changing some of size of Caches?

When we increase the size to bigger size, the hit ratio will be higher.

c. In presentation, show the pictures from the simulator containing the values used and results obtained. Also, give reasons why the hit ratio increases when you change the size of caches?

A block of main memory can present any where in the cache. Due to increase in space more blocks will gets accommodated.

d. Explain the reason for the number of bits being used for Tag and for the number of bits required for RAM address?

1-To find the number of bits required for main memory address:

Simply by adding tag bits with the block offset

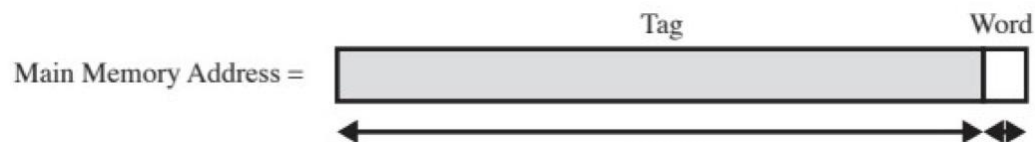
It's also known as "Physical Address Space"

Because there's only tag and block offset

The formula:

$PAS = \text{tag} + \text{block offset}$

As in the picture the PAS was 256 B  $\rightarrow$  number of bits =  $\lceil \log_2 256 \rceil = 8$  bits



2-To find tag bits :

Since the block offset is 0

$\text{Tag} = \text{PAS}$

$\text{Tag} = 8$  bits

### 3. For All caches (4-way Associative Mapping)

Cache Comparison		
1. 4-Way Set Associative Cache[4]		
HIT 7% MISS 93%    ADD. RESOURCES 2720 NAND		
Instruction Breakdown		
110001	01	0
6 bit	2 bit	0 bit
2. 4-Way Set Associative Cache[8]		
HIT 11% MISS 89%    ADD. RESOURCES 2304 NAND		
Instruction Breakdown		
11000	101	0
5 bit	3 bit	0 bit
3. 4-Way Set Associative Cache[16]		
HIT 15% MISS 85%    ADD. RESOURCES 1856 NAND		
Instruction Breakdown		
1100	0101	0
4 bit	4 bit	0 bit

a. Set the size of Cache so that 4-way Associative Mapping has the maximum hit ratio and minimum miss ratio?

16 size of cache has maximum hit ratio: 13%, and minimum miss ratio: 88%.

4 size of cache has minimum hit ratio: 8%, and maximum miss ratio: 92%.

b. How can you increase the hit ratio in case of 4-way Associative mapping by changing some of size of Caches?

When the size increasing, the hits will be increasing and miss hits will decreasing.

c. In presentation, show the pictures from the simulator containing the values used and results obtained. Also, give reasons why the hit ratio increases when you change the size of caches?

Due to increase in size, number of sets increases, but set size remains same. Since there are many sets chance of finding word is high.

Decreasing in cache size reduces hit rate, as there are only blocks.

d. Explain the reason for the number of bits being used for Tag and for the number of bits required for RAM address?

1-To find the number of bits required for main memory address:

Simply by adding tag bits and bits of sets with the block offset

It's also known as "Physical Address Space"

The formula:

$PAS = \text{tag} + \text{set} + \text{block offset}$

As in the picture the PAS was 256 B  $\rightarrow$  number of bits =  $\lceil \log_2 256 \rceil = 8$  bits



Since the block offset is 0

the number of sets = number of ways or sets (v)

the number of sets = 4 B

number of bits =  $\lceil \log_2 4 \rceil = 2$  bits

2-To find tag bits :

$\text{Tag} = PAS - \text{bits of sets}$

$\text{Tag} = 8 - 2 \rightarrow \text{Tag} = 6$  bits

## Part2: Write an Assembly Language Program

This assembly code will convert an octal number to a decimal number and will make sure that the input is between 0-7 only. By using two loops, one if condition and two functions.

```
1 .data
2 message1: .ascii "Rama Aliyoubi Ragnad Almutari Ryouf Alghamdi \n\n"
3 EnterMessage: .ascii "Enter three digit of octal number\t"
4 above7message: .ascii "One of the digits was above 7\nPlease enter a valid octal number\t"
5 invalidMessage: .ascii "Invalid octal number please try again\t"
6 result: .ascii "The result:\t"
7
8
9 .text
10 main:
11
12     la $a0, message1          # print message
13     li $v0, 4
14     syscall
15
16     la $a0, EnterMessage
17     li $v0, 4
18     syscall
19
20     jal input                  # calling input function to take integer value from user
21
22
23 while1:
24     li $t1, 0                 # Load immediately $t1=0
25     li $t2, 777               # Load immediately $t2=777
26     sgt $t3, $t0, $t1         # is input grater than 0? , $t3 = boolean excprtion (true or false) (1,0)
27     slt $t4, $t0, $t2         # is input less than 777? , $t4 = boolean excprtion (true or false) (1,0)
28     and $t5, $t4, $t3         # condition 1 AND condition 2 (to make it OK)
29     beq $t5, 1, exit1        # branch to exit if true
30
31     la $a0, invalidMessage
32     li $v0, 4
33     syscall
34     jal input                  # calling input function to take integer value from user
35     move $t0, $v0
36
37     j while1                  # jump to the loop function untel the condition in branch statement is true
38     exit:                     # end of loop
39
40     move $t7, $t0
41 while2:
42     li $t6, 10                # Load immediately $t6 = 10
43     li $t3, 0                 # Load immediately $t3 = 0
44     rem $t3, $t7, $t6         # $t7 = input % $t6 (save the remainder in $t7)
45     li $t8, 8                 # Load immediately $t8 = 8
46     slt $t2, $t3, $t8         # is $t2 less than 8? , $t2 = boolean excprtion (true or false) (1,0)
47     beq $t2, 1, else1        # branch to else if true
48     if:
49     la $a0, above7message
50     li $v0, 4
51     syscall
52     jal input                  # calling input function to take integer value from user
53     move $t7, $v0
54     j while2
55     else:
56     div $t7, $t7, $t6
57     beq $t7, 0, else2        # branch to exit if true
58
59     j while2                  # jump to the loop function untel the condition in branch statement is true
60
61 exit2:                         # end of loop
62
63
64     li $a1, 0                 # Load immediately $a1 = 0 (the power)
65     li $a0, 8                 # Load immediately $a0 = 8 (the base)
66
67     jal power                  # calling pow function
68     move $t2, $v0             # return the value from the power function and move it to $t2
69
70
71     rem $t3, $t0, 10          # $t3 = input % $t6 (save the remainder in $t3)
72     mul $t4, $t3, $t2         # $t4 = $t3 * $t2 (save the multiplication in $t4)
73     add $t1, $t1, $t4         # $t1 = $t1 + $t4 (save the addition in $t1)
74     div $t0, $t0, 10          # $t0 = $t0 / 10 (save the division in $t0)
75
```

Registers	Coproc 1	Coproc 0
Name	Number	Value
\$Zero	0	0x00000000
\$At	1	0x00000000
\$V0	2	0x00000000
\$V1	3	0x00000000
\$A0	4	0x00000000
\$A1	5	0x00000000
\$A2	6	0x00000000
\$A3	7	0x00000000
\$T0	8	0x00000000
\$T1	9	0x00000000
\$T2	10	0x00000000
\$T3	11	0x00000000
\$T4	12	0x00000000
\$T5	13	0x00000000
\$T6	14	0x00000000
\$T7	15	0x00000000
\$S0	16	0x00000000
\$S1	17	0x00000000
\$S2	18	0x00000000
\$S3	19	0x00000000
\$S4	20	0x00000000
\$S5	21	0x00000000
\$S6	22	0x00000000
\$S7	23	0x00000000
\$S8	24	0x00000000
\$S9	25	0x00000000
\$K0	26	0x00000000
\$K1	27	0x00000000
\$GP	28	0x10000000
\$SP	29	0x7fffffc0
\$FP	30	0x00000000
\$RA	31	0x00000000
PC		0x00400000
hi		0x00000000
\$Zero	0	0x00000000
\$At	1	0x00000000
\$V0	2	0x00000000
\$V1	3	0x00000000
\$A0	4	0x00000000
\$A1	5	0x00000000
\$A2	6	0x00000000
\$A3	7	0x00000000
\$T0	8	0x00000000
\$T1	9	0x00000000
\$T2	10	0x00000000
\$T3	11	0x00000000
\$T4	12	0x00000000
\$T5	13	0x00000000
\$T6	14	0x00000000
\$T7	15	0x00000000
\$S0	16	0x00000000
\$S1	17	0x00000000
\$S2	18	0x00000000
\$S3	19	0x00000000
\$S4	20	0x00000000
\$S5	21	0x00000000
\$S6	22	0x00000000
\$S7	23	0x00000000
\$S8	24	0x00000000
\$S9	25	0x00000000
\$K0	26	0x00000000
\$K1	27	0x00000000
\$GP	28	0x10000000
\$SP	29	0x7fffffc0
\$FP	30	0x00000000
\$RA	31	0x00000000
PC		0x00400000
hi		0x00000000



```

76  li $a0,8                # Load immediately $a0 = 8 (the base)
77  li $a1,1                # Load immediately $a1 = 1 (the power)
78  jal power               # calling pow function
79  move $t5, $v0           # return the value from the power function and move it to $t5
80
81  rem $t3,$t0,10          # $t3 = input % $t6 (save the remainder in $t3)
82  mul $t4,$t3,$t5         # $t4 = $t3 * $t2 (save the multiplication in $t4)
83  add $t1,$t1,$t4         # $t1 = $t1 + $t4 (save the addition in $t1)
84  div $t0,$t0,10         # $t0 = $t0 / 10 (save the division in $t0)
85
86  li $a0,8                # Load immediately $a0 = 8 (the base)
87  li $a1,2                # Load immediately $a1 = 2 (the power)
88  jal power               # calling pow function
89  move $t7, $v0           # return the value from the power function and move it to $t7
90
91  rem $t3,$t0,10          # $t3 = input % $t6 (save the remainder in $t3)
92  mul $t4,$t3,$t7         # $t4 = $t3 * $t2 (save the multiplication in $t4)
93  add $t1,$t1,$t4         # $t1 = $t1 + $t4 (save the addition in $t1)
94  div $t0,$t0,10         # $t0 = $t0 / 10 (save the division in $t0)
95
96
97
98  la $a0, result          # print message
99  li $v0, 4
100 syscall
101
102 move $a0, $t1
103
104 li $v0, 1                # print the value
105 syscall
106
107
108 li $v0,10               # end of main
109 syscall
110
111 input:
112
113     li $v0, 5            # take input from the user
114     syscall
115
116     move $t0, $v0        # move to a temp register
117     jr $ra               # return value
118
119 power:
120     bne $a1, $zero, recursion # if the power is greater than 1, then do some recursion
121     li $v0,1             # otherwise, return 1.
122     jr $ra               # Return to the calling function
123
124 recursion:
125     addi $sp, $sp, -4     # Allocate space for one integer on the stack
126     sw $ra, 0($sp)        # Store the return address on the stack
127     addi $a1, $a1, -1     # Decrement the power by 1.
128     jal power            # Call the power function with the new parameters
129     mul $v0, $a0, $v0     # Multiply the result by the base and save it as the new result
130     lw $ra, 0($sp)       # Restore the return address from the stack
131     addi $sp, $sp, 4     # Deallocate the memory on the stack
132     jr $ra               # Return to the calling function
133

```

\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400000
hi		0x00000000

Mars Messages

Run I/O

Rama Alyoubi Raghad Almutari Ryouf Alghamdi

```

Enter three digit of octal number      -1
invalid octal number please try again  290
One of the digits was above 7
Please enter a valid octal number      123
The result:      83
-- program is finished running --

```