Туре	Size	Latency (cycles)
Registers (frying pan)	8-16 bytes	0
L1 (table)	64 kb	3
L2 (shelves)	2 mb	20
L3 (storage fridge)	32 mb	~100
DRAM (supermarket)	4 gb	2-300
Disk (farm)	200+ gb	300,000

Runtime complexity vs "cache-miss complexity"

Always exponential