

Changes for ver 4 SiC/MIST PCB

Removed diod5 and diod6, they are not needed in finals version with SiC transistor

Connecting protective diods diod1, diod2, diod3 and diod4 to +3.3V. Renamed to D7 to D10

Changed connectors according to drawing 04/03/19

Changes to RS485-circuit

Removing R50

Connecting DE to PA12 with net DE485

Connecting /RE to PA11 with net /RE485

This will need changes in the program

	DE	/RE
Driver and receiver enabled	High	Low
Driver enabled, receiver disabled	High	High
Driver disabled, receiver enabled	Low	Low
Driver and receiver disabled	Low	High

Transmitting to piezolegs DE high and /RE high

Receiving from piezolegs DE low and /RE low

Low power when not communicating with Piezolegs: DE low, /RE high