4-way Trachie Signal
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TBCSE-IP
Hardware Design Lab.

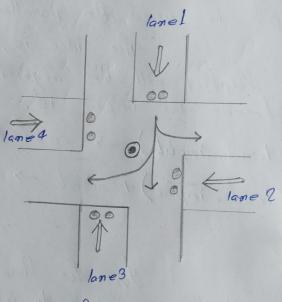
Design a system fon a traffic signal at a 9-way enossing.

the 24 hm. clock designed in the powerious assignment is used here.

Basic design:

There are of lanes. When one lane is open, all others are closed. In this way, when a lane is open, the vehicles can go straight, two left on turn right aithout any problem.

light control:



Sigure - lane 1 is open

At every lane, the green light is connected to the control line for that lane and the sud light is complement of the green light line. The control line of a lane is only high, when all other control lines are low. That means, when one lane has a green signal on, all other lenes will have a red signal on.

mod 4 counter, so that after a contain time interval, the 4 control lines change slate.

slate. The sime interval between the slate change is monitored by a simen. The simen

is basically a med 60 counter with a "net busy" pin. When the pin is connected to a low line, the timen is a simple med 60 counter (designed in the previous assignment). Esten the "not bersy" line is high, the timer is capped at 29, so that it becomes a mod 30 counter (design similar to that of the mod 14 counter.) In this way, the state of the signals change every minute, when the traffic is busy, and every 30 seconds when the truffic is not busy. The state of the busy-hows-selecton: Here comes the clock designed provious assignment.

Opto this point, the whole system is independent of the chek. Only the busy hours depend on the clock. The system is designed, such that 08:00:00 - 9:59:59 and 17:00:00 - 18:59:59 are busy hours. The mode counter and the Simen cincuit has pins for this berry-hour-selector to switch between a 60 second interval on a 30 second interval. be ligh when the houn at clock is 8, on 9 or 17 on 18. the impacts are too Abil lines, that represent the two digits of the hour display system of the clock, Let them be HO and HI. = Busy = (HO3. HO2. HO, HO,). (HI3. HI2. HI, HI6) + (HI, HI, HI). (HO3, HO2! HO, HO) + (FIZ FIZ. HI, . HI.). (HO, . HO, . HO.) + (HI3 . HI2 . HI, . HI6) . (HO3 . HOL . HO, HO)

the mod4 counter is controlling the oi4 way signal said-thing system.

The 60/30 second Linner is controling the mod 4 counter The busy hown selector circuit is The four outperts of the clock control the bessy hown selector - His is the bosic design of the 4-way touble signal system. (, on , on , if , on); (m , m , on , m

