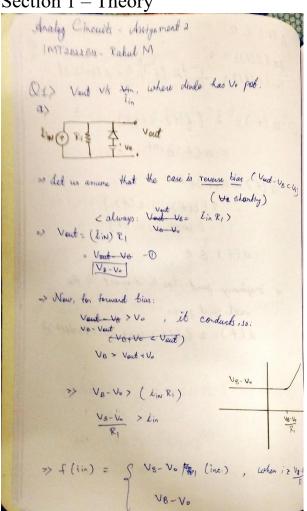
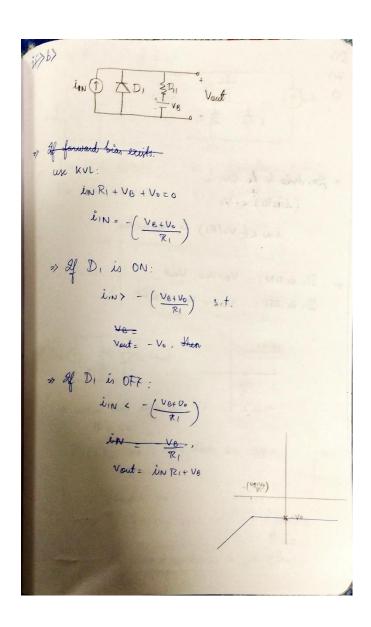
ANALOG CIRCUITS THEORY ASSIGNMENT 1

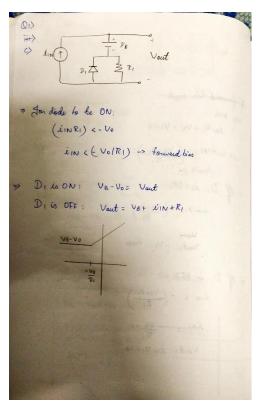
Name: Rahul Mukundhan(IMT2022518)

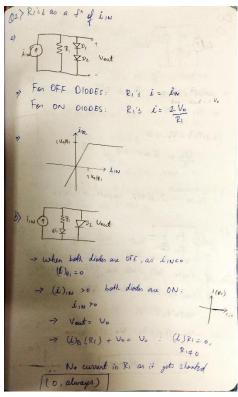
mail: rahul.mukundhan@iiitb.ac.in

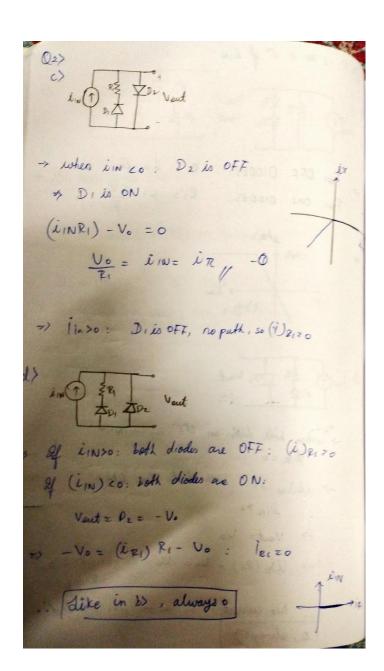
Section 1 – Theory

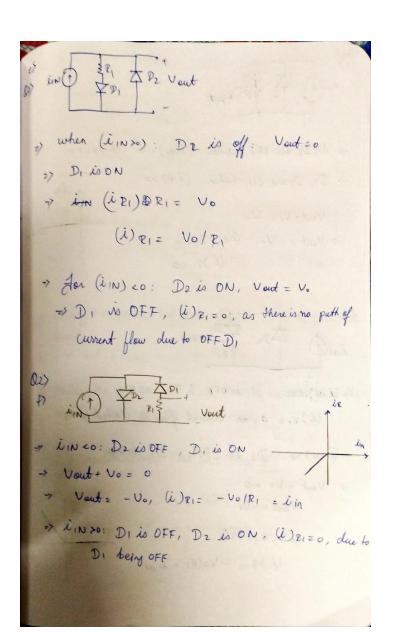


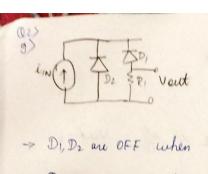












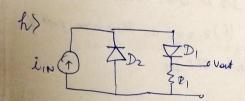
-> DI, D2 are OFF when (iIN) co: (i) p1 >0

-> DI, Drare ON, when (in) to:

Vout + Voz Vo

-> Vaut = Vo, (iRI) RI=0

(i) R1 =0



+ (in) co: Dissoff & Dz ison:

(i) R1 = 0, as current flow is blocked by P,

TUNDO: DZ is OFF, D, ison.

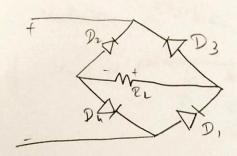
> Vout + V0 = 0

Vout = - Vo

(Nei) Ri=-Vo

(i) R1= - Vo/R1 = iin

(3) Jull-wave Rectifier



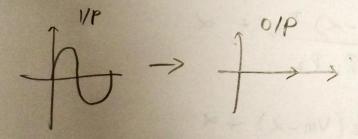
Problem: connection of D3

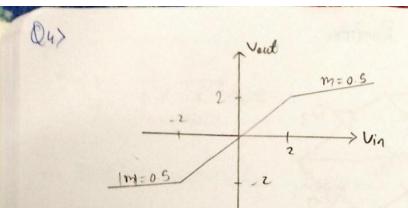
AD3, but neversed here

» During the positive half cycle, Dz & Dz will he reverse

→ Vout 20, due to i in Plzo

- => During the negative half cycle, D1&D3 are ON, Shout the input & once again, no current via PL.
- => No rectification, Vout is always [0]

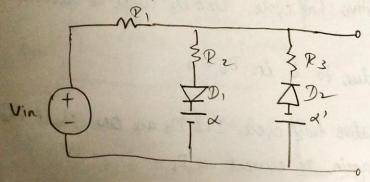




& Criven: 1K2 m, diodes (Vozo), other component

=> Lake &, &' as 2,-2 V respectively, as:
m goes from 1 to 1/2

(m:slope)



Rizika Rzzika Rzzika

=> 2f Vin> x, D2 is OFF
D1 is ON

Vout = Vin-x) Rz + x
RI+Rz

= 1/2 (VIn-2) + x

z Vin +1

Ow X' < Vined: D, &Dz are OFF

i. Voute Vin, as 1=0

if Vin < x': Di is OFF

Dz is ON

Vout = -2 + (Vin + 1×11)R3

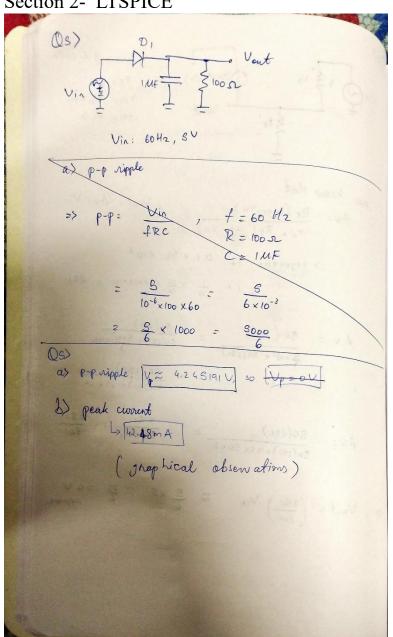
 $= -2 + (V \ln + 2) R_3$ $R_1 + R_3$

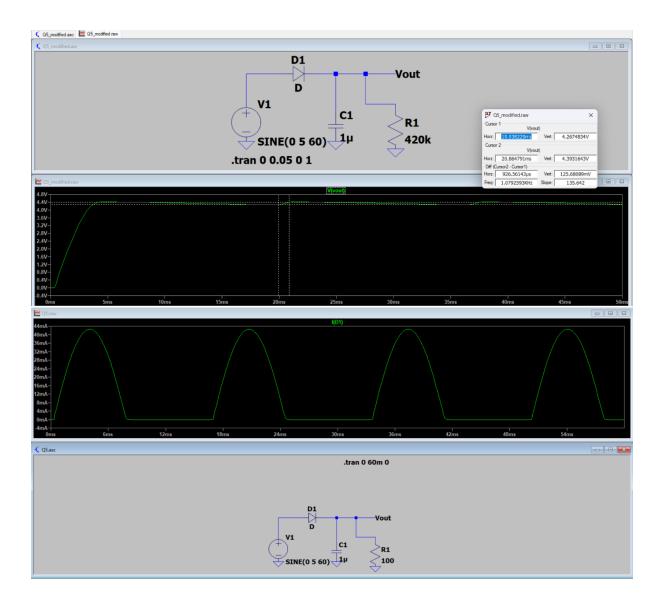
= -2 + 1/2 (Vin+2)

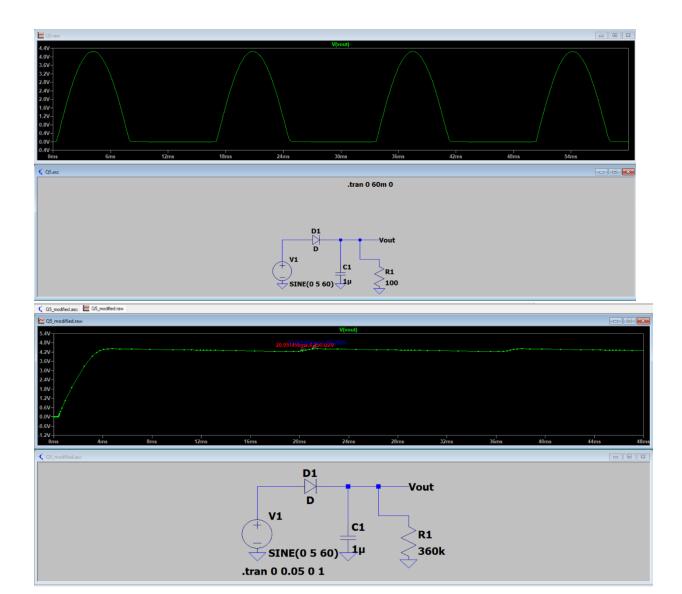
= Vin/2-1

< Vin /2 comes when RI=RZ=R3=1Ksl>

Section 2- LTSPICE







QB) or for this let value of me be R (here Vo= 0 V) -> 200 mV > Vp. → 0.2 ≥ S 60×R×106 -> R > S × 10 6 × S \rightarrow R 2 $\frac{25}{60}$ $\times 10^6$ R2 3/12 × 106 Rz 416, 666.67 s R2 ≈ 417 km (Lake R= 420ks) A Vo= 0.76 V, Vp= 4.24 V D.2 x 60 x 10 - 6 Rz 393,333 sc R % 353 kn (2)

