**RESUME**

**Bollikonda Rambabu**

**Address: 3-26/2**

**Hanuman temple street ,**

**Muthyalagudem**

**Khammam – 507159 Email : bollikondarambabu14@gmail.c om**

**Telangana. Mobile : 9948967405**

**Career Objective:**

To ensure a challenging position in a growing organization where I would be able to utilize my capabilities to the best extent and in the process add value to the organization and my career.

**Educational Qualifications:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Educational  Qualifications** | **Board/University** | **Name of the College/School** | **Year of passing** | **Percentage**  **Obtained** |
| B-Tech(ECE) | JNTUH | Khammam institute of technology & sciences | 2021 | 62 |
| Intermediate | Board of Intermediate education | Excellent junior college | 2016 | 89 |
| S.S.C. | Board of Secondary      Education | ZP high school | 2014 | 82 |

**Technical Skills:**

* Programming languages : Python, Full stack .Net
* Tools : MS-office
* Web : HTML, CSS, Java script

**Strengths:**

* Enthusiastic Team Player.
* Good Communication and Interpersonal Skills.
* Hard Working achiever.
* Honest and Self motivator.

**Projects:**

**Academic Mini Project :**

**Title**:- Adiabatic technique for power efficiency logic circuit design.

**Description:**

Adiabatic Logic circuits are one of the most efficient technology that are being used today to minimize power consumption. Different adiabatic logic families such as ECRL, 2N-2N2P and PFAL are investigated in this paper all simulations have been carried out with W/L ratio of the all circuits kept same for fair comparison of results. HSPIC has been used at 65NM technology with supply voltage of 1V at 100Mhz frequency finally plotting of average power dissipation characteristics have been done with the help of graph and different logic families comparisons have been done.

**Responsibilities**:

* Gathering required information and prepare the presentation
* Managing, coordinating, planning and executing project
* Interacting with faculty and getting the required guidance
* Project documentation and demonstration

**Academic Main Project :**

**Title**:- Electronics voting machine using face recognition and aadhar verification with IOT

**Description:**

The device which we use for the voting process is an electronic voting machine with highly secured steps such as having unique number of aadhar card, face recognition. This system provides best solution to problems related to the voting system. In this the voting process can done using face recognition and aadhar verification.

**Responsibilities**:

* Interacting with faculty and getting the required guidance
* Integrating chips and other devices and testing
* Managing, coordinating, planning and executing project
* Training, Project documentation and demonstration

**Personal Information:**

Father's Name : Venkanna

Mother’s Name         : Bharatamma

Marital Status : Single

Nationality  : Indian

Languages Known  : English, Telugu

**Declaration:**

I hereby declare that the above information furnished is true and correct to the best of my knowledge.

**Date:**

**Place: Hyderabad                                                                                        (B.Rambabu)**