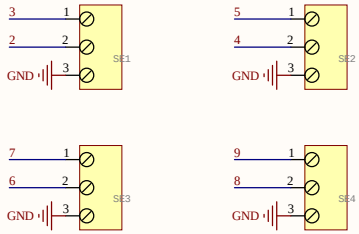
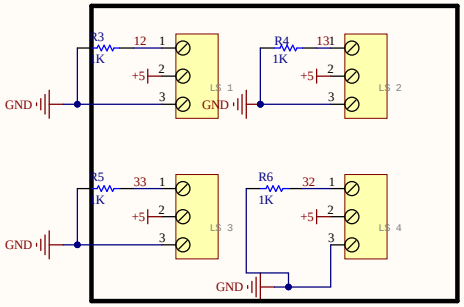
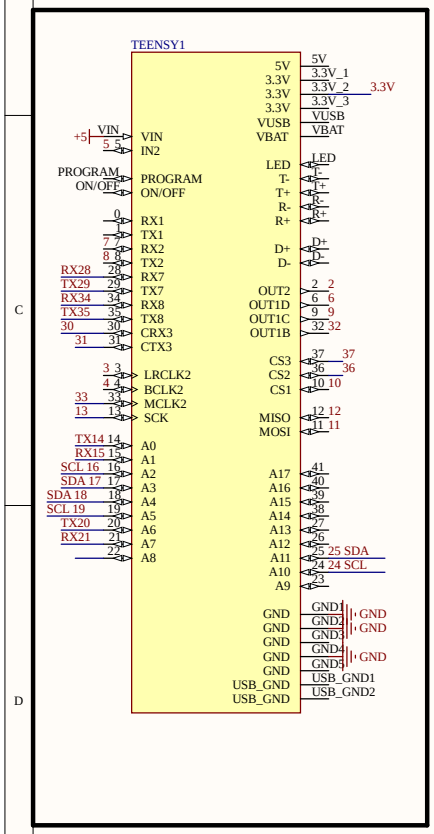
Cytron

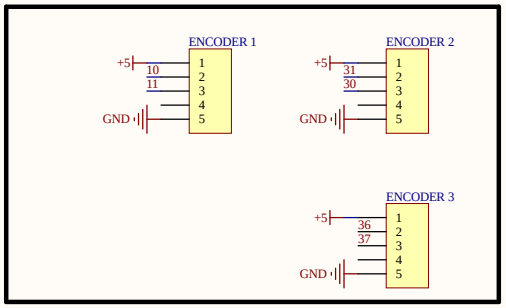


Limit Switch

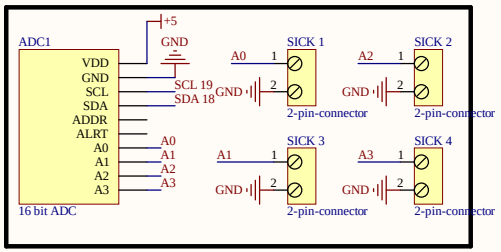




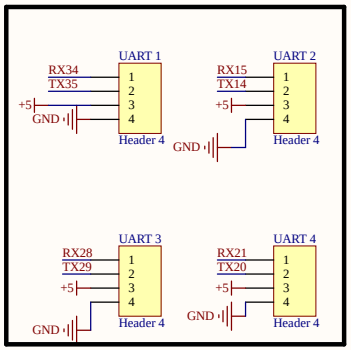
ENCODER INTERRUPT PINS

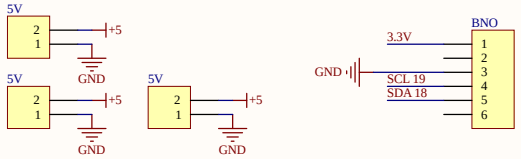


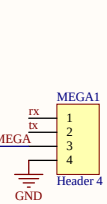
ADC



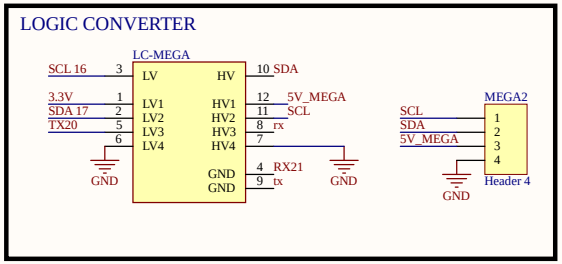
UART



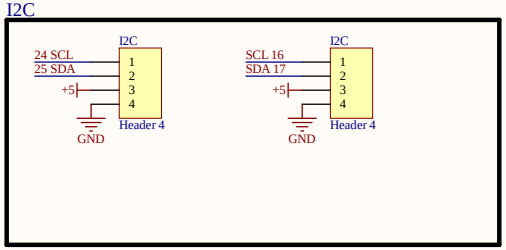




LOGIC CONVERTER



I2C



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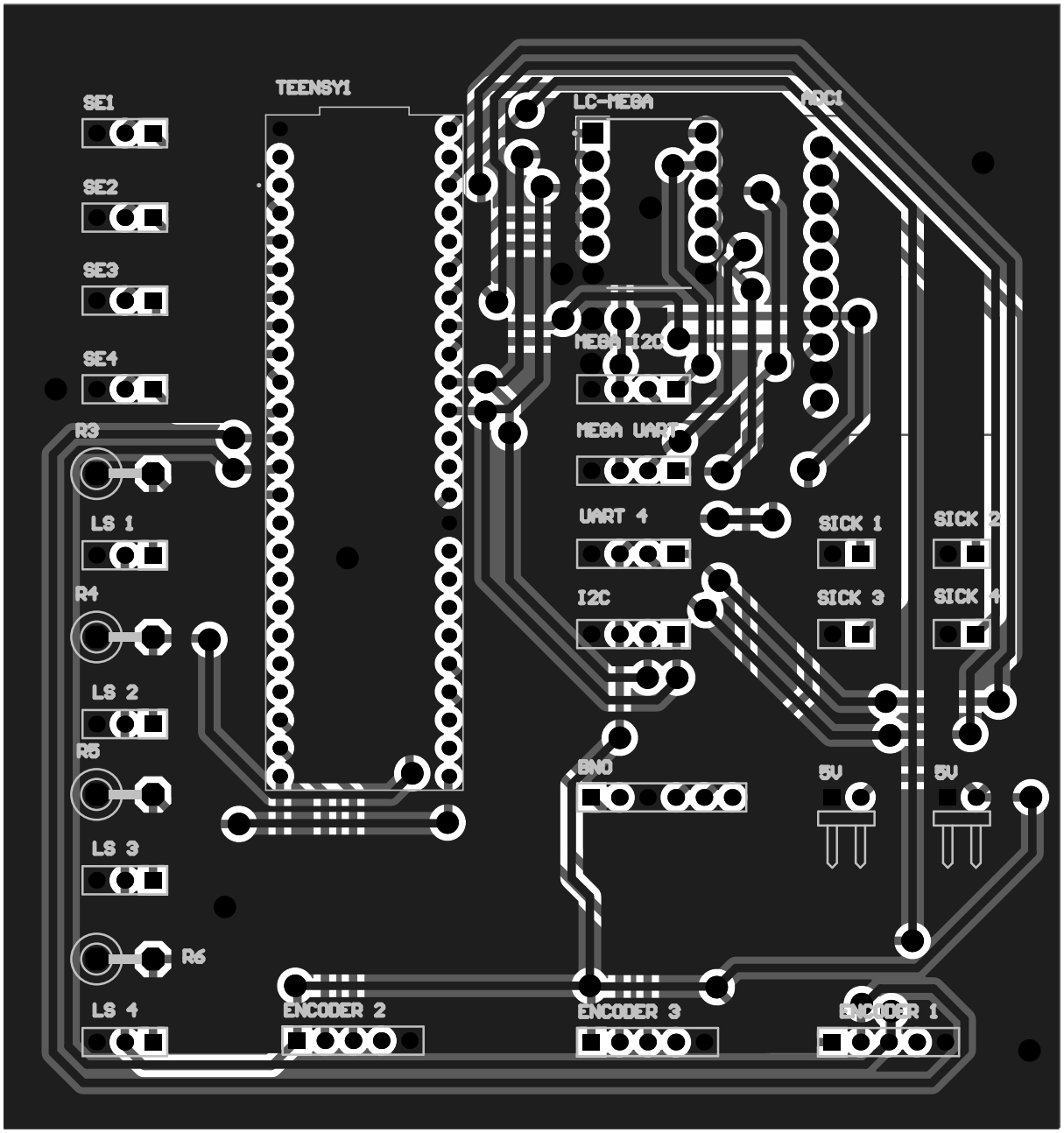
SE1
SE2
SE3
SE4
TEENSY1
LC-MEGA
MEGA I2C
MEGA UART
UART 4
I2C
R3
LS 1
R4
LS 2
R5
LS 3
R6
LS 4
BNO
5V
5V
SICK 1
SICK 2
SICK 3
SICK 4
ENCODER 2
ENCODER 3
ENCODER 1

Comment	Description	Designator	Footprint	LibRef	Quantity
Header 2H	Header, 2-Pin, Right Angle	5V	HDR1X2H	Header 2H	3
16 bit ADC		ADC1	16 bit ADC	CMP-010-000000-1	1
Header 6	Header, 6-Pin	BNO	HDR1X6	CMP-022-000108-1	1
Header 5	Header, 5-Pin	ENCODER 1, ENCODER 2, ENCODER 3	HDR1X5	Header 5	3
Header 4	Header, 4-Pin	I2C, MEGA1, MEGA2, UART 1, UART 2, UART 3, UART 4	HDR1X4	Header 4	8
BOB-12009	LOGIC LEVEL CONVERTER - BI-DIREC	LC-MEGA	FP-BOB-12009-MFG	CMP-157458-000001-1	1
PHOENIX CONNECTOR		LS 1, LS 2, LS 3, LS 4, SE1, SE2, SE3, SE4	HDR1X3	3-PIN-CONNECTOR	8
1K	Standard resistor	R3, R4, R5, R6	MY_(1w)_0613V	Res	4
2-pin-connector		SICK 1, SICK 2, SICK 3, SICK 4	HDR1X2	2-pin-connector	4
DEV-16771	RT1062 Teensy 4.1 series ARMÂ® CortexÂ®-M7 MPU Embedded Evaluation Board	TEENSY1	MODULE_DEV-16771	CMP-010-000002-1	1