

**Hanoi University of Science and Technology**

School of Information and Communication Technology



# **Project Report: Game Tetris using STM32CubeIDE hardware and software**

IT4210E - Embedded Systems

## **Group Members:**

Group 5 - Class: 161346

Luong Ngoc Vu Long - 20235967

Tran Sy Nguyen - 20235985

Nguyen Vu Anh Khoa - 20235957

## **Lecturers:**

Prof. Ngô Lam Trung

January 23, 2026

# Contents

<b>1</b>	<b>Introduction</b>	<b>2</b>
<b>2</b>	<b>Hardware Design</b>	<b>2</b>
2.1	Development Board Specifications . . . . .	2
2.2	Peripheral Configuration . . . . .	2
2.2.1	Display Subsystem (LTDC & DMA2D) . . . . .	2
2.2.2	Audio Subsystem (PWM) . . . . .	2
2.2.3	Input Controls (GPIO & EXTI) . . . . .	2
<b>3</b>	<b>Software Design</b>	<b>2</b>
3.1	Software Architecture Overview . . . . .	2
3.2	FreeRTOS Configuration . . . . .	2
3.3	Game Logic (The Model) . . . . .	2
3.3.1	Grid Representation . . . . .	2
3.3.2	Game Loop (‘tick’) . . . . .	2
3.4	Audio Engine Implementation . . . . .	2
3.5	User Interface Design . . . . .	2
3.5.1	Main Menu . . . . .	2
3.5.2	Game Screen . . . . .	2
3.6	Input Debouncing . . . . .	2
3.7	Memory Management . . . . .	2
<b>4</b>	<b>Results and Conclusion</b>	<b>2</b>
4.1	Project Outcomes . . . . .	3
4.2	Future Improvements . . . . .	3

# 1 Introduction

This project aims to replicate the classic arcade game "Tetris" on an embedded system platform. The primary goal is to demonstrate the integration of real-time operating systems (FreeRTOS), graphical user interfaces (TouchGFX), and hardware peripheral control (GPIO, Timers, Interrupts) on the STM32F429I-DISCO development board.

The system features a 240x320 pixel color display, a dedicated audio engine for background music and sound effects using PWM, and physical button controls for game interaction. The software architecture is designed using the Model-View-Presenter (MVP) pattern provided by TouchGFX, ensuring a clean separation between game logic and visual rendering.

## 2 Hardware Design

### 2.1 Development Board Specifications

### 2.2 Peripheral Configuration

#### 2.2.1 Display Subsystem (LTDC & DMA2D)

#### 2.2.2 Audio Subsystem (PWM)

#### 2.2.3 Input Controls (GPIO & EXTI)

## 3 Software Design

### 3.1 Software Architecture Overview

### 3.2 FreeRTOS Configuration

### 3.3 Game Logic (The Model)

#### 3.3.1 Grid Representation

#### 3.3.2 Game Loop ('tick')

### 3.4 Audio Engine Implementation

### 3.5 User Interface Design

#### 3.5.1 Main Menu

#### 3.5.2 Game Screen

### 3.6 Input Debouncing

### 3.7 Memory Management

## 4 Results and Conclusion

**4.1 Project Outcomes**

**4.2 Future Improvements**