

Hanoi University of Science and Technology

School of Information and Communication Technology



Project Report: Game Tetris using STM32CubeIDE hardware and software

IT4210E - Embedded Systems

Group Members:

Group 5 - Class: 161346

Luong Ngoc Vu Long - 20235967

Tran Sy Nguyen - 20235985

Nguyen Vu Anh Khoa - 20235957

Lecturers:

Prof. Ngô Lam Trung

January 26, 2026

Contents

1	Introduction	2
2	Hardware Design	2
2.1	Development Board Specifications	2
2.2	Peripheral Configuration	2
2.2.1	Display Subsystem (LTDC & DMA2D)	3
2.2.2	Audio Subsystem (PWM)	3
2.2.3	Input Controls (GPIO & EXTI)	4
2.3	Random Number Generator (RNG)	4
3	Software Design	4
3.1	Model-View-Presenter (MVP) Architecture	4
3.2	The View: User Interface	5
3.2.1	Main Menu	5
3.2.2	Game Screen	5
3.3	The Model: Game Logic	6
3.3.1	Grid Representation	6
3.3.2	Game Loop ('tick')	6
3.4	The Presenter: Logic-UI Synchronization	6
3.5	System Integration (RTOS & Audio)	6
3.5.1	FreeRTOS Task Management	6
3.5.2	Audio Engine	7
4	System Integration & Challenges	7
4.1	Clock Tree Configuration for RNG	7
4.2	Input Debouncing	7
5	Results and Conclusion	8
5.1	Project Outcomes	8
5.2	Future Improvements	8

1 Introduction

This project aims to replicate the classic arcade game "Tetris" on an embedded system platform. The primary goal is to demonstrate the integration of real-time operating systems (FreeRTOS), graphical user interfaces (TouchGFX), and hardware peripheral control (GPIO, Timers, Interrupts) on the STM32F429I-DISCO development board.

The system features a 240x320 pixel color display, a dedicated audio engine for background music and sound effects using PWM, and physical button controls for game interaction. The software architecture is designed using the Model-View-Presenter (MVP) pattern provided by TouchGFX, ensuring a clean separation between game logic and visual rendering.

2 Hardware Design

2.1 Development Board Specifications

The project utilizes the STM32F429I-DISCO kit, which is built around the STM32F429ZIT6 microcontroller. Key specifications utilized in this project include:

- **Core:** ARM Cortex-M4 with FPU, running at 168 MHz (configured via PLL).
- **Flash Memory:** 2 MB (Storing code and const assets like fonts/images).
- **RAM:** 256 KB Internal SRAM + 64 Mbits External SDRAM (used for Frame Buffers).
- **Display:** 2.4" QVGA TFT LCD via LTDC interface.

2.2 Peripheral Configuration

The hardware configuration is generated using STM32CubeMX (inside STM32CubeIDE).

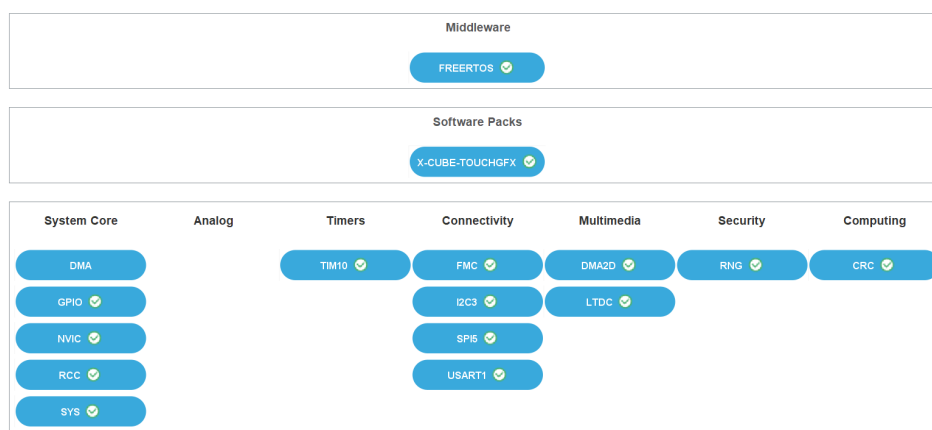


Figure 1: STM32CubeMX System and Peripheral Initialization Overview

- **Logic:** The frequency of the PWM signal controls the musical pitch (Note), and the duty cycle controls the volume.

2.2.3 Input Controls (GPIO & EXTI)

The game is controlled via four external push buttons connected to GPIO pins configured as external interrupts (EXTI).

Function	Pin	Port	Mode
Rotate Piece	Pin 12	GPIOB	EXTI Rising/Falling
Move Right	Pin 13	GPIOB	EXTI Rising/Falling
Soft Drop	Pin 2	GPIOG	EXTI Rising/Falling
Move Left	Pin 3	GPIOG	EXTI Rising/Falling

Table 1: GPIO Pin Mapping for Game Controls

2.3 Random Number Generator (RNG)

To ensure fair and unpredictable gameplay, the hardware Tetromino generation relies on the STM32's built-in Random Number Generator (RNG) peripheral. The peripheral requires a precise 48 MHz clock (PLL48CLK) and is configured to provide entropy for the piece spawning logic. Detailed implementation challenges regarding the clock tree configuration are discussed in Section 4.1.

3 Software Design

3.1 Model-View-Presenter (MVP) Architecture

The software architecture is centered around the Model-View-Presenter (MVP) pattern provided by the TouchGFX framework. This pattern ensures a strict separation of concerns between the visual interface, the underlying game logic, and the synchronization layer that connects them.

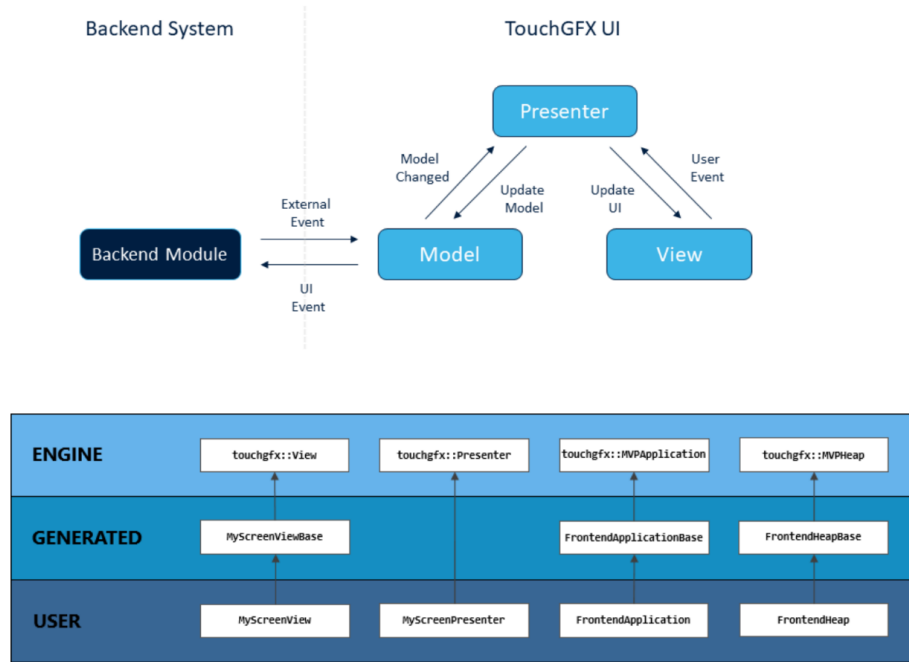


Figure 3: TouchGFX MVP Architecture and Class Hierarchy

- **View:** Responsible for the visual representation and layout.
- **Model:** Contains the core game state, rules, and mathematical logic.
- **Presenter:** Acts as the mediator, handling events from the View and updating the Model, or reflecting Model changes back to the View.

3.2 The View: User Interface

The View layer is implemented using C++ classes generated by TouchGFX. It focuses on rendering the graphical assets and handling the screen layout for the 240x320 portrait display.

3.2.1 Main Menu

Displays the "Tetris Art" logo and options to start the game. The background plays a slower, ambient menu theme.

3.2.2 Game Screen

The layout is optimized for the portrait screen, dividing the interface into functional zones:

- Center: 10x20 Game Matrix showing active and resting Tetrominos.
- Left Sidebar: HOLD piece slot and Level indicator.
- Right Sidebar: NEXT piece preview and Score display.

3.3 The Model: Game Logic

The Model layer maintains the internal state of the Tetris grid and implements the rules of the game. It is independent of the GUI framework, allowing for easier testing and logic updates.

3.3.1 Grid Representation

The 10x20 game board is represented as a 2D array: 'signed char grid[20][10]', where each cell stores a value corresponding to a Tetromino type or remains -1 if empty.

3.3.2 Game Loop ('tick')

The 'tick()' function is the entry point for game updates, called every frame (60Hz). It handles gravity, collision detection via 'isCollision()', and line clearing logic.

```
1 void Model::tick()
2 {
3     if (isGameOver || isPaused) return;
4
5     tickCounter++;
6     if (tickCounter >= dropSpeed)
7     {
8         tickCounter = 0;
9         step(); // Gravity drop
10    }
11    // ... Input queue processing ...
12 }
```

3.4 The Presenter: Logic-UI Synchronization

The Presenter layer serves as the bridge. Classes like 'GameViewPresenter' listen for notifications from the Model (via 'ModelListener') and trigger updates in the View.

When the Model state changes (e.g., a piece moves), the Presenter is notified and calls the corresponding View methods to refresh the display, ensuring the visual layer stays perfectly synchronized with the logical state.

```
1 // Example from GameViewPresenter.cpp
2 void GameViewPresenter::modelStateChanged()
3 {
4     view.updateBoard(); // Signal the View to redraw based on new Model
5     data
6 }
```

3.5 System Integration (RTOS & Audio)

The application logic sits atop a real-time foundation that manages hardware resources and asynchronous background tasks.

3.5.1 FreeRTOS Task Management

Three primary tasks ensure smooth operation:

- **GUI_Task:** Executes the TouchGFX engine and MVP logic (High Priority).
- **DefaultTask:** Handles button debouncing and system monitoring.
- **SoundTask:** Processes audio requests in the background (Low Priority).

3.5.2 Audio Engine

To maintain performance, audio is handled via a Producer-Consumer pattern. The Game Model (Producer) sends 'TrackID' requests to a queue, which are processed by the 'Sound-Task' (Consumer) to drive the PWM-based buzzer.

4 System Integration & Challenges

4.1 Clock Tree Configuration for RNG

A significant challenge during hardware integration was the 48 MHz clock requirement for the RNG. The STM32F429 reaches its maximum performance at a SysClk of 180 MHz, but the Main PLL cannot generate both 180 MHz and 48 MHz simultaneously due to integer divider limitations.

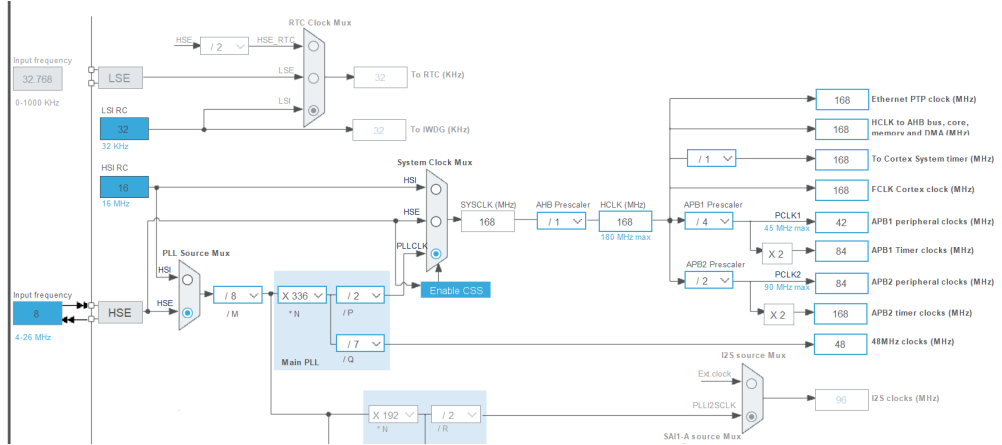


Figure 4: Final Clock Configuration (168 MHz SysClk, 48 MHz PLL48CLK)

- **The Issue:** At 180 MHz (VCO = 360 MHz), the PLLQ divider would need to be 7.5 to reach 48 MHz, which is not supported. Deviations from 48 MHz lead to peripheral initialization failure and "Clock Error" flags.
- **The Solution:** We adjusted the system clock to 168 MHz (PLLM=8, PLLN=336, PLLP=2, PLLQ=7).
- **VCO Calculation:** $(8 \text{ MHz}/8) \times 336 = 336 \text{ MHz}$.
- **Resulting Clocks:** SysClk = $336 \text{ MHz}/2 = 168 \text{ MHz}$; RNG Clock = $336 \text{ MHz}/7 = 48 \text{ MHz}$ (Exact).

4.2 Input Debouncing

Directly reading GPIO pins caused erratic behavior due to mechanical switch bounce. We implemented software debouncing using FreeRTOS Timers.

- When a button press interrupt occurs, a timer starts (e.g., 50ms).
- The input is only registered if the signal remains stable until the timer callback executes.

5 Results and Conclusion

5.1 Project Outcomes

The project successfully delivers a playable Tetris clone on the STM32F429I-DISCO.

- Performance: The game runs at a stable 60 FPS.
- Audio: Background music plays smoothly without interrupting game logic.
- Gameplay: All standard Tetris rules (rotation, wall kicks, line clears, leveling) are implemented.

5.2 Future Improvements

- Implement "Ghost Piece" visualization (indicated in design but currently simplified).
- Save high scores to internal Flash memory to persist after power loss.
- Add support for using the on-board Gyroscope (SPI5) for experimental tilt controls.