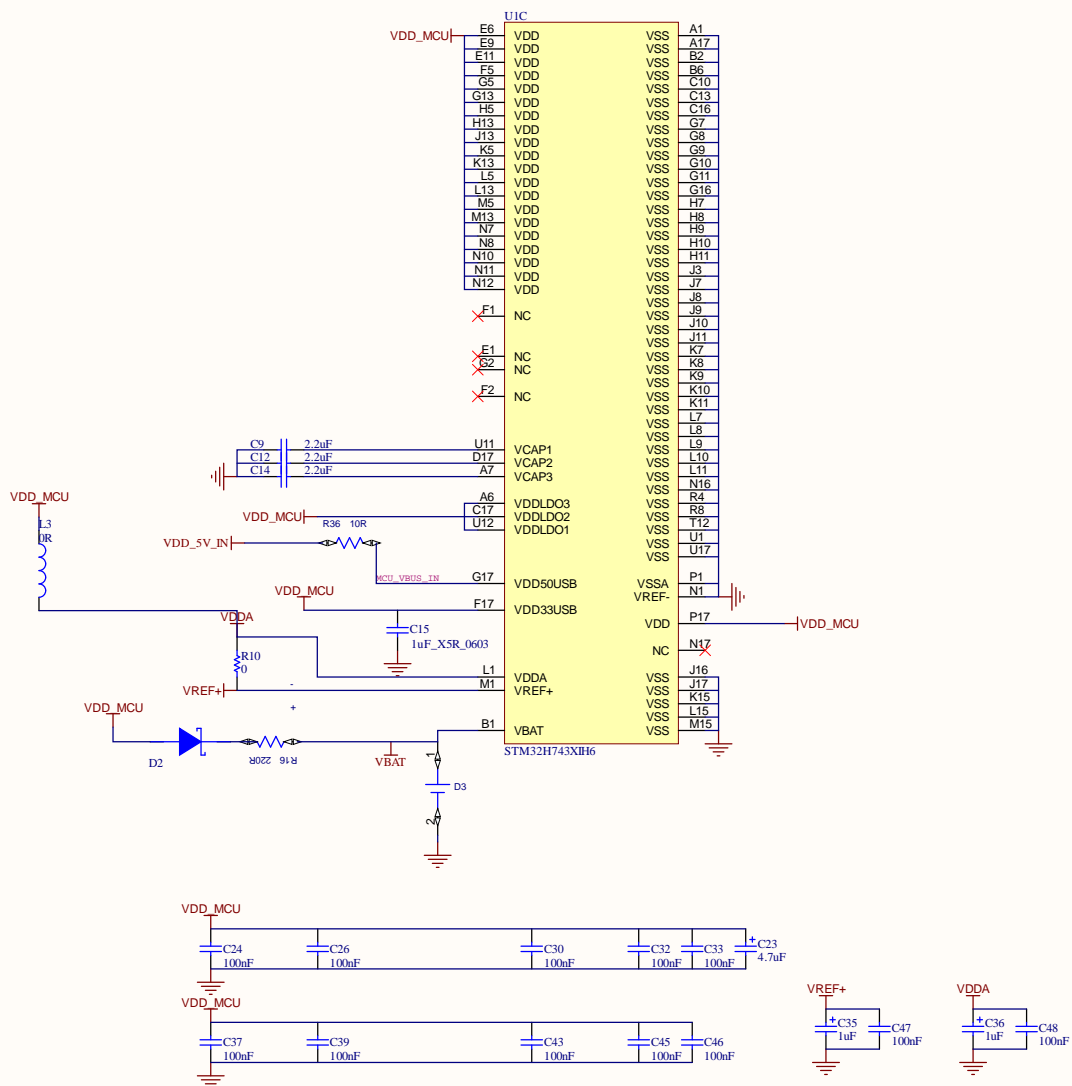



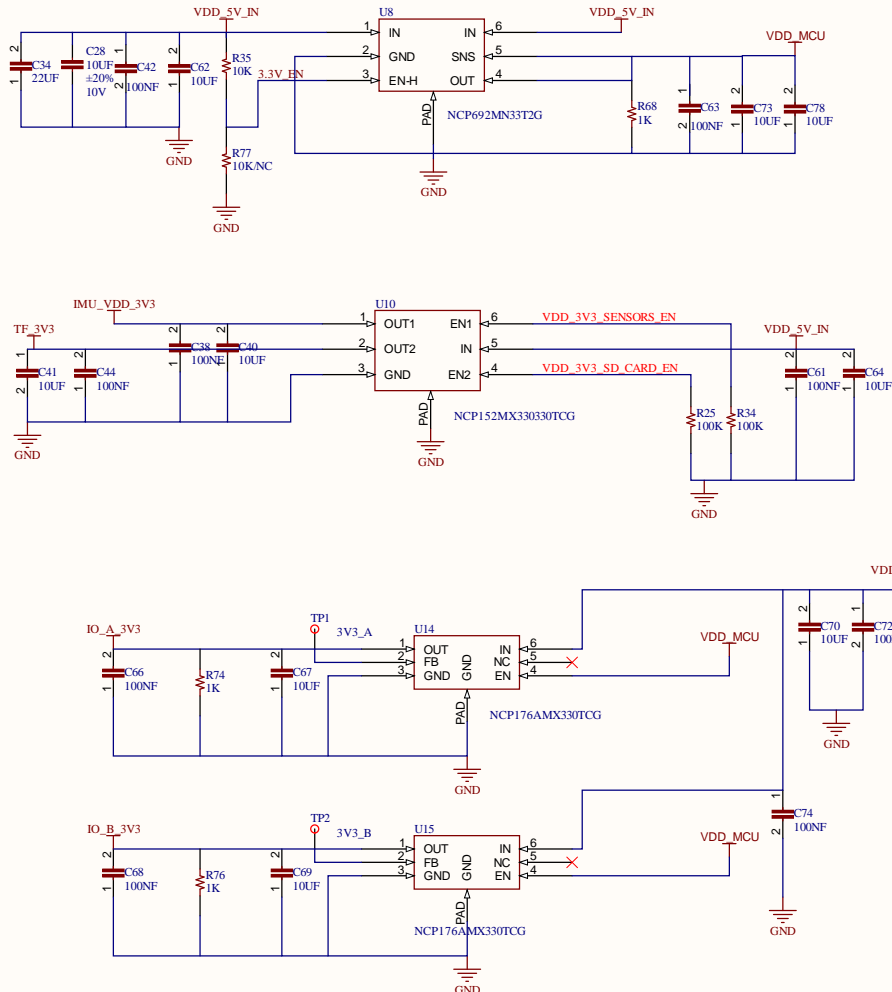
H7 MCU POWER



PROJECT:		 CUAV Tech Inc., Ltd	
PROJECT REVISION: RC04		DOCUMENT REVISION:	
TITLE <h1>H7 MCU POWER</h1>			
SIZE	FILE NAME	Drawn By:	
A3	02_MCU_Power_SchDoc	Ray	
Date:	2021/12/18	10:48:38	SHEET 2 OF 9

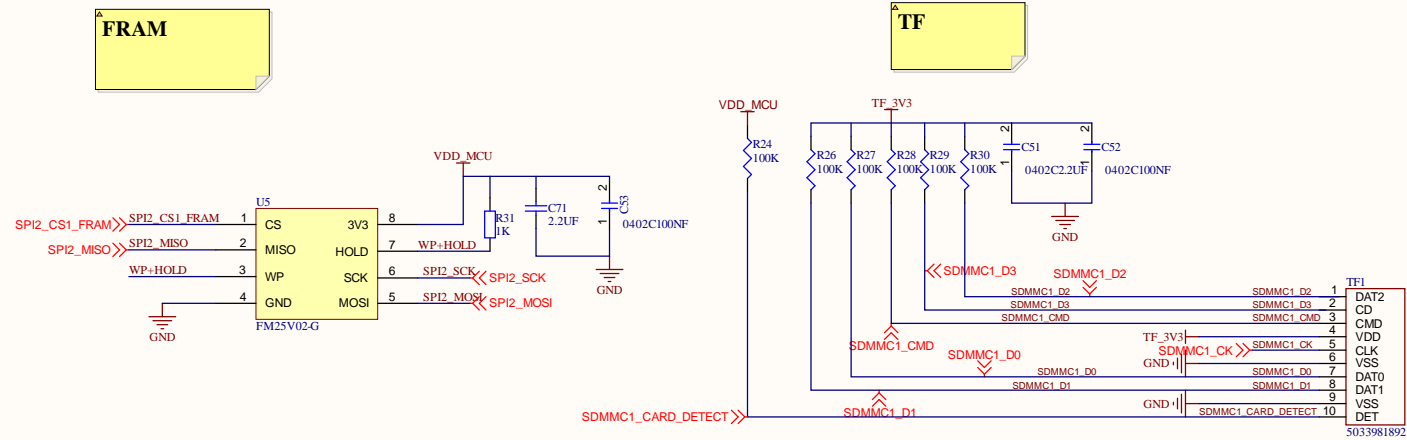
LDO Power


5V-->3.3V+3.3V MCU+TF CARD



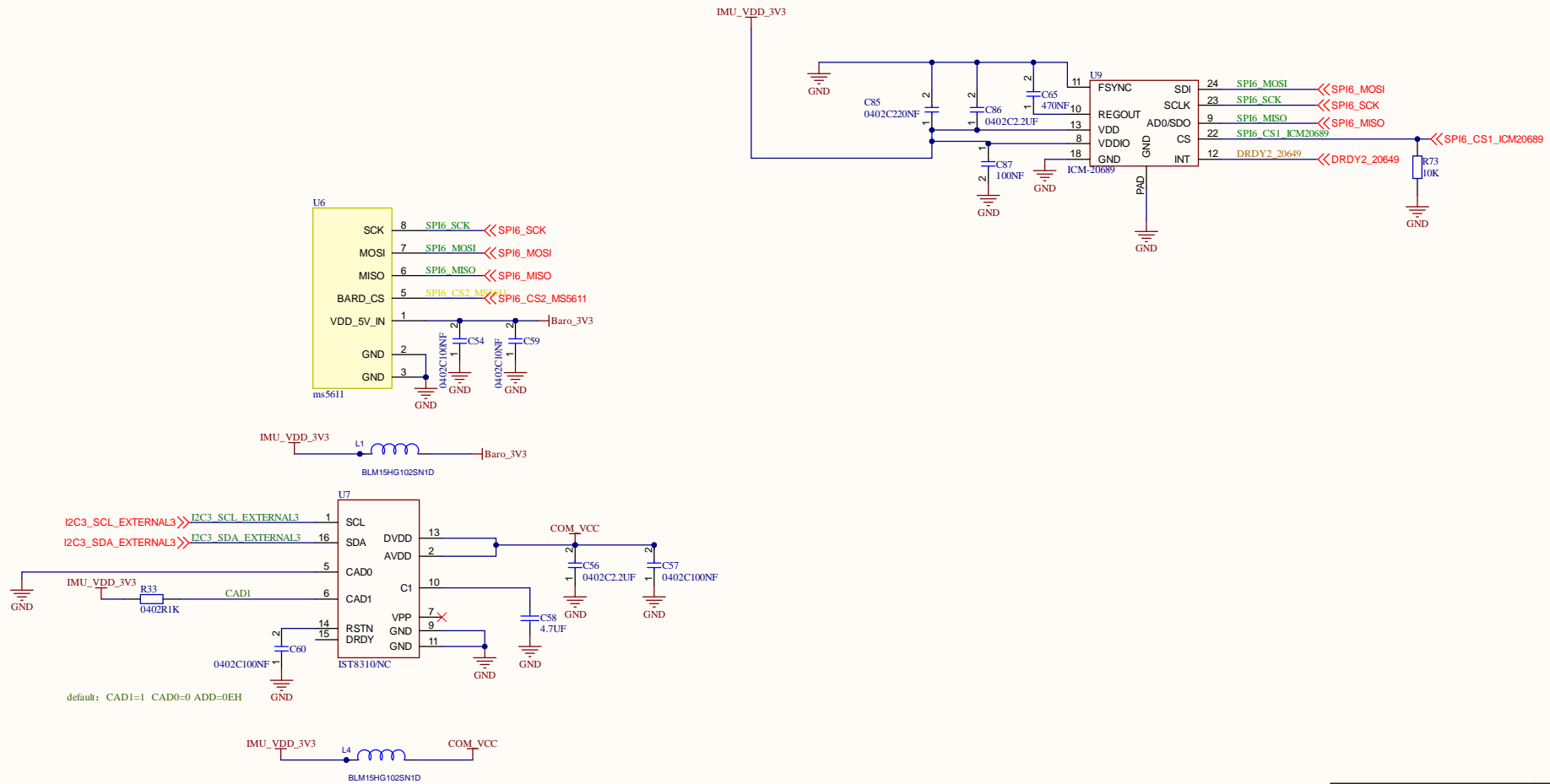
PROJECT: X7+ CORE		CUAV CUAV Tech Inc.,Ltd	
PROJECT REVISION: RC04		DOCUMENT REVISION:	
TITLE LDO Power			
SIZE: A3	FILE NAME 03_Power_LDO.SchDoc	Drawn By: Ray	
Date:	2021/12/18 10:48:38	SHEET	3 OF 9


TF CARD + MTD of STM32H7



PROJECT		X7+ CORE		 CUAV Tech Inc., Ltd	
PROJECT REVISION: RC04			DOCUMENT REVISION:		
TITLE					
TF CARD + MTD					
SIZE	FILE NAME		Drawn By:		
A3	04_TF_CARD+MTD.SchDoc		Ray		
Date:	2021/12/18	10:48:38	SHEET 4		OF 9

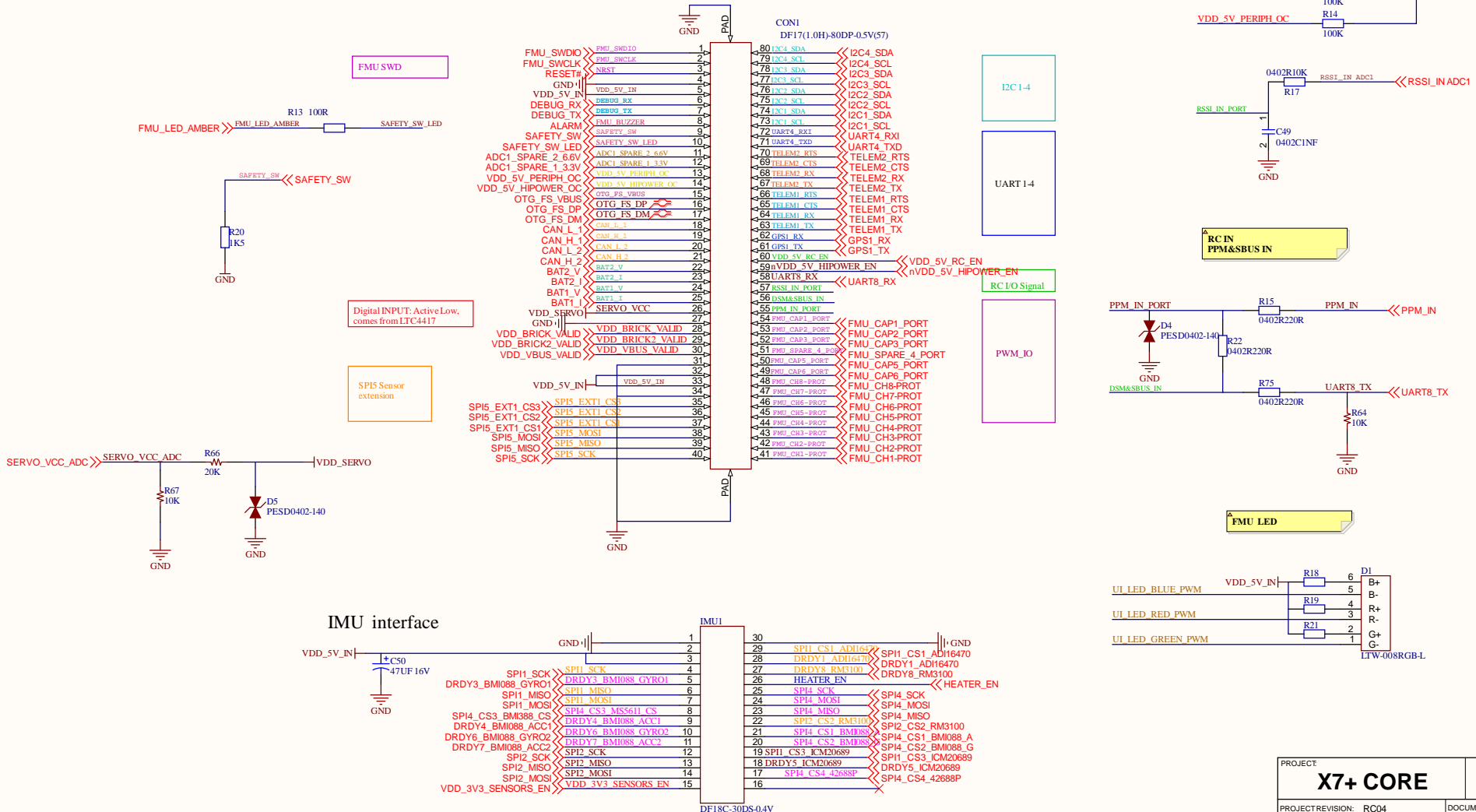
ON Board IMU



PROJECT		X7+ CORE				CUAV Tech Inc.,Ltd	
PROJECT REVISION: RC04				DOCUMENT REVISION:			
TITLE							
ON Board IMU							
SIZE		FILE NAME		Drawn By:			
A3		05_ONB_IMU.SchDoc		Ray			
Date:		2021/12/18 10:48:38			SHEET		5 OF 9

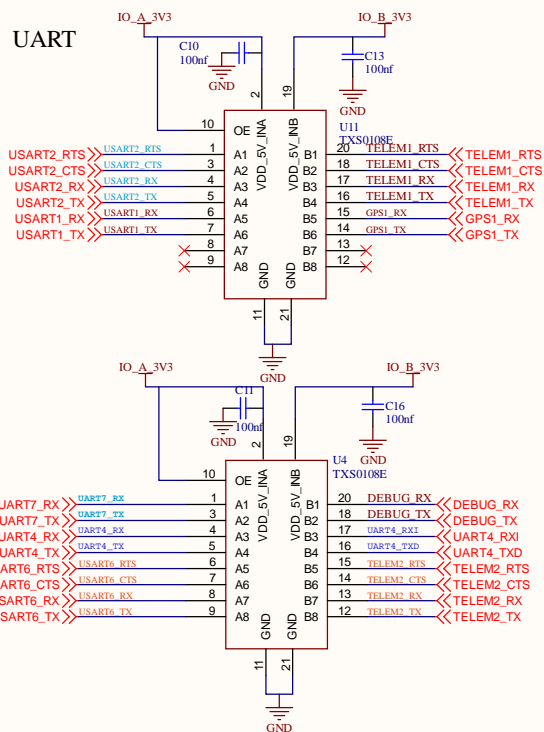
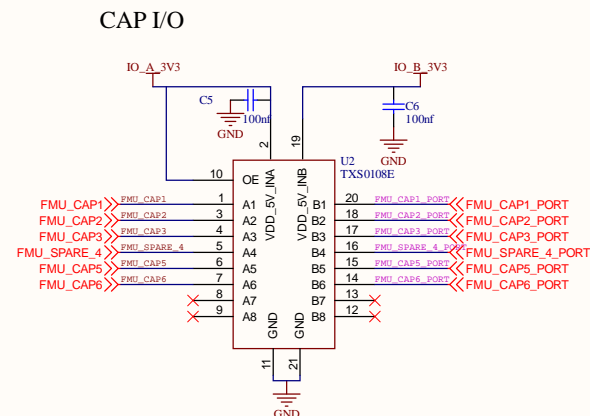
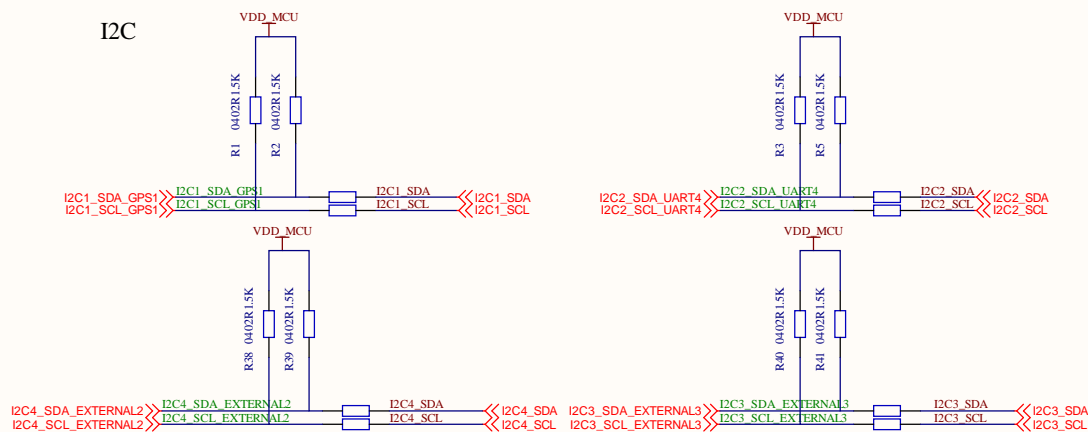
B_TO_B interface

80P板对板连接器定义只兼容CUAV V5+_CORE 标准，其他类似模块不能兼容
Only compatible with CUAV V5+_CORE module, other incompatible

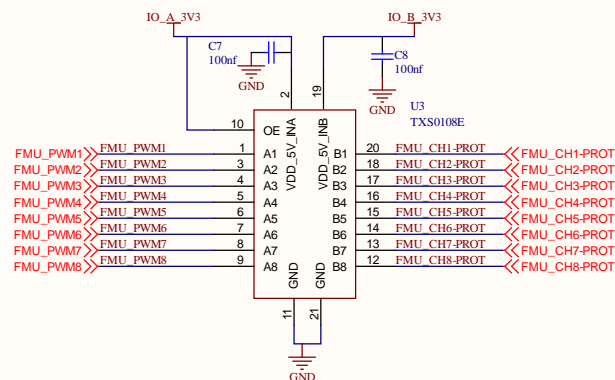


PROJECT: X7+ CORE		CUAV CUAV Tech Inc.,Ltd	
PROJECT REVISION: RC04		DOCUMENT REVISION:	
TITLE: B_TO_B interface			
SIZE: A3	FILE NAME: 06_Interface.SchDoc	Drawn By: Ray	
Date: 2021/12/18 10:48:38		SHEET 6	OF 9

IO Bridge

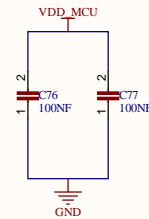
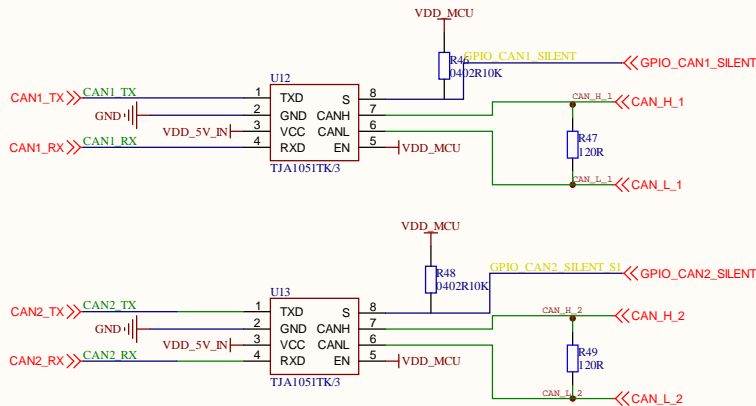
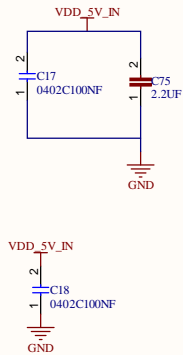



PWM I/O RAIL



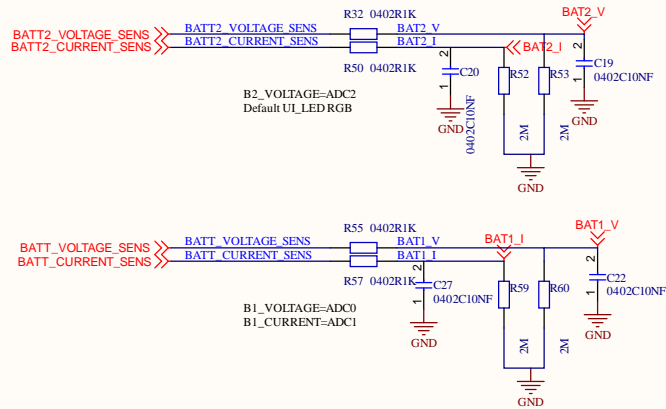
PROJECT:		X7+ CORE		CUAV		CUAV Tech Inc.,Ltd	
PROJECT REVISION: RC04				DOCUMENT REVISION:			
TITLE							
IO Bridge							
SIZE		FILE NAME		Drawn By:			
A3		07_IO_bridge.SchDoc		Ray			
Date:		2021/12/18		10:48:38		SHEET 7 OF 9	

CAN BUS

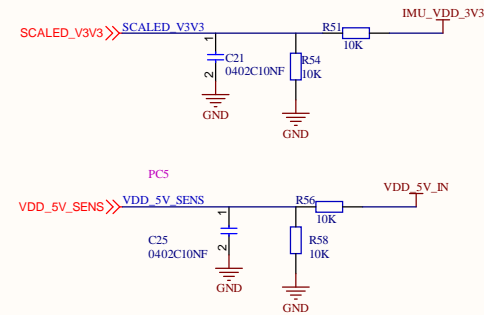


PROJECT		X7+ CORE				CUAV Tech Inc., Ltd.	
PROJECT REVISION: RC04				DOCUMENT REVISION:			
TITLE							
CAN BUS							
SIZE		FILE NAME		Drawn By:			
A3		08_CAN&USB.SchDoc		Ray			
Date:		2021/12/18 10:48:38			SHEET 8 OF 9		

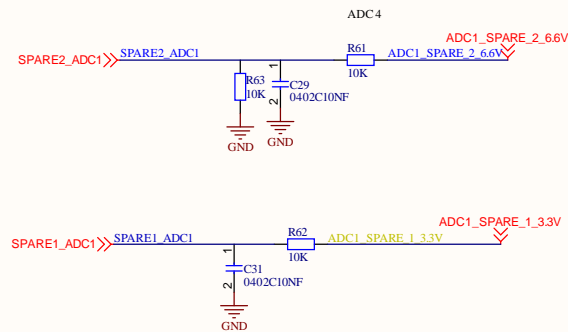
PM1 & PM2 voltage and current sensing




Core voltage detection



ADC of 3.3V + 6.6V



PROJECT		X7+ CORE		 CUAV Tech Inc.,Ltd	
PROJECT REVISION: RC04			DOCUMENT REVISION:		
TITLE					
ADC Rail					
SIZE	FILE NAME		Drawn By:		
A3	09_ADC_Rail.SchDoc		Ray		
Date:	2021/12/18	10:48:38	SHEET		9 OF 9

