

Machine Language Part 2

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Outlines

- Hack assembly programming
 - ➤ Registers and memory
 - ➤ Branching, variables, iteration
 - **≻**Pointers

Hack input / output

Hack assembly language (overview)

A-instruction:

@value // A = value

where *value* is either a constant or a symbol referring to such a constant

C-instruction:

dest = comp ; jump

(both *dest* and *jump* are optional)

where:

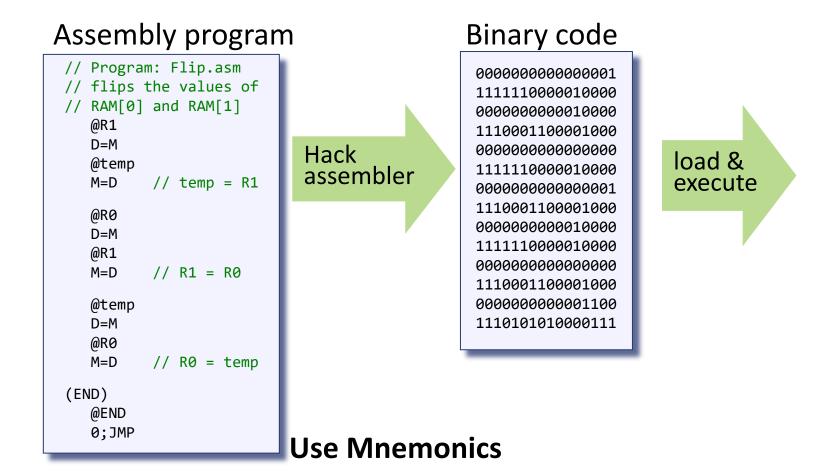
dest = | null, M, D, MD, A, AM, AD, AMD | (M refers to RAM[A])

jump = | null, JGT, JEQ, JGE, JLT, JNE, JLE, JMP

Semantics:

- Compute the value of comp
- Store the result in dest
- If the Boolean expression (comp jump 0) is true, jump to execute the instruction at ROM[A]

Hack assembler

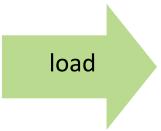


We'll develop a Hack assembler later in this module.

CPU emulator

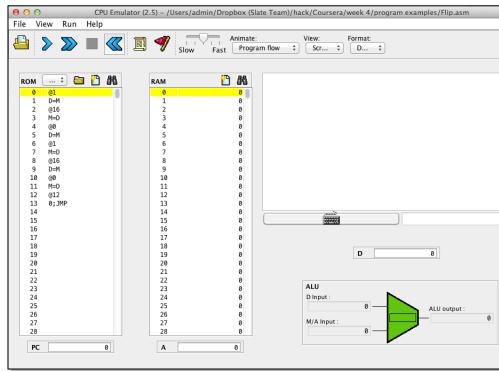
Assembly program

```
// Program: Flip.asm
// flips the values of
// RAM[0] and RAM[1]
   @R1
   D=M
   @temp
          // temp = R1
   M=D
   @R0
   D=M
   @R1
   M=D
          // R1 = R0
   @temp
   D=M
   @R0
   M=D
          // R0 = temp
(END)
   @END
   0;JMP
```



(The simulator translates from symbolic to binary as it loads)

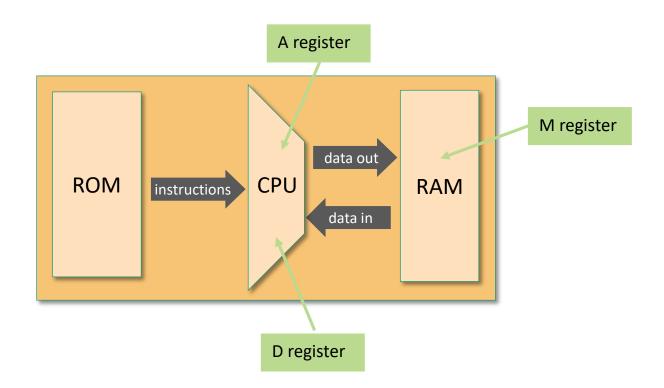
CPU Emulator



- A software tool
- Convenient for debugging and executing symbolic Hack programs.

Registers and memory

- D: Store data.
- A: Store data / address the memory.
- M: Currently addressed memory register: M = RAM[A].



Registers and memory

- D: Store data.
- A: Store data / address the memory.
- M: Currently addressed memory register: M = RAM[A].

Typical operations:

```
// D++
D=D+1
// D=10
@10
D=A
```

```
// D=RAM[17]
@17
D=M
// RAM[17]=D
@17
M=D
```

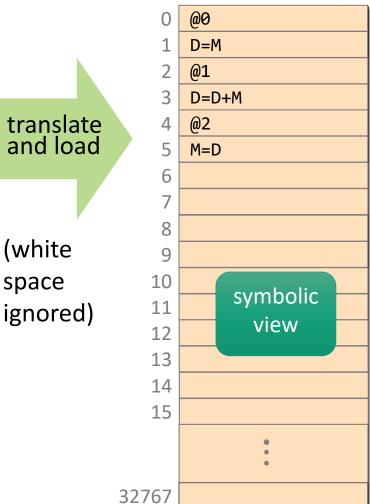
```
// RAM[17]=10
@10
D=A
@17
M=D
// RAM[5] = RAM[3]
@3
D=M
@5
M=D
```

Program example: add two numbers

Hack assembly code

```
// Program: Add2.asm
// Computes: RAM[2] = RAM[0] +
// RAM[1]
// Usage: put values in RAM[0],
// RAM[1]
00
D=M // D = RAM[0]
@1
D=D+M // D = D + RAM[1]
@2
M=D // RAM[2] = D
```

Memory (ROM)

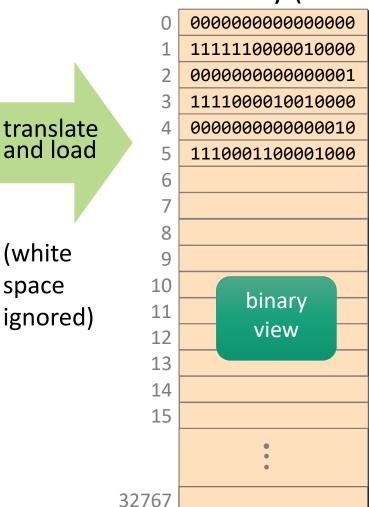


Program example: add two numbers

Hack assembly code

```
// Program: Add2.asm
// Computes: RAM[2] = RAM[0] +
// RAM[1]
// Usage: put values in RAM[0],
// RAM[1]
00
D=M // D = RAM[0]
@1
D=D+M // D = D + RAM[1]
@2
M=D // RAM[2] = D
```

Memory (ROM)



Terminate a program

Memory (ROM) Hack assembly code @0 // Program: Add2.asm D=M // Computes: RAM[2] = RAM[0] + @1 D=D+M // RAM[1] translate @2 // Usage: put values in RAM[0], and load M=D // RAM[1] @0D=M // D = RAM[0]10 @1 D=D+M // D = D + RAM[1]malicious code starts here ... @2 M=D // RAM[2] = DAttack on the computer 32767 10

Terminate a program

Hack assembly code

```
// Program: Add2.asm
// Computes: RAM[2] = RAM[0] + RAM[1]
// Usage: put values in RAM[0], RAM[1]

@ 0
D=M // D = RAM[0]

@ 1
D=D+M // D = D + RAM[1]

@ 2
M=D // RAM[2] = D
```

• Jump to instruction number A (which happens to be 6),

translate

and load

• 0: syntax convention for JMP instruction.

Best practice:

6

@6

0;JMP

To terminate a program safely, end it with an infinite loop.

Memory (ROM)



Built-in symbols

The Hack assembly language features built-in symbols:

<u>symbol</u>	<u>value</u>	
RO	0	Attention: Hack is case-sensitive!
R1	1	R5 and r5 are different symbols.
 R15	 15	,
バエコ	10	

These symbols can be used to denote "virtual registers"

Example: suppose we use RAM[5] to represent some variable, and we wish to let RAM[5]=7



```
// let RAM[5] = 7
@7
D=A
@5
M=D
```

better style:

```
// let RAM[5] = 7
@7
D=A
@R5
M=D
```

Built-in symbols

The Hack assembly language features built-in symbols:

<u>symbol</u>	<u>value</u>	<u>symbol</u>	<u>value</u>
RO	0	SP	0
R1	1	LCL	1
1/1	•••	ARG THIS	3
R15	15	THAT	4
SCREEN	16384		•
KBD	24576		

- RO, R1,..., R15: "virtual registers", can be used as variables.
- SCREEN and KBD: base addresses of I/O memory maps
- Remaining symbols: used in the implementation of the Hack virtual machine, discussed in Chapter 7-8.

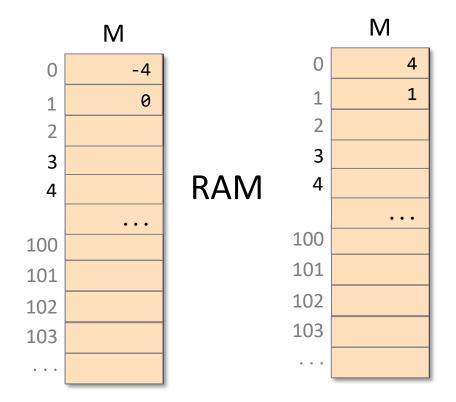
Outlines

- Hack assembly programming
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Hack input / output

Branching

```
// Program: Signum.asm
      // Computes: if R0>0
              R1=1
             else
              R1=0
      // Usage: put a value in RAM[0],
           run and inspect RAM[0].
       @R0
0
       D=M // D=RAM[0]
       @8
2
       D;JGT // If R0>0 goto 8
3
       @R1
       M=0 // RAM[1]=0
       @10
       0;JMP // goto end
8
       @R1
9
       M=1 // RAM[1]=1
       @10 // end
10
11
       0;JMP
```



For condition jump, <u>D register</u> will be used in checking the condition.

Labels

```
// Program: Signum.asm
     // Computes: if R0>0
              R1=1
            else
              R1=0
     // Usage: put a value in RAM[0],
           run and inspect RAM[1].
       @R0
0
       D=M // D = RAM[0]
                            referring
                            to a label
       @POSITIVE4
       D;JGT // If R0>0 goto POSITIVE
       @R1
       M=0 // RAM[1]=0
       @END
       0;JMP // goto end
                           declaring
                           a label
     (POSITIVE)
       @R1
8
       M=1 // R1=1
     (END)
10
       @END // end
11
       0;JMP
```

ROM

Memory



Label resolution rules:

 Label declarations generate no code!!!

resolving labels

 Each reference to a label is replaced with a reference to the instruction number following that label's declaration.

Labels

```
// Program: Signum.asm
     // Computes: if R0>0
              R1=1
            else
              R1=0
     // Usage: put a value in RAM[0],
           run and inspect RAM[1].
       @R0
       D=M // D = RAM[0]
                            referring
       @POSITIVE 	
                            to a label
       D;JGT // If R0>0 goto 8
       @R1
       M=0 // RAM[1]=0
       @END
       0;JMP // goto end
                           declaring
                           a label
     (POSITIVE)
       @R1
       M=1 // R1=1
     (END)
10
       @END // end
       0;JMP
```

resolving labels

Implications:

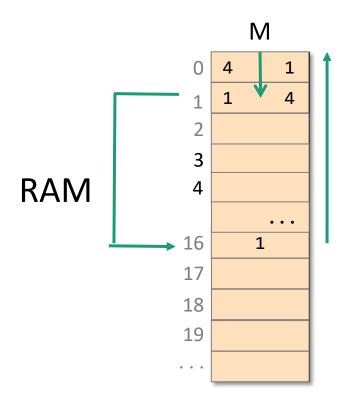
- Instruction numbers no longer needed in symbolic programming
- The symbolic code becomes *relocatable*.

Memory



Variables

```
// Program: Flip.asm
// flips the values of
// RAM[0] and RAM[1]
// RAM[16] = R1
// R1 = R0
// R0 = RAM[16]
   @R1
   D=M
   @16
          // RAM[16] = R1
   M=D
   @R0
   D=M
   @R1
   M=D
         // R1 = R0
   @16
   D=M
   @R0
          // R0 = RAM[16]
   M=D
(END)
   @END
   0;JMP
```



Variables

```
// Program: Flip.asm
// flips the values of
// RAM[0] and RAM[1]
// temp = R1
//R1 = R0
// R0 = temp
            symbol used for
   @R1
            the first time
   D=M
  @temp
  M=D
          // temp = R1
   @R0
   D=M
  @R1
          // R1 = R0
  M=D
            symbol used
   @temp
            again
   D=M
  @R0
          // R0 = temp
  M=D
(END)
  @END
   0;JMP
```

resolving symbols

Symbol resolution rules:

- A reference to a symbol without label declaration is treated as a reference to a variable.
- If the reference @ symbol occurs in the program for first time, symbol is allocated to address 16 onward (say n), and the generated code is @ n.
- All subsequencet
 @ symbol commands are translated into @ n.

Memory

```
@1
       D=M
            // @temp
       @16
       M=D
       @0
       D=M
       @1
       M=D
       @16
            // @temp
       D=M
   10
       @0
       M=D
       @12
       0;JMP
   14
   15
32767
```

Note: variables are allocated to **RAM[16]** onward.

Variables

```
// Program: Flip. sm
// flips the values of
// RAM[0] and RAM[1]
// temp = R1
// R1 = R0
// R0 = temp
  @R1
  D=M
  @temp
  M=D
         // temp = R1
  @R0
  D=M
  @R1
  M=D // R1 = R0
  @temp
  D=M
  @R0
  M=D // R0 = temp
(END)
  @END
   0;JMPa
```

resolving symbols

Implications:

symbolic code is easy to read and debug

Memory



Iterative processing

pseudo code

```
// Computes RAM[1] = 1+2+ ... + RAM[0]
  n = R0
                                                         4
  i = 1
  sum = 0
                                                        16
                                                   n
 LOOP:
                                                        17
  if i > n goto STOP
                                                   sum 18
  sum = sum + i
                                                        19
                    // Program: Sum1toN.asm
  i = i + 1
                    // Computes RAM[1] = 1+2+ ... +n
  goto LOOP
                    // Usage: put a number n in RAM[0]
 STOP:
                      @R0
  R1 = sum
                      D=M
                      @n
                      M=D // n = R0
                      @i
assembly code
                      M=1 // i = 1
                      @sum
                      M=0 // sum = 0
```

10 55 Memory @0 D=M @16 // @n M=D 10 // @i @17 1 M=1// @sum @18 0 M=0 . . . Variables are allocated to consecutive RAM locations from address 16 onward 32767

M

Iterative processing

pseudo code

```
// Compute RAM[1] =
    1+2+ ... +RAM[0]
    n = R0
    i = 1
    sum = 0

LOOP:
    if i > n goto STOP
    sum = sum + i
    i = i + 1
    goto LOOP

STOP:
    R1 = sum
```

 $i>n \Leftrightarrow i-n>0$

assembly code

```
// Compute RAM[1] = 1+2+ ... +n
// Usage: put a number (n) in
   RAM[0]
 @R0
 D=M
 @n
 M=D // n = R0
 @i
 M=1 // i = 1
 @sum
 M=0 // sum = 0
(LOOP)
 @i
 D=M // D = i
 @n
 D=D-M // D = i - n
 @STOP
 D;JGT // if i > n goto STOP
```

```
@i
 D=M // D = i
 @sum
 M=D+M // sum = sum + i
 @i
 M=M+1 // i = i + 1
 @LOOP
 0;JMP // goto LOOP
(STOP)
 @sum
 D=M // D = sum
 @R1
 M=D // RAM[1] = sum
(END)
 @END
 0;JMP // end
```

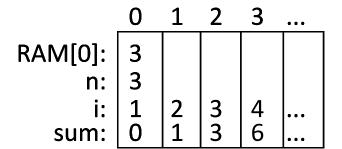
Program execution

assembly program

```
// Compute RAM[1] = 1+2+ ... +n
// Usage: put a number (n) in RAM[0]
 @R0
 D=M
 @n
 M=D // n = R0
 @i
 M=1 // i = 1
 @sum
 M=0 // sum = 0
(LOOP)
 @i
 D=M // D = i
 @n
 D=D-M // D = i - n
 @STOP
 D;JGT // if i > n goto STOP
 @i
 D=M // D = i
 @sum
 M=D+M // sum = sum + i
 @i
 M=M+1 // i = i + 1
 @LOOP
 0;JMP // goto to LOOP
(STOP)
 @sum
 D=M // D = sum
 @R1
 M=D // RAM[1] = sum
(END)
 @END
 0;JMP // end
```

Variable-Value Trace Table

iterations



Sample exam question

@5 D=A @R0 M=D

@R0 D=M @n M=D @pre M=0 @cur M=1

(LOOP) @cur D=M @n D=D-M @STOP D;JGT @pre
D=M
@cur
D=D+M
@nex
M=D

@cur D=M @pre M=D

@nex D=M @cur M=D @LOOP 0;JMP (STOP) @nex D=M @R1 M=D

(END) @END 0;JMP 2.(a) I. Please derive the value of RAM[1] after the execution of this piece of code. [4 marks]

From 2019-2020 CSF exam.

Hint: this piece of code implements Fibonacci number.

Writing assembly programs

assembly program

```
// Compute RAM[1] = 1+2+ ... +n
// Usage: put a number (n) in RAM[0]
 @R0
 D=M
 @n
 M=D // n = R0
 @i
 M=1 // i = 1
 @sum
 M=0 // sum = 0
(LOOP)
 @i
 D=M // D = i
 D=D-M // D=i-n
 @STOP
 D;JGT // if i > n goto STOP
 @i
 D=M // D = i
 @sum
 M=D+M // sum = sum + i
 @i
 M=M+1 // i = i + 1
 @LOOP
 0;JMP // goto to LOOP
(STOP)
 @sum
 D=M // D = sum
 @R1
 M=D // RAM[1] = sum
(END)
 @END
 0;JMP // end
```

Best practice:

- Design the program using pseudo code,
- Write the program in assembly language,
- Test the program (on paper) using a variable-value trace table.

Outlines

- Hack assembly programming
 - ➤ Registers and memory
 - ➤ Branching, variables, iteration
 - **→** Pointers

Hack input / output

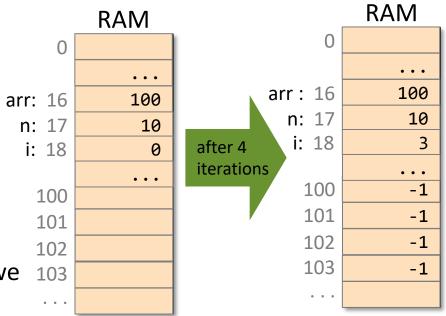
Pointers

Example:

```
// for (i=0; i<n; i++) {
// arr[i] = -1
// }
```

Observations:

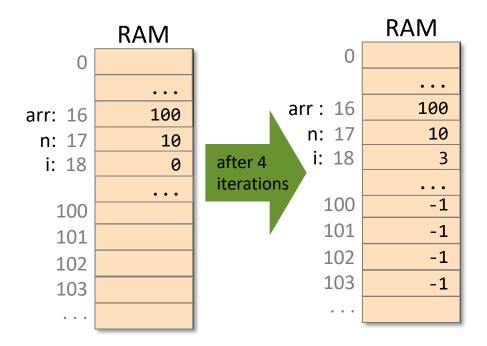
- The array is implemented as a block of memories.
- To access these memories one by one, we need a variable to hold the current address.
- Variables that represent addresses are called **pointers**.
- There is nothing special about pointer variables, except that their values are interpreted as addresses.



Pointers

Example:

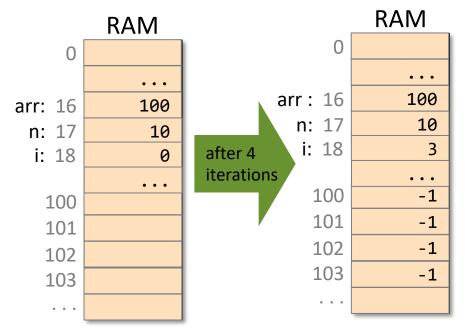
```
// for (i=0; i<n; i++) {
// arr[i] = -1
//}
 // Suppose that arr=100 and n=10
 // Let arr = 100
  @100
 D=A //D = 100
  @arr
  M=D // arr = 100
 // Let n = 10
  @10
 D=A // D = 10
  @n
  M=D // n = 10
 // Let i = 0
  @i
  M=0 // i = 0
 // Loop code continues
 // in next slide...
```



Pointers

Example:

```
(LOOP)
 // if (i==n) goto END
 @i
 D=M // D = i
 @n
 D=D-M // D = i-n
 @END
 D;JEQ // if (i==n) goto END
 // RAM[arr+i] = -1
 @arr
 D=M // D = arr
                              typical pointer
 @i
                               manipulation
 A=D+M // A = arr + i
 M=-1 // M[arr+i] = -1
 // j++
 @i
 M=M+1 // i = i + 1
 // goto LOOP
 @LOOP
 0;JMP
(END)
 @END
 0;JMP // END
```



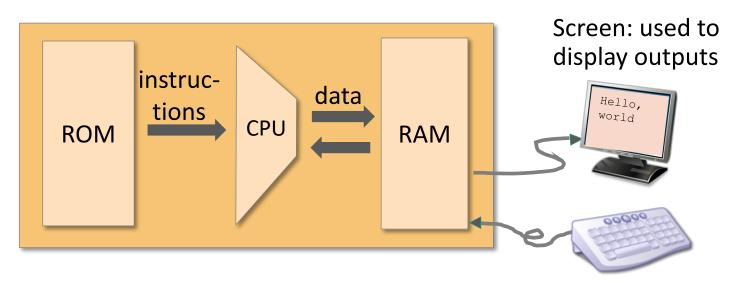
- Pointers: Variables that store memory addresses (like arr).
- Pointers in Hack: Whenever we have to access memory using a pointer, we need an instruction like A=expression.
 - Semantics: "set the address register to some value".

Outlines

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 - **≻**Pointers

Hack input / output

Input / output



Keyboard: used to enter inputs

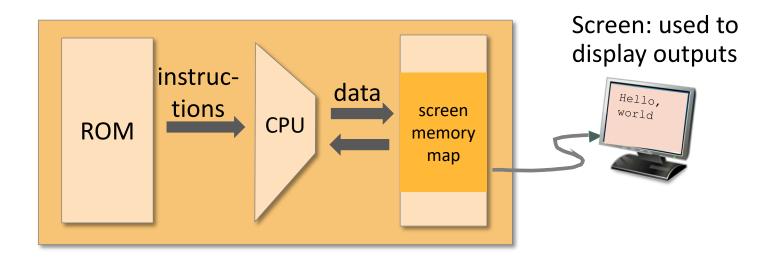
I/O handling (high-level):

Software libraries enabling text, graphics, audio, video, etc.

I/O handling (low-level):

Bit manipulation.

Memory mapped output

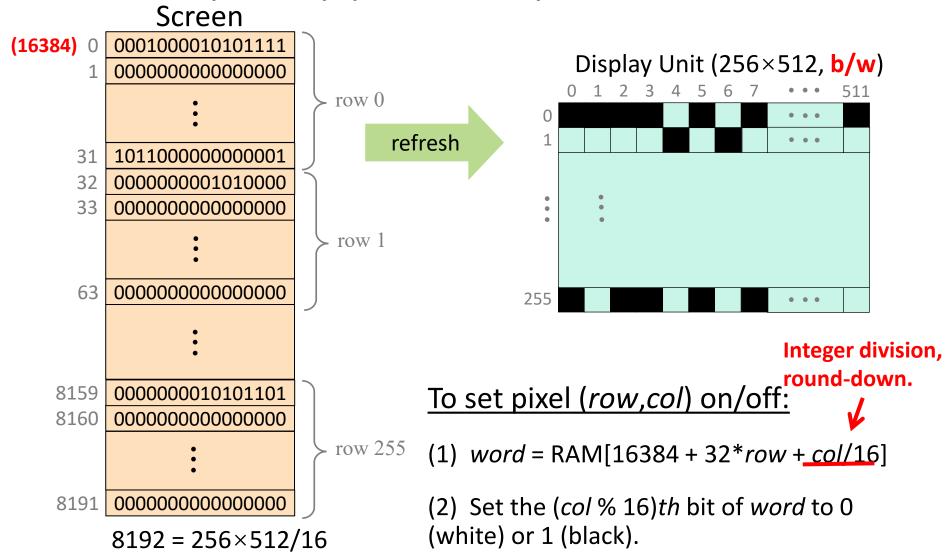


- Memory mapped output
 - > A designated memory area to manage a display unit.
 - The physical display is continuously *refreshed* from the memory map, many times per second. (It is slow in Hack computer.)
 - Output is effected by writing code that manipulates the screen memory map.

32

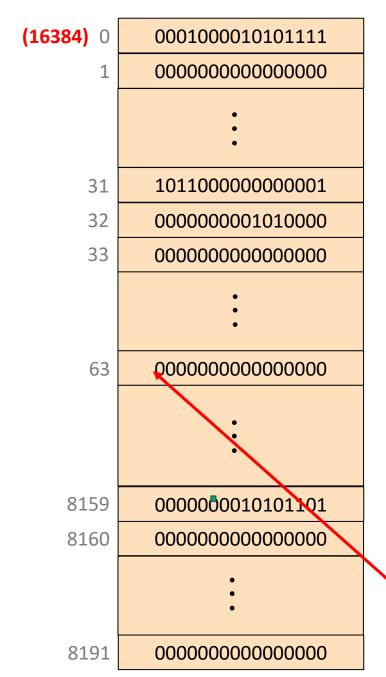
Memory mapped output

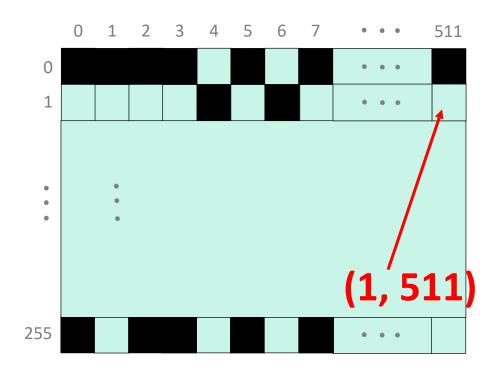
16 X 32 = 512



Screen

Display Unit (256×512, b/w)





(1)
$$word = RAM[16384 + 32*row + col/16]$$

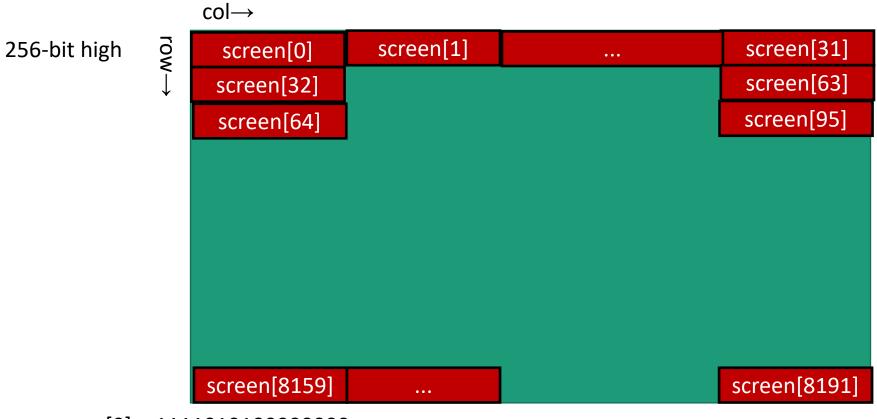
word=63 32 31

(2) Set the (col % 16)th bit of word to 0 (white) or 1 (black).

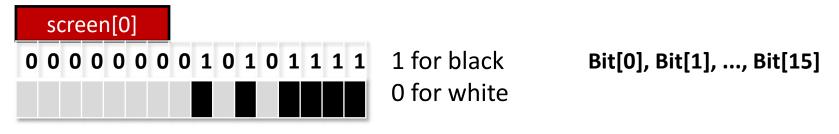
<u>bit=15</u>

Hack Screen

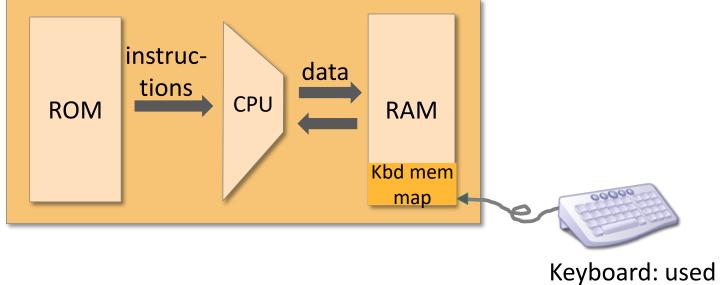
512-bit wide



screen[0] = 1111010100000000

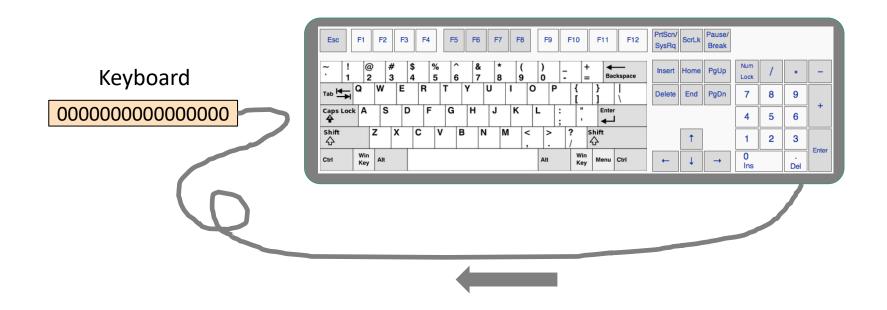


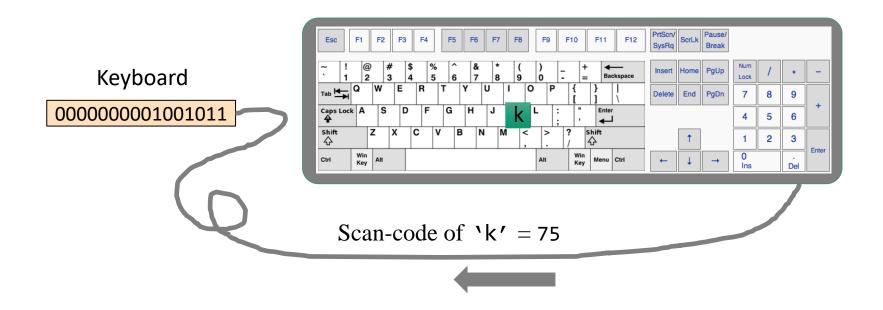
Input

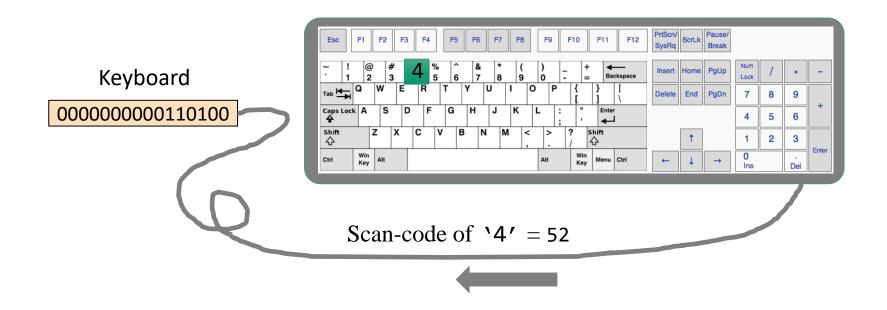


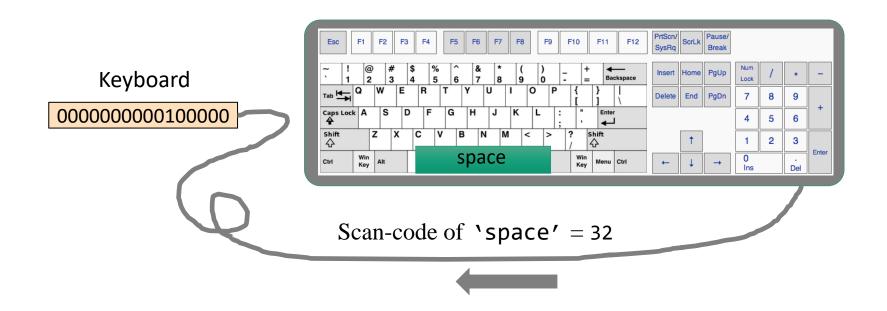
Keyboard: used to enter inputs

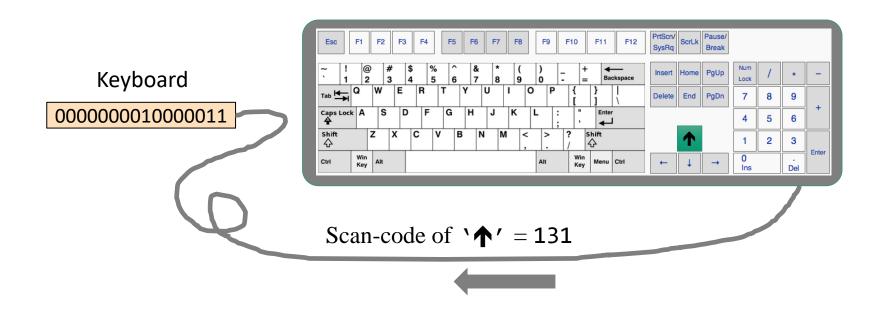
The physical keyboard is associated with a *keyboard memory* map.











- When a key is pressed on the keyboard, the key's scan code appears in the keyboard memory map.
- When no key is pressed, the resulting code is 0.

The Hack character set

key	code
(space)	32
!	33
"	34
#	35
\$	36
%	37
&	38
r	39
(40
)	41
*	42
+	43
,	44
_	45
•	46
/	47

key	code
0	48
1	49
•••	
9	57

:	58
;	59
<	60
=	61
>	62
?	63
@	64

key	code
А	65
В	66
С	
Z	90

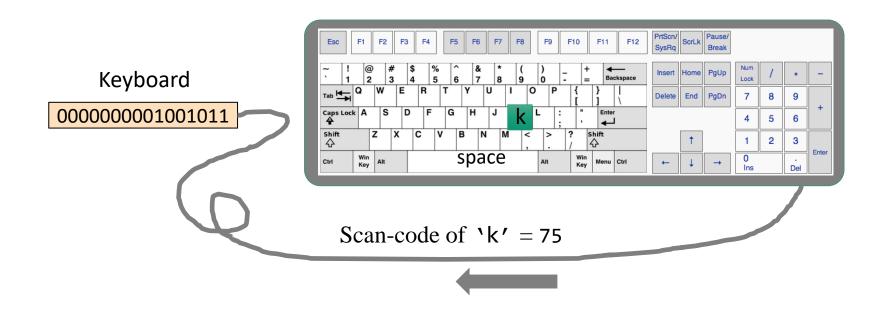
]	91
/	92
]	93
۸	94
-	95
`	96

key	code
а	97
b	98
С	99
Z	122

{	123
- 1	124
}	125
~	126

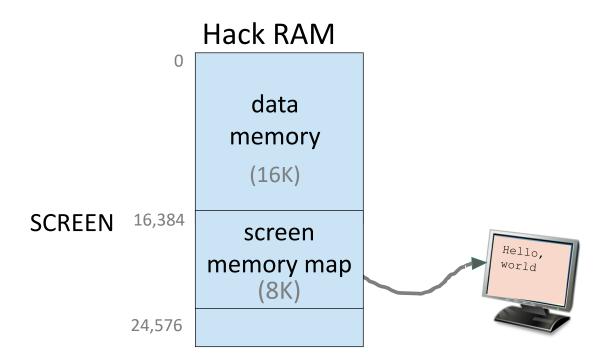
key	code
newline	128
backspace	129
left arrow	130
up arrow	131
right arrow	132
down arrow	133
home	134
end	135
Page up	136
Page down	137
insert	138
delete	139
esc	140
f1	141
f12	152

Handle the keyboard



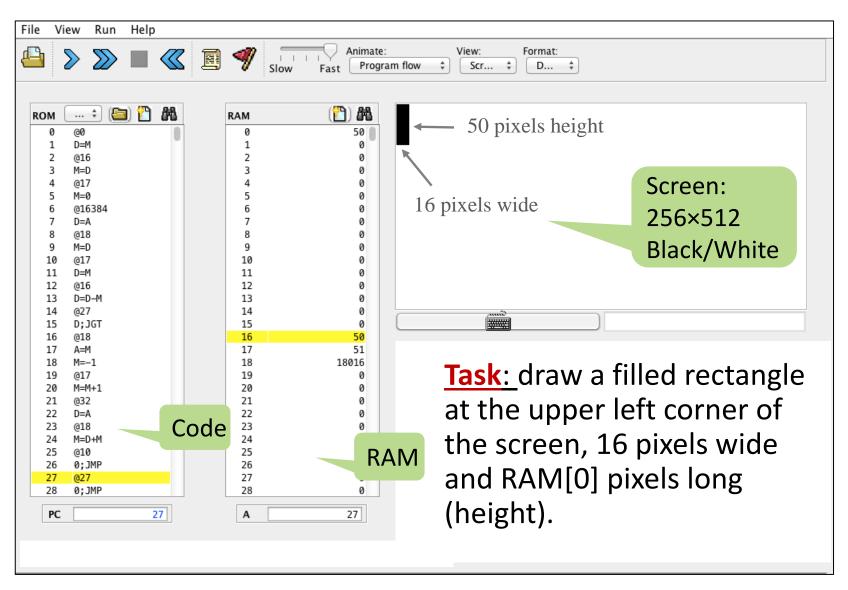
- To check which key is currently pressed:
 - Probe the contents of the Keyboard chip
 - > In the Hack computer: probe the contents of RAM[24576].

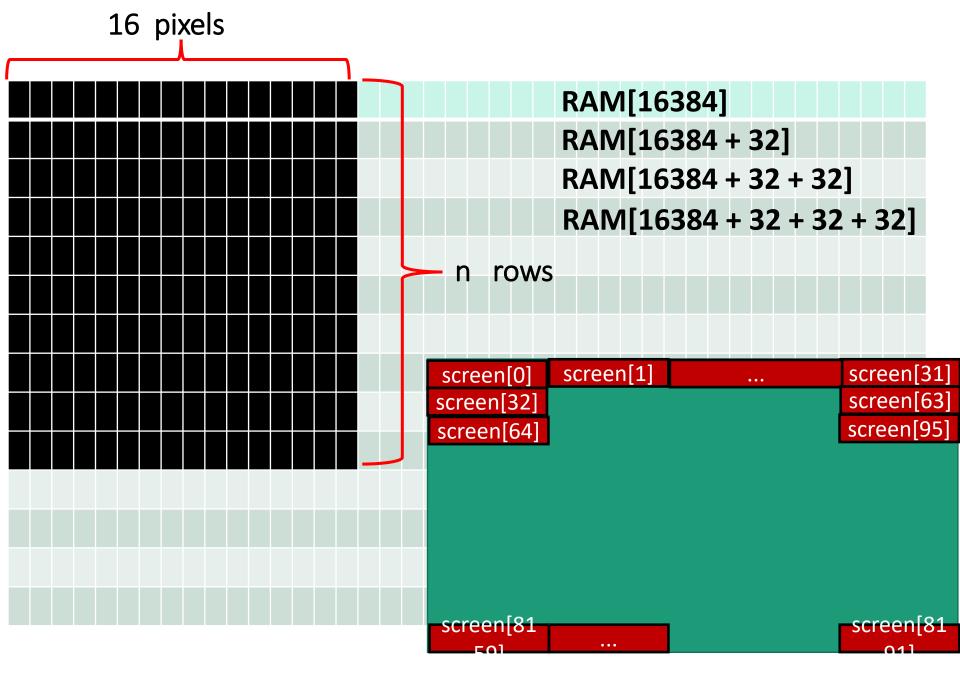
Output

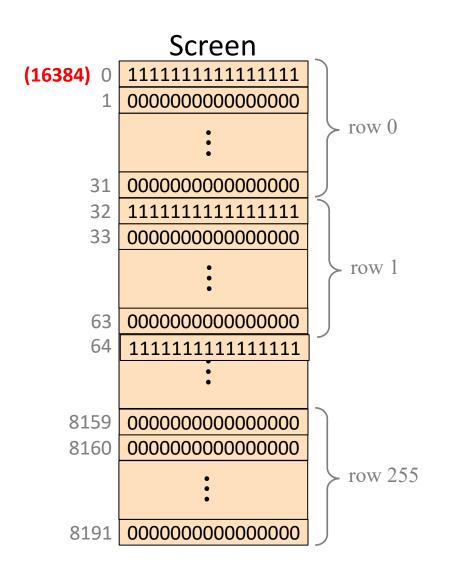


Hack language convention:

SCREEN: base address of the screen memory map





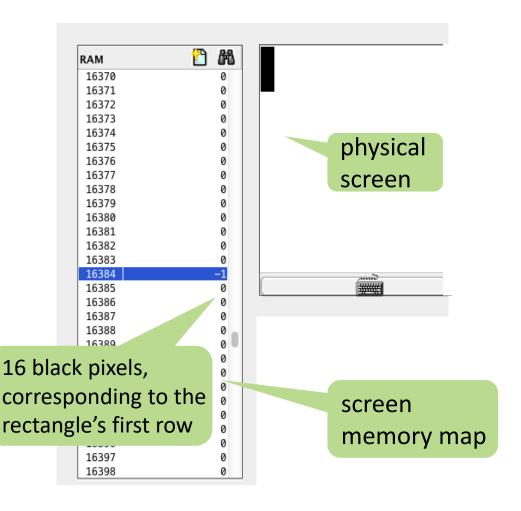


Two's Complement

1 = 00000000000001

Pseudo code

```
// for (i=0; i<n; i++) {
    draw 16 black pixels at the
     beginning of row i
//}
addr = SCREEN
n = RAM[0]
i = 0
LOOP:
 if i == n goto END
 RAM[addr] = -1 //
   11111111111111111
 // advances to the next row
 //512 = 16 \times 32
 addr = addr + 32
 i = i + 1
 goto LOOP
END:
 goto END
```



Assembly code

```
// Program: Rectangle.asm
// Draws a filled rectangle at the
// screen's top left corner, with
// width of 16 pixels and height of
// RAM[0] pixels.
// Usage: put a non-negative number
// (rectangle's height) in RAM[0].
 @SCREEN
 D=A
 @addr
 M=D // addr = 16384
    // (screen's base address)
 @R0
 D=M
 @n
 M=D // n = RAM[0]
 @i
 M=0 // i = 0
```

```
(LOOP)
 @i
 D=M
 @n
 D=D-M
 @END
 D;JEQ // if i==n goto END
 @addr
 A=M
 M=-1 // RAM[addr]=11111111111111111
 @i
 M=M+1 // i = i + 1
 @32
 D=A // D = 32
 @addr
 M=D+M // addr = addr + 32
 @LOOP
 0;JMP // goto LOOP
(END)
 @END // program's end
 0;JMP // infinite loop
```

Pseudo code

```
// for (i=0; i<n; i++) {
    draw 16 black pixels at the
    beginning of row i
// }
addr = SCREEN
n = RAM[0]
i = 0
LOOP:
 if i == n goto END
 RAM[addr] = -1 //
    11111111111111111
 // advances to the next row
 //512 = 16 \times 32
 addr = addr + 32
 i = i + 1
 goto LOOP
END:
 goto END
```

Assembly code

```
// Program: Rectangle.asm
// Draws a filled rectangle at the
// screen's top left corner, with
// width of 16 pixels and height of
// RAM[0] pixels.
// Usage: put a non-negative number
// (rectangle's height) in RAM[0].
  @SCREEN
  D=A
 @addr
 M=D // addr = 16384
    // (screen's base address)
 @R0
  D=M
 @n
 M=D // n = RAM[0]
  @i
  M=0 // i = 0
```

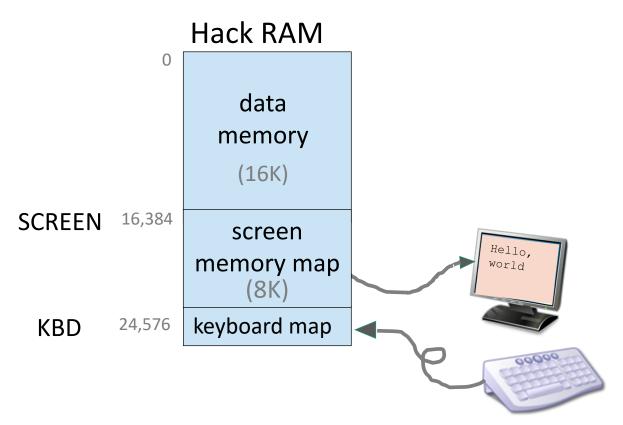
Handling the screen (example) Assembly code

Pseudo code

```
// for (i=0; i<n; i++) {
    draw 16 black pixels at the
    beginning of row i
// }
addr = SCREEN
n = RAM[0]
i = 0
LOOP:
 if i == n goto END
 RAM[addr] = -1 //
    11111111111111111
 // advances to the next row
 // 512 = 16 × 32
 i = i + 1
 addr = addr + 32
goto LOOP
END:
 goto END
```

```
(LOOP)
 @i
 D=M
 @n
 D=D-M
 @END
 D;JEQ // if i==n goto END
 @addr
 A=M
 M=-1 // RAM[addr]=1111111111111111
 @i
 M=M+1 // i = i + 1
@32
 D=A // D = 32
 @addr
 M=D+M // addr = addr + 32
 @LOOP
 0;JMP // goto LOOP
(END)
 @END // program's end
 0;JMP // infinite loop
```

Input

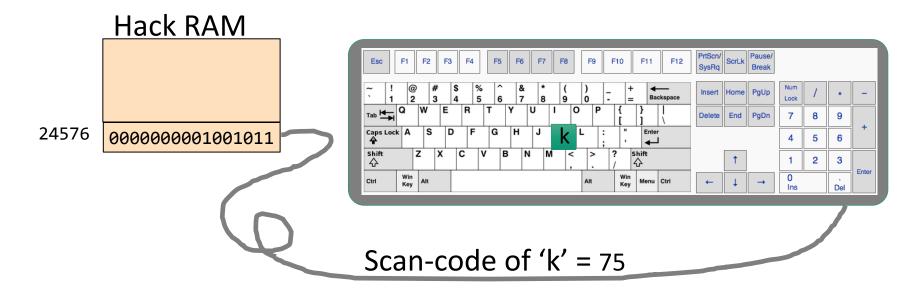


Hack language convention:

SCREEN: base address of the screen memory map

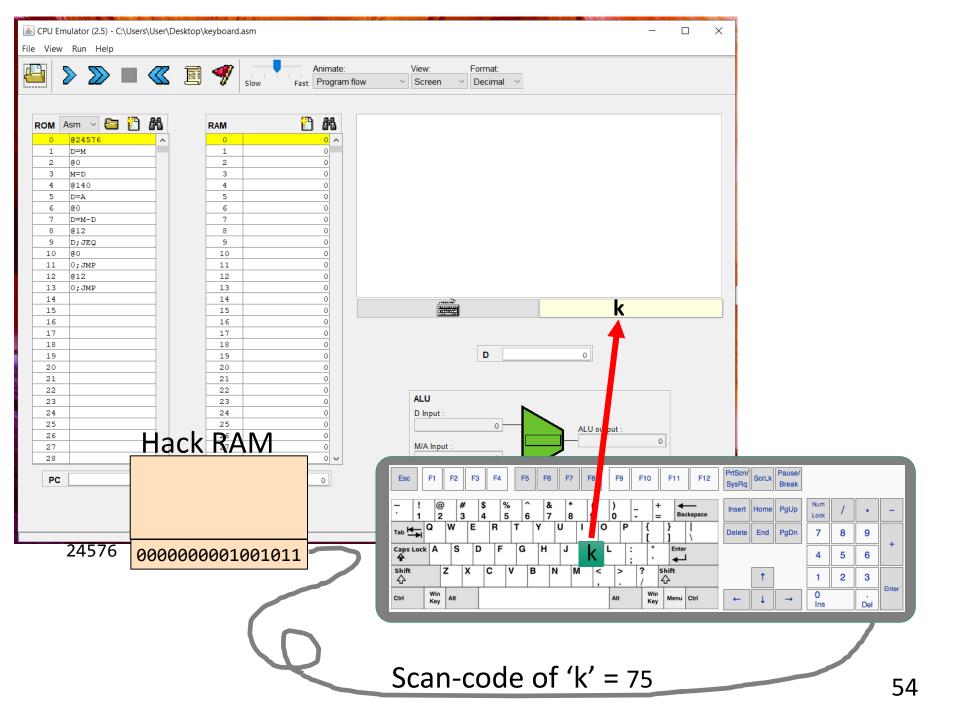
• KBD: address of the keyboard memory map

Handle the keyboard



To check which key is currently pressed:

- Read the contents of RAM[24576] (address KBD).
- If the register contains 0, no key is pressed.
- Otherwise, the register contains the scan code of the currently pressed key.



Keyboard input (example)

```
// Example: Run an infinite loop to listen to the
   keyboard input
(LOOP)
// check keyboard input
 @KBD
 D = M //get keyboard input
 @R0
 M=D //set R0 to keyboard input
 //if R0 = 'esc', goto END
 @140 // 'esc' = 140
 D=A
 @R0
 D=M-D
 @END
 D;JEQ
 @LOOP
 0;JMP // an infinite loop.
(END)
 @END
 0;JMP //end
```

Comments on assembly programming

High level code

```
for (i=0; i<n; i++) {
    arr[i] = -1
}
```



Assembly programming is:

- Low-level
- Efficient (or not)
- Intellectually challenging.

Machine language

```
@i
   M=0
(LOOP)
   @i
   D=M
   @n
   D=D-M
   @END
   D;JEQ
   @arr
   D=M
   @i
   A=D+M
   M=-1
   @i
   M=M+1
   @LOOP
   0;JMP
(END)
   @END
   0;JMP
```

Summary

- Hack assembly programming
 - ➤ Registers and memory
 - ➤ Branching, variables, iteration
 - **≻**Pointers

Hack input / output

Acknowlegement

- This set of lecture notes are based on the lecture notes provided by Noam Nisam / Shimon Schocken.
- You may find more information on: www.nand2tetris.org.