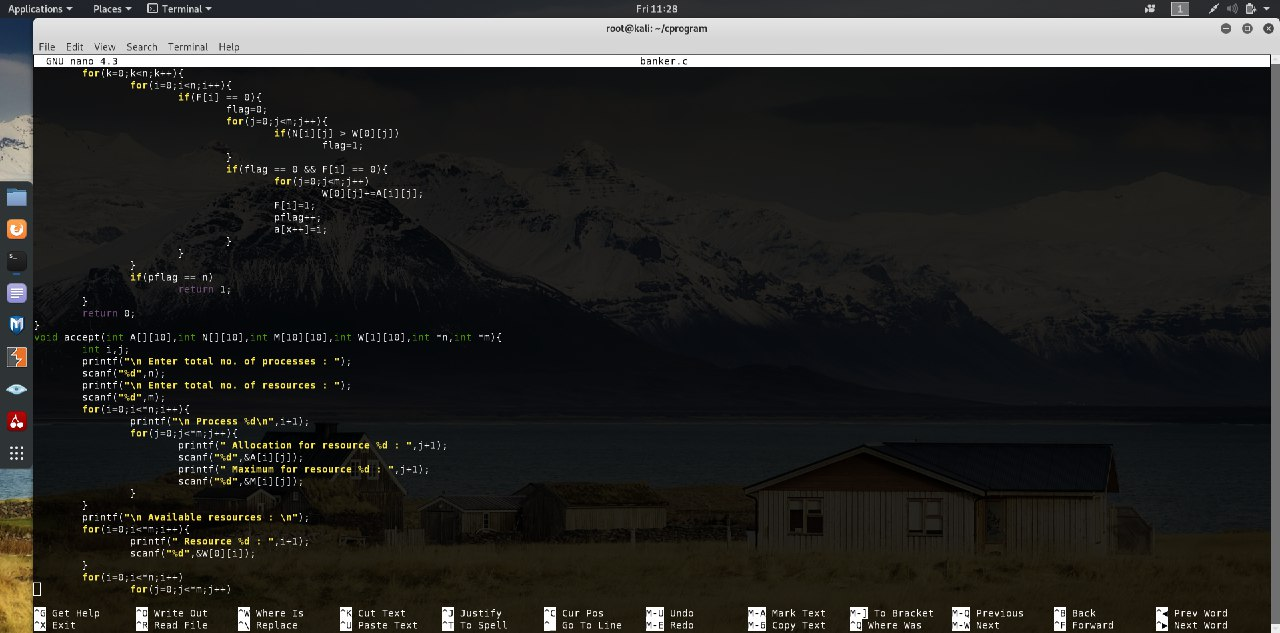
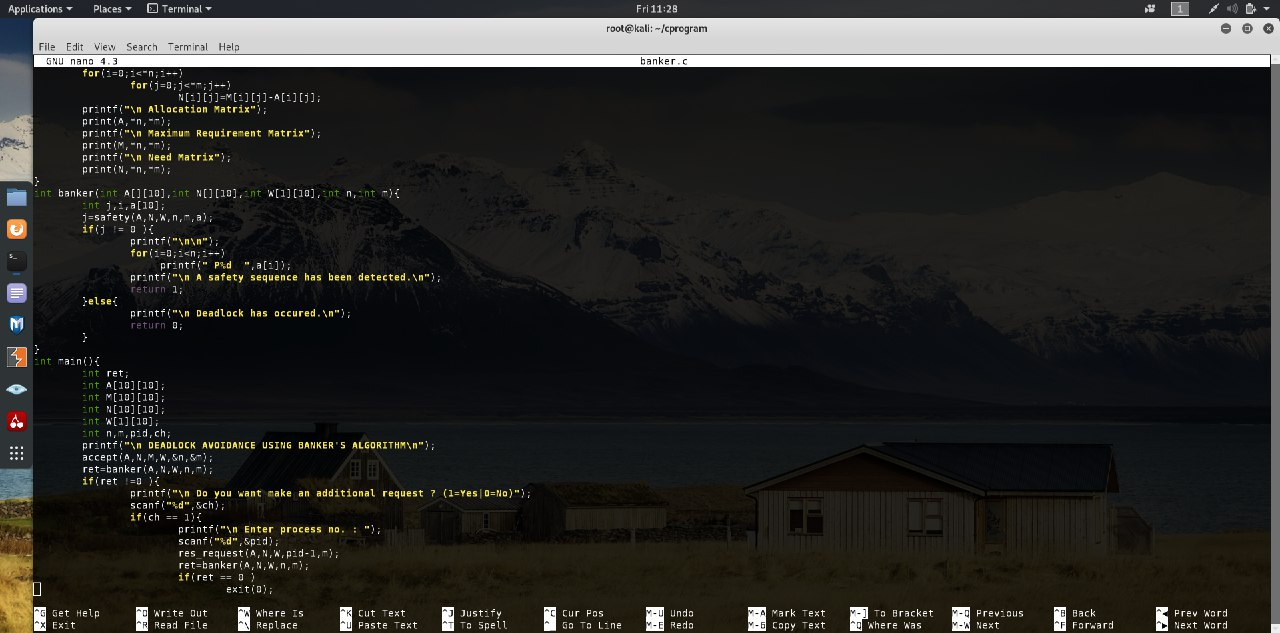
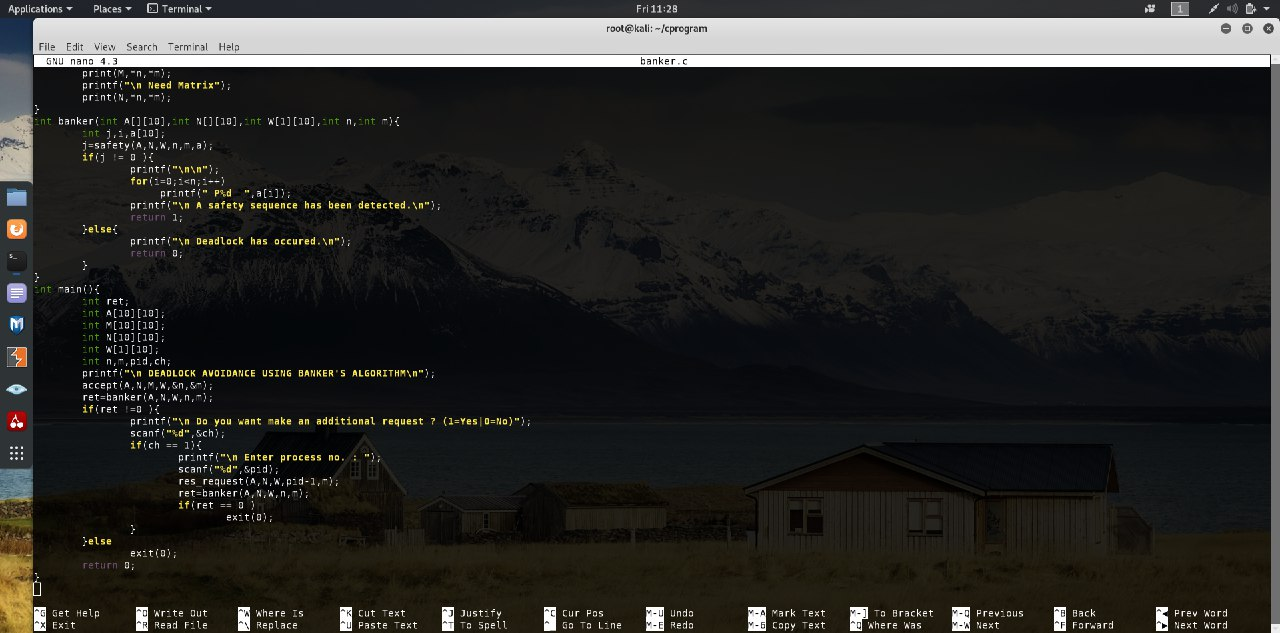
1.Banker process:

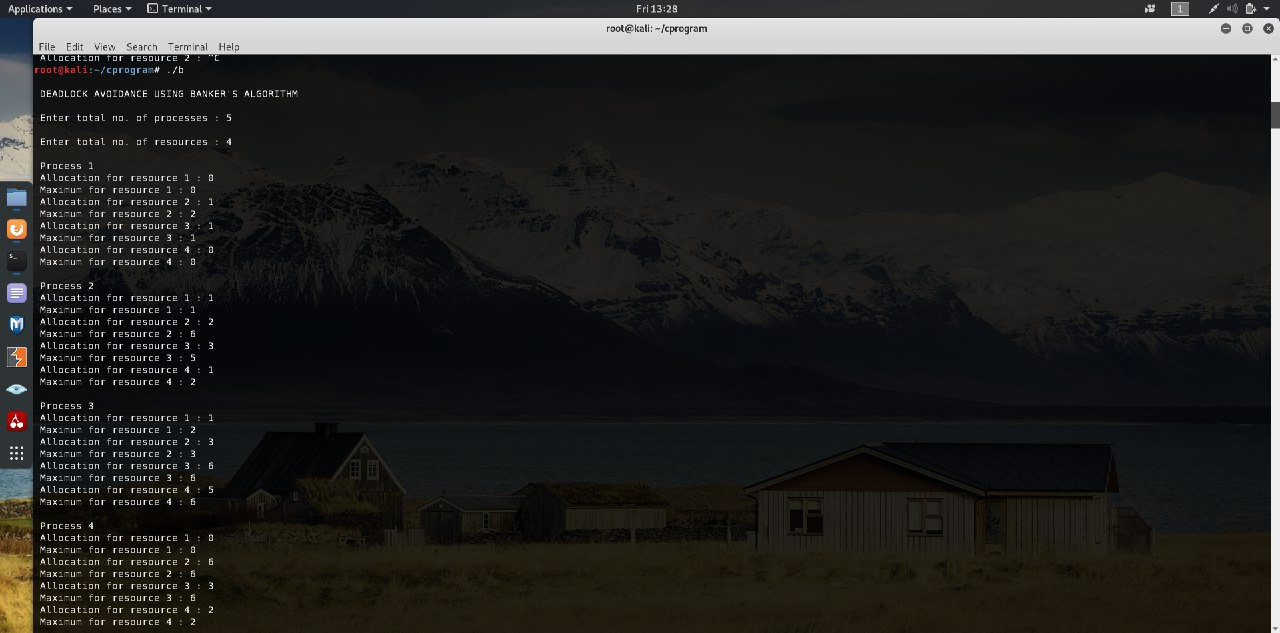


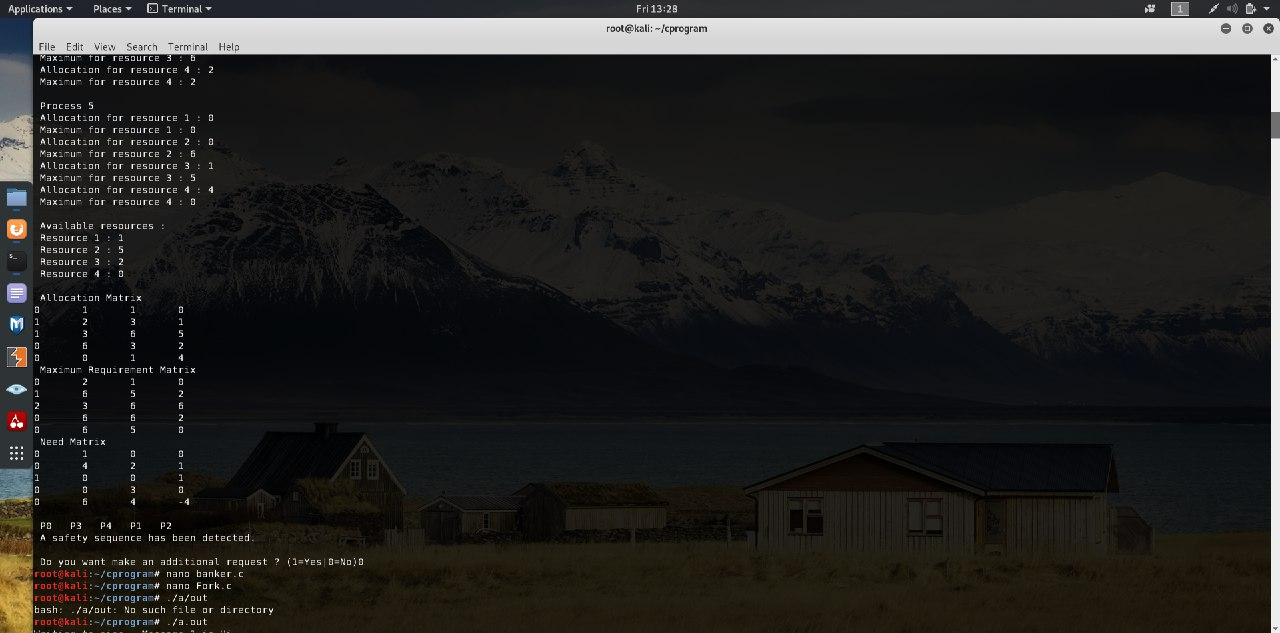






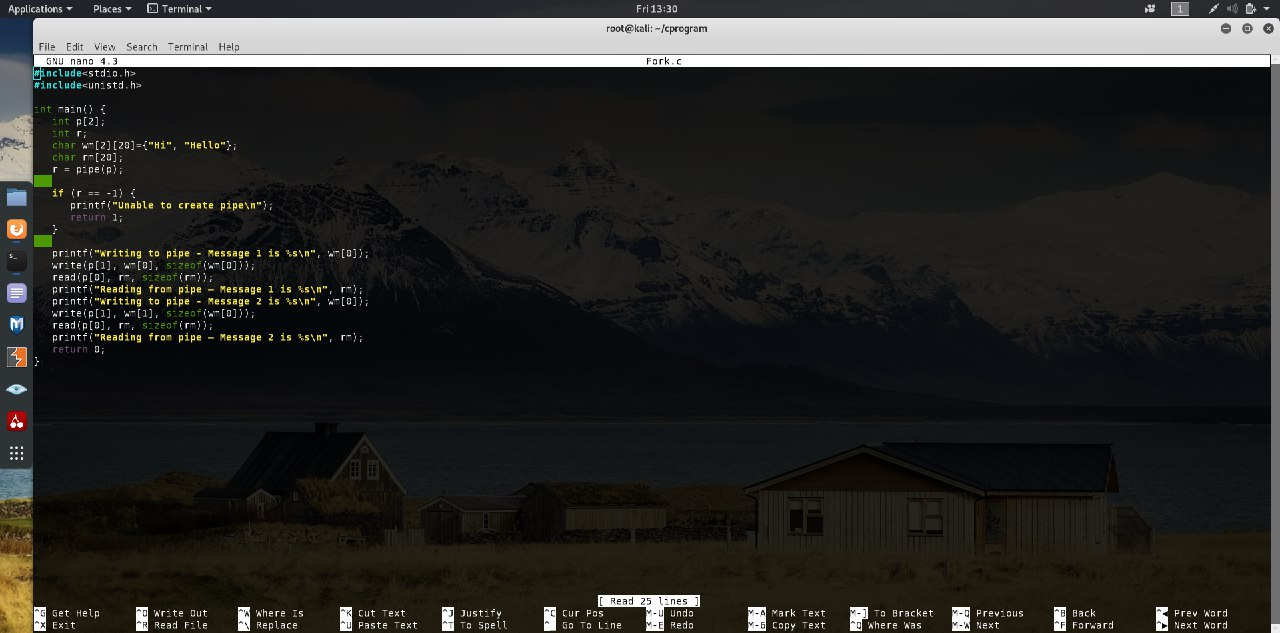
Output:





2.

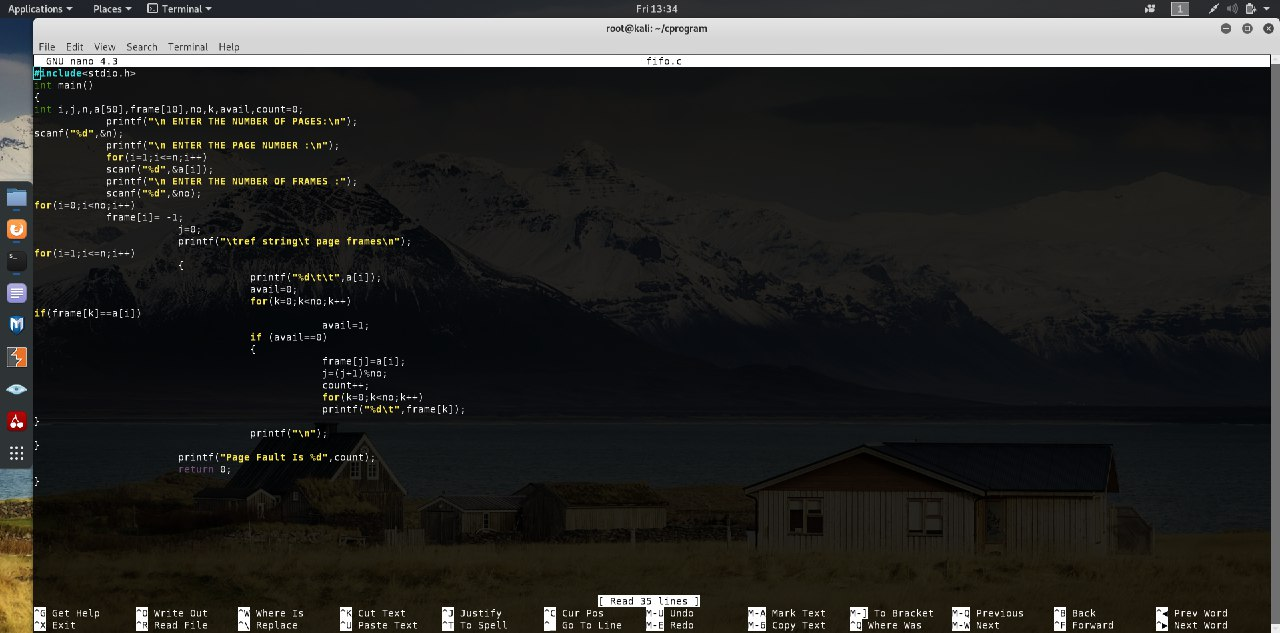
3.a)pipe:



Output:



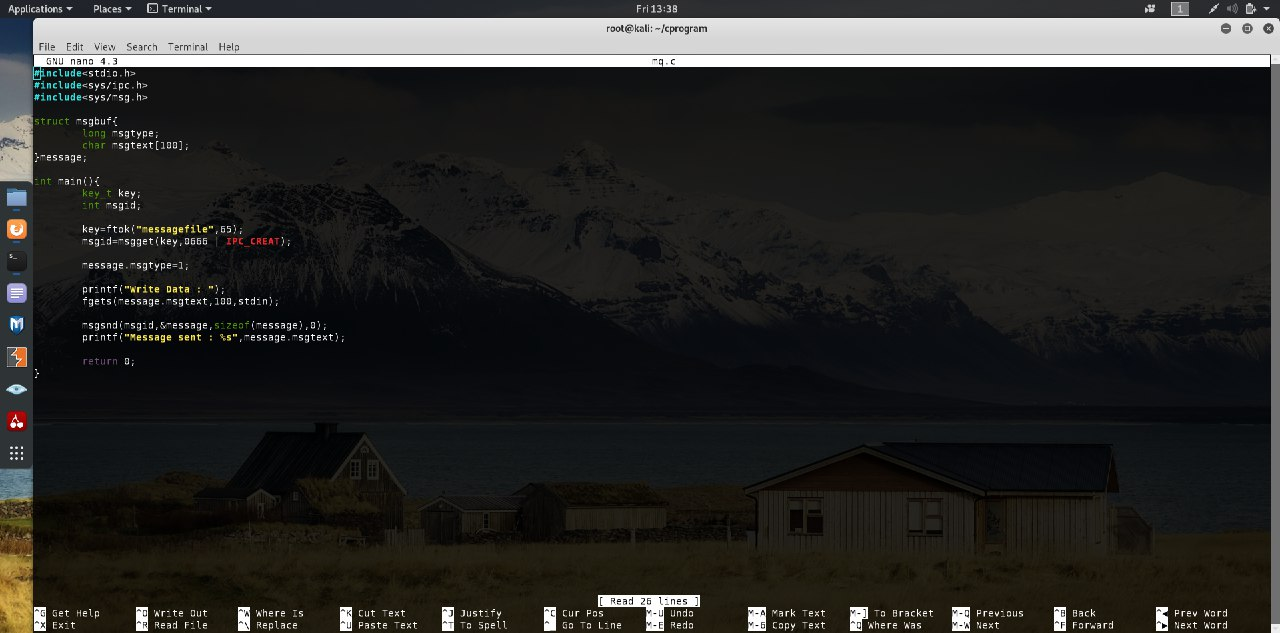
b)FIFO:



Output:



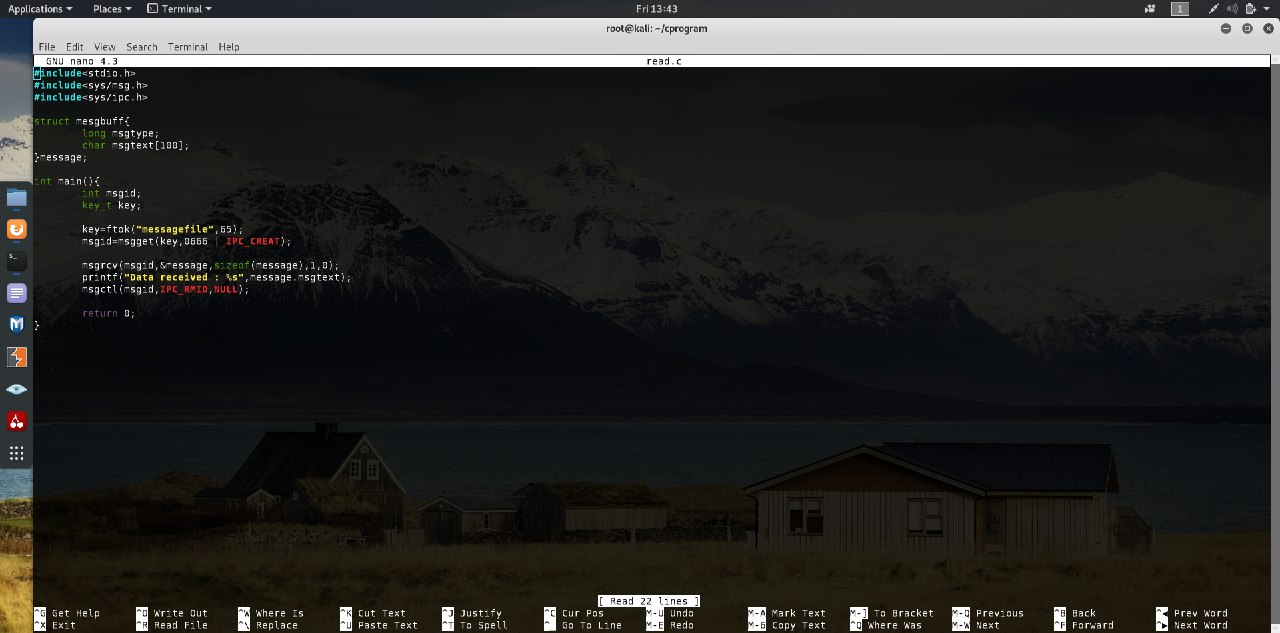
c)Write:



Ouput:



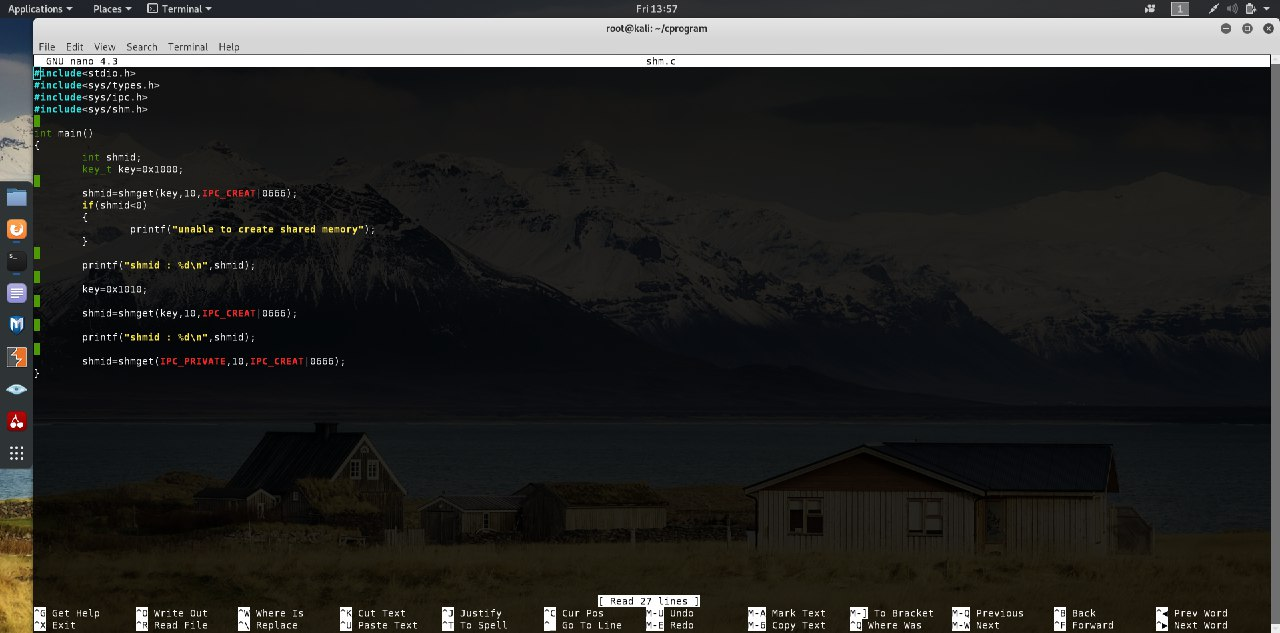
Read:



Output:



d)shared memory:



Output:

