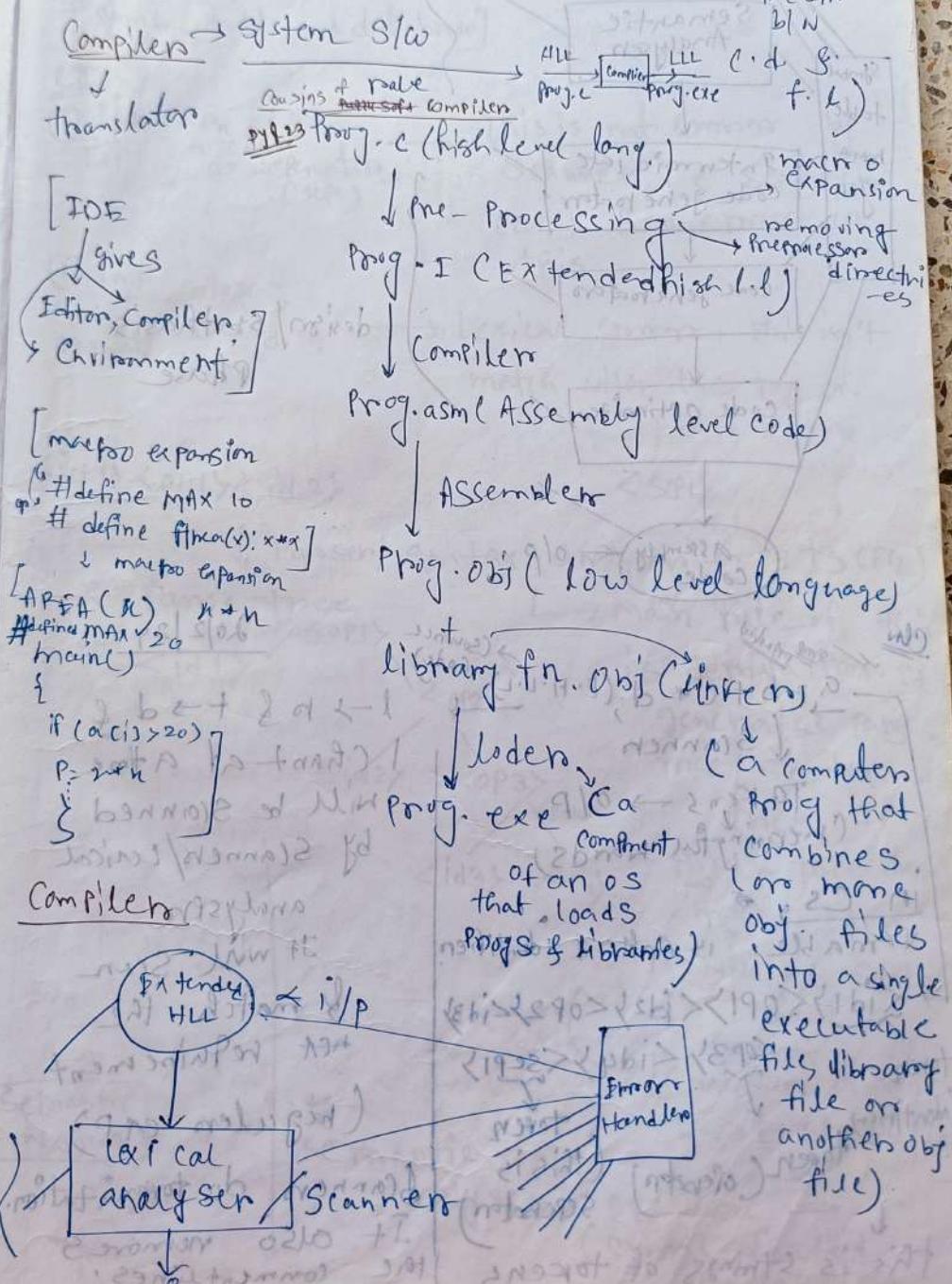
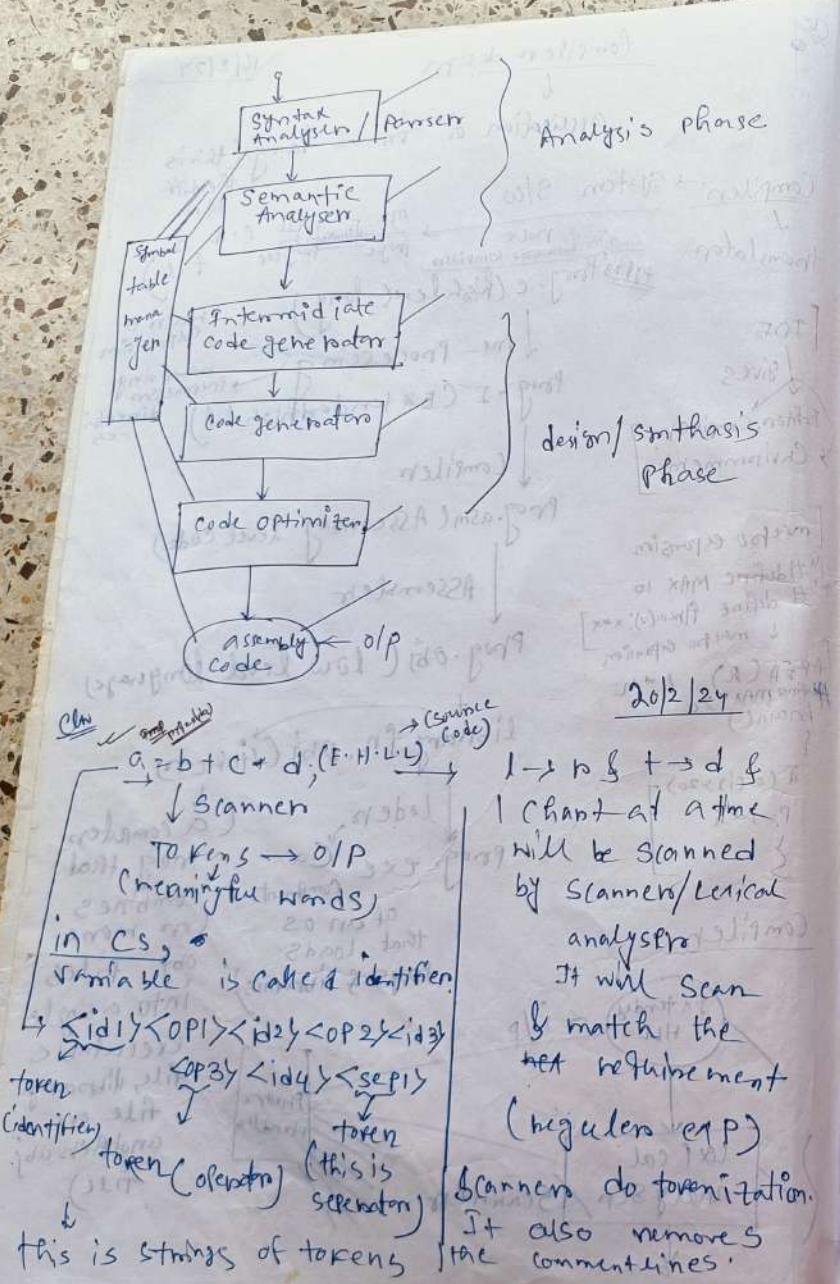


Compiler design

16/2/24

application of formal lang. (this is the diff b/w)





STM (Symbol table) this will maintain all symbols (identifier, separator, operator)

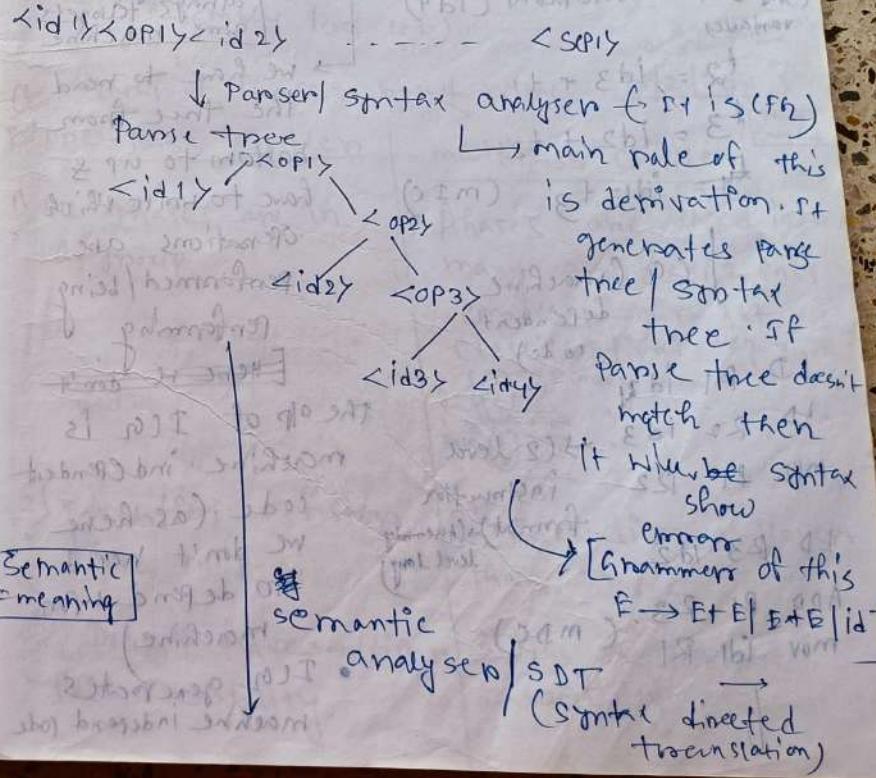
It is a data structure

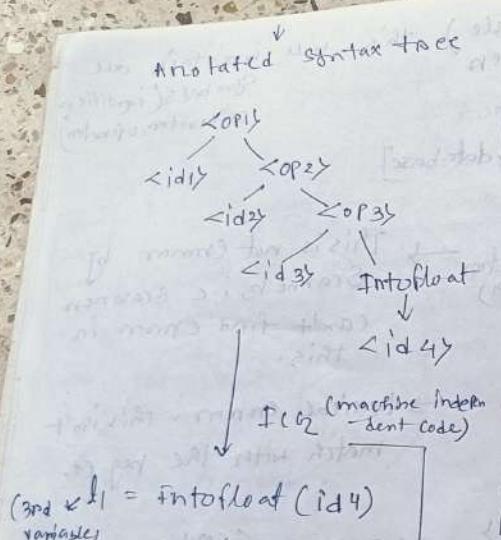
[Symbol table → database]

e.g.

(1) $int \quad n; \quad id_1 \quad id_2 \quad \text{separators} \quad (sep_1)$ → This is not error by Scanner, i.e. Scanner can't find errors in this.

(2) $int \quad log; \quad id_1 \quad id_2 \quad \text{sep_1}$ → Lexical error. This isn't match with the reg ex.





SA can find the errors named 'type casting'.
 If a, b float
 d not then we can't do mul of int & float then we need to correct type casting errors & redraw the correct tree.

Assembly lang - always targets some machine

We have to read the tree from bottom to up & have to write which operations are performed/being performing.

Hence we don't

the op of IL_{C2} is machine independent code.

(as here we don't need to depend upon machine)

- IL_{C2} generates machine independent code

$$\begin{aligned} t_2 &= id_3 + t_1 \\ t_3 &= id_2 + t_2 \\ t_1 &= id_1 + t_3 \quad (\text{MIS}) \end{aligned}$$

IL_{C2} (machine dependent code)

$\begin{array}{l} LD\ R1\ id_2 \\ LD\ R2\ id_3 \\ MUL\ R1\ R2 \end{array}$ → (2 level instruction format) (Assembly level lang)

$\begin{array}{l} LD\ R3\ id_2 \\ ADD\ R1\ R3 \\ mov\ id1\ R1 \end{array}$ (MDG)

(Machine Independent)

CO

LD R1 id4

LD R2 id3

MUL R1 R2

LD R2 id2

ADD R1 R2

MOV id1 R1

Optimized Assembly level code

- CO generates machine dependent code

IF 1 level ins → we will use AC

Optimizations: i++ j++

PC = "A1/A2"

PC("A1/A2")

PC("A1/A2"); Optimized

AS R2 is free now we are using this resource rather than taking an extra resource (optimization).

Optimization
comes directly from basic

CO group of phases based name

21/2/24

One-pass compiler

All phases are in 1 group.

Multi-pass compilers

All phases are divided into many groups. (2-pass compiler - ours 1st, classification) e.g. Java

1 { 1 } 2 { 2 }

3 { 3 } 4 { 4 }

5 { 5 } 6 { 6 }

7 { 7 } 8 { 8 }

9 { 9 } 10 { 10 }

11 { 11 } 12 { 12 }

13 { 13 } 14 { 14 }

15 { 15 } 16 { 16 }

17 { 17 } 18 { 18 }

19 { 19 } 20 { 20 }

21 { 21 } 22 { 22 }

23 { 23 } 24 { 24 }

25 { 25 } 26 { 26 }

27 { 27 } 28 { 28 }

29 { 29 } 30 { 30 }

Execution is faster than M.P.C.

Execution is slower than O.P.C.

Doesn't have multiple purpose usability.

Has multiple purpose usability.

- i) It's called harmonic compiler.
- ii) Has a limited scope.
- iii) There is no code optimization.
- iv) There is no intermediate code generation.
- v) Takes some time to compile.
- vi) memory consumption is lower.
- vii) memory consumption is higher.

in called on wide compiler.

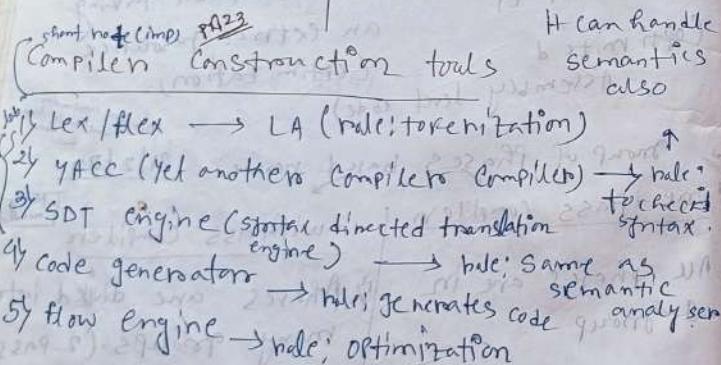
v) Has a great scope.

vi) There is code optimization.

vii) There is i. d. g.

viii) Takes some time to compile.

ix) memory consumption is higher.



Lexical analysis : with which we are matching is called lexeme.

- Rules of LA -
- i) Scanning ($I \rightarrow d, l \rightarrow n, 1 \text{ char at a time}$)
 - ii) After scanning, finding some meaningful terms / trying to try to match with some pattern ie reg ex
 - iii) Tokenization
 - iv) comment line removal.

- v) white space removal. $[int \rightarrow \dots x;]$ $[fn \text{ compiler multiline codes are analized}]$

$[int \rightarrow x;]$
it is a single line code

Reg Ex of lang starting with 'ab' $\rightarrow \{a, b\}$

Reg Ex: $ab(ab)^*$ $\rightarrow ab(ab)^*$ $\{ab\}^*$ $\{t\} \text{ as } t \text{ isn't a int value.}$

Integers constant starting with the lex code)

Reg ex: $(0+1+2+3+4+5+6+7+8+9)^+$ $\rightarrow [0123456789]^+$ $\{01-19\}^+$ $\{t\} \text{ not correct}$

In compilers $\rightarrow (0|1|2|3|4|5|6|7|8|9)^+ \rightarrow [0123456789]^+$

Inflex (compilers), $[a b] \rightarrow [\dots \rightarrow \text{class format}]$

$a|b|C$ means either a or b or c
without class

In b/w class we can define range

$\rightarrow [0-9]^+ \rightarrow$ the regex of int. const.

② $[0123678]$ $\{t\} \text{ As we can't write}$

$\rightarrow [0-25-8] \rightarrow [0123678]$

Space is a char in reg ex

③ $[-ab]^*$ \rightarrow this can be anything except 'ab'

$[-abcd]^* \rightarrow$ this ... $\{t\} \text{ except 'a', 'b', 'c', 'd'}$

④ $[a-z] \rightarrow$ either 'a' or 'z'

$\{t\} \text{ underscore}$

alphabet \rightarrow (prog ex)

① peg ex:
 $[A-Z a-z]^*$

variable no identifiers

② peg ex: P.E. of identifiers

$[alpha\ bet] [alpha\ bet + number]^*$

Int - x;

↳ from above this is matched to write

with variable, but it isn't a variable.
So, that we have to define the neg ex. of the keywords first.

" " → Exact matching (literals)

• keywords have no neg ex., if it is exact matching.

• v ("if"|"else") → we can't define the neg ex. of keywords in a class.

• we have to write the neg ex. of keywords at the first of prog then the neg ex. of variables / identifiers.

• ["if"|"else"] → not connect as too many 'ops' as every char is treated as single char.

- 4 ":" 5 \rightarrow If there's a single char only then we can write it in the b/w class.

It (char) is simple & define some range

Floating constants

peg ex - $[0-9]^+ \cdot [0-9]^+$ → [In cProg
we can't write ".09"
for this neg ex]

It gives the LA does → lexemes the name tokens (tokenization)
Int x ; (Every idp is lexeme)
→ lexeme, which LA gives name identifiers

RE
integer constant $[0-9]^+$

floating "

identifiers $[a-zA-Z][a-zA-Z0-9]^*$ [as a.t=a]

keywords (int|"if"|"else") But normally t is not a string (Id)

operators "+ "+" "-" "=" "<=" "<=" "<=" "<"
[we have to write all keywords like this]

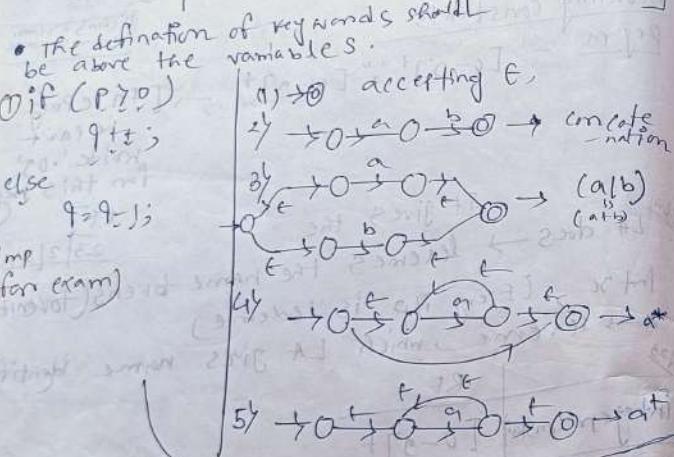
RegEx (eg - "+ "+") lone one will be at 1st

then the 2nd ones
If the prefix is same then we have to see v the lengthiest neg ex.
look for

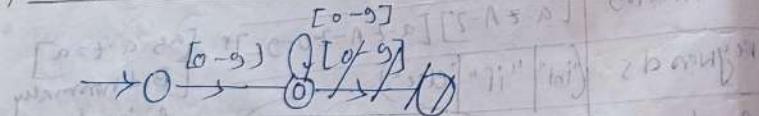
Separators

$[> ;] \cup \{ \} \cup \text{tab } \backslash n \cup \text{space}$

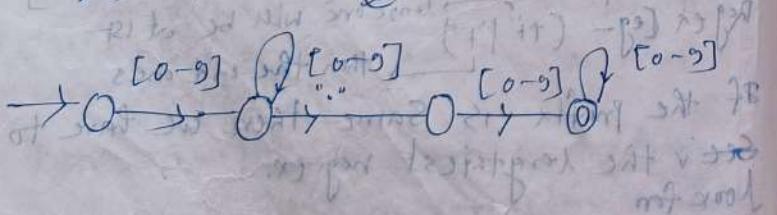
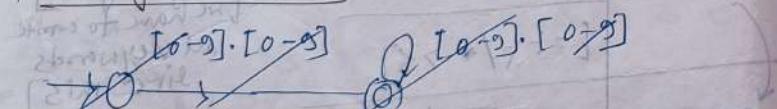
\uparrow it is itself a symbol so we can keep it into/ write it into class.



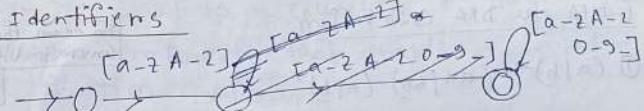
FA
Integers constant



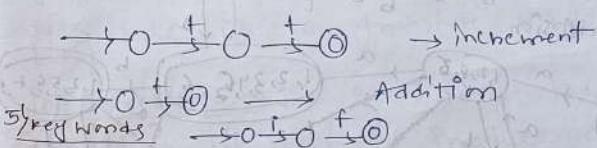
Floating constant



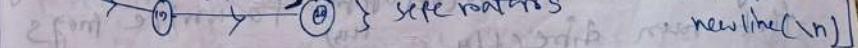
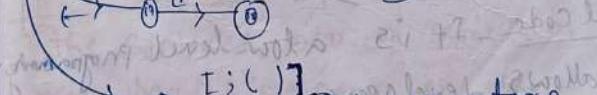
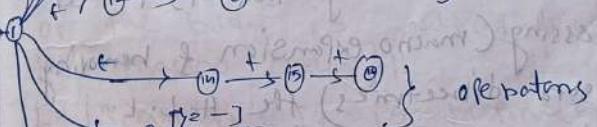
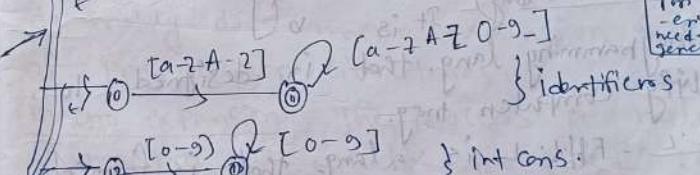
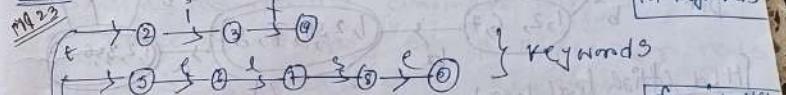
Identifiers



Operators



Ans



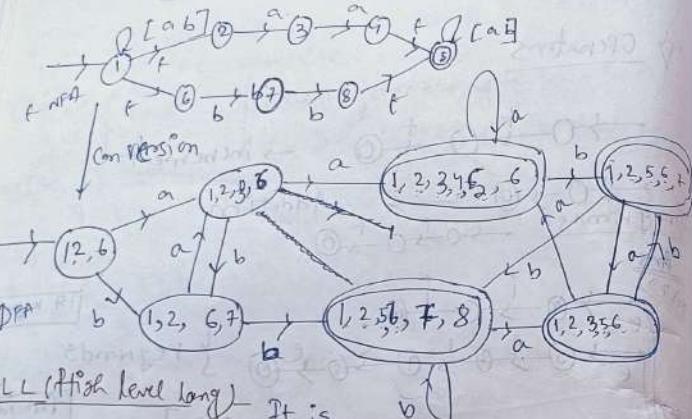
can ignore space, tab, newline ($\backslash n$)

the finite automata of FMS(1)

- operators ~~can~~ also act as separators as (int + acting as sep)
- sep = $\{ \} \cup \{ \} \cup \{ \} \cup \{ \}$

t-NFA to DFA Conversion

$$\text{① } (a|b)^* (aa/bb) (a|b)^*$$



[HLL (High level lang)] It is a programming lang. that is designed to simplify Computer prog.

E-HLL - F-HLL is a lang. that is formed by PreProcessing (macro expansion & removing Preprocessor directives) the H.L.L.

Assembly level code - It is a low-level program lang. that allows developers to write programs that run directly on a computer's CPU.

Low L.L - It is a programming lang that provides little or no abstraction from a computer's instruction set architecture. It deals with a computer's hardware components & constraints.

System SW - It is a collection of Progs & Software Components that enable a Computer to function properly.

Compiler - It's a Software prog that translates Source Code into machine code by tc code or another programming lang. It's rule is to translate H.L.L into L.L.L.

Pre-processor - It is a prog that processes it's i/p data to produce o/p that is used as i/p in another prog. It is a process where a Preprocessor reads symbols & macros to substitute. It mainly does macro expansion & removal of Preprocessor directives.

Macro expansion - The process of replacing a macro call with the processed copy of the body is called expansion of the macro call.

LA / Scanner - It is responsible for breaking the syntaxes into a series of tokens, by removing whitespace in the source code. It is also known as scanner. It takes source code as i/p & scans it & give a series of tokens as o/p. Token is a predefined sequence of chars that represents a unit.

Syntax analyzer - It is a process that checks if source code follows the grammatical rules of a programming lang. It is also known as parser. It generates parse tree.

Semantic analyzer - It is a process that checks a program's semantic consistency with the language definition. It makes sure that declaration & statements of prog are semantically correct. It uses a syntax tree & symbol table to gather type info & check for semantic errors. It is also called SDT. It generates annotated syntax tree.

Intermediate Code generator - It is the process of creating a representation of source code that is closer to the target machine lang. (I.e. generation), the mark which generates it is called ICG. If generates machine independent code.

Code generator - It is a compiler that translates the intermediate representation of the source prog. into target prog. In other words, a code generator translates an abstract syntax tree into machine-dependent executable code. It generates machine dependent code.

Code optimizer - code optimization is the process of improving the performance & efficiency of a prog. by changing its code. This is done by code optimizer. Its main objectives are to reduce resource consumption.

- ↳ To decrease execution in time.
- ↳ To ensure that the prog uses minimal memory.
- ↳ To lessens CPU time
- ↳ To deliver optimum speed.] //

Symbol table - It is a data structure that stores info about every symbol in a prog. [It is used by a lang. translators, like compilers / interpreters, to help the compiler determine the semantics of a source prog]

Parse tree - It is a hierarchical representation of terminals & non-terminals. - Is, on the other words, it is an ordered rooted tree that represents the syntactic structure of a string according to some context-free grammar.

Annotated Syntax tree - It is a parse tree that shows the values of the attributes at each node. It is generated after the rectification in parse tree & reduced by the well-formed tree.

single/multi pass compiler - A type of compiler that passes through the parts of each compilation unit only once, immediately translating each code section into its final machine code.

multipass compiler - A type of compiler that processes the source code or abstract syntax tree of a prog several times.] //

Lex/flex - It is a computer prog that generates lexical analyzers. LA, also called tokenizers, identify lexical patterns in i/p progs & convert i/p text into a sequence of tokens. It is commonly used with the Yacc parsers generators. Lex works by - i) Reading an i/p stream that specifies the lexical analyzer. ii) Writing source code in the C programming lang. that implements the lexical analyzer. iii) Converting the stream of characters into tokens.

iv) Dividing the i/o into meaningful units.

v) Discovering the relationships among the units.

lex is the standard lexical analyzer generator on many Unix & Unix-like systems.

The lex tool itself is a compiler.

Yacc - It stands for Yet Another Compiler compiler. It provides a tool to produce a parser for a given grammar. [Yacc is a prog. designed to compile a LALR(1) grammar. It is used to produce the source code of the syntactic analyzers of the lang. produced by LALR(1) grammar. The i/p of Yacc is the rule of grammar & the o/p is a C prog.] //

It is a grammar parser & parser generator that reads a grammar specification & generates code that can organize i/p tokens in a syntactic tree.

It helps in converting lang. rules into code that can understand & interpret complex lang. structure in langs like C/C++. It is a prog. designed to compile a LALR(1) grammar. Its specification consists of 3 parts - i) Yacc declarations, ii) Translation rules, iii) User-defined auxiliary procedures.] //

Error handling - Error detection + error report + error recovery. In C/C++, error handling is the process of detecting & managing errors [or exceptional conditions] during compilation. This process is done by error handling.] //

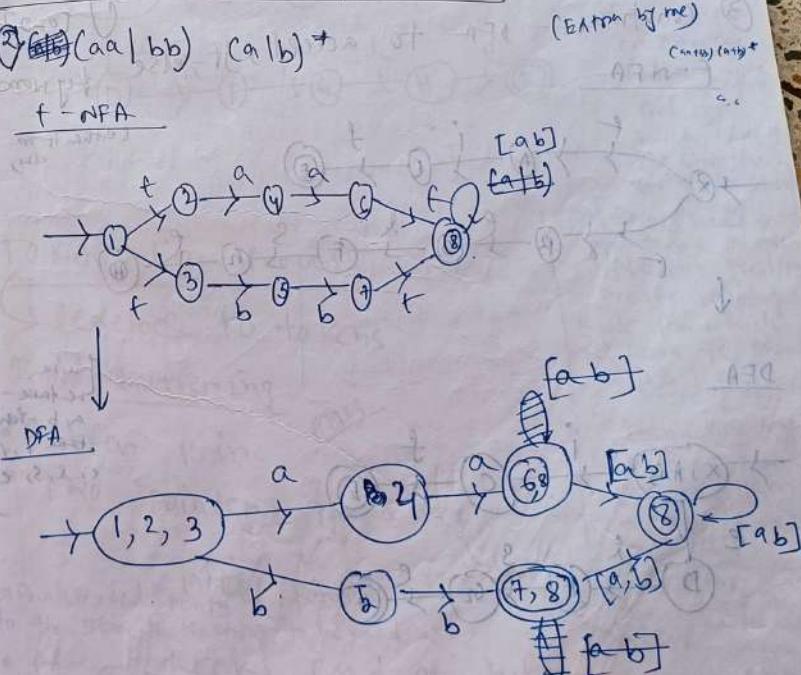
SDT engine - It is a technique of compilers execution where the source code translation is totally conducted by the parser, is known as SDT. The parser primarily uses a CFG or the parser's primary uses a CFG to check the I/P sequence & deliver O/P for the compiler's next stage. It can also convert infix to Postfix expression.

Code generator - It is the final phase of a compiler model. It takes an intermediate representation of the source code as I/P & produces an equivalent target prog as O/P. Code generation is part of a compiler's process chain. It converts the source code's intermediate representation into a form that the target system can execute. Sophisticated compilers often go through multiple passes over various intermediate forms: the I/P to the code generation is usually an abstract syntax tree or a parse tree, this tree is then converted into an intermediate lang. like three address code.

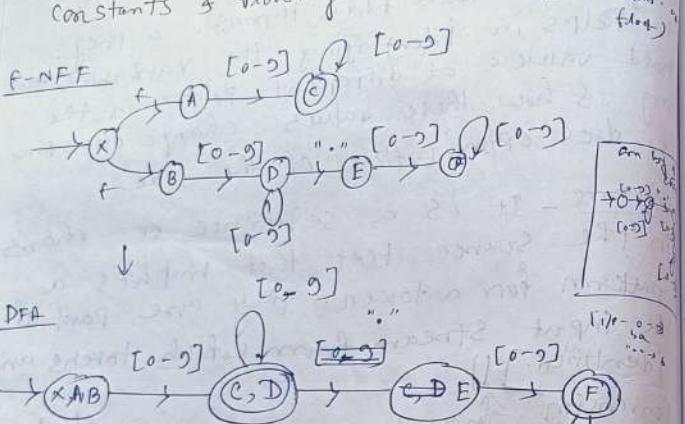
Flow engine - Data flow analysis in compiler design is a technique used in C.D. to analyze how data flows through a prog. It helps in identifying the variables that hold values at different points in the prog. & how these values change over time. It does optimization of the code.

Lexeme - It is a sequence of chars in the source text that matches a pattern for a token. They are part of the input stream from which tokens are identified.] 11 24/2/24

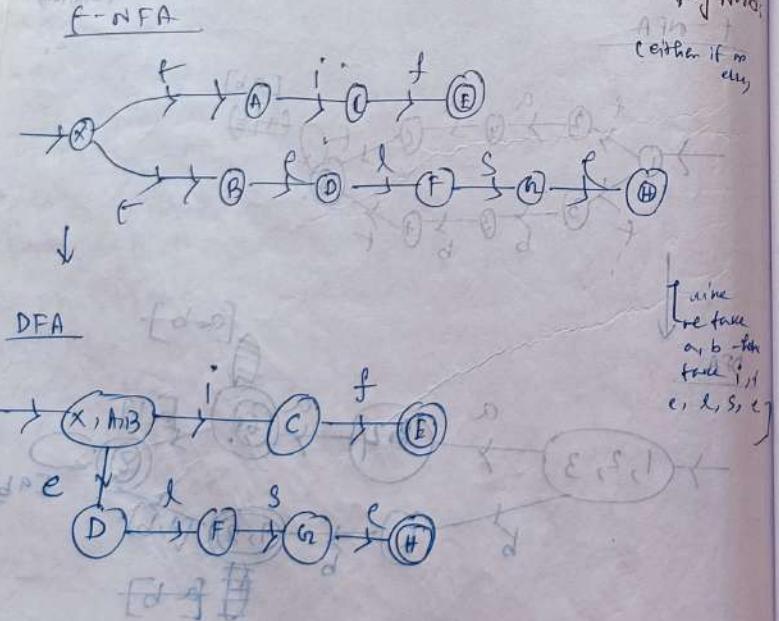
Conversion from t-NFA to DFA



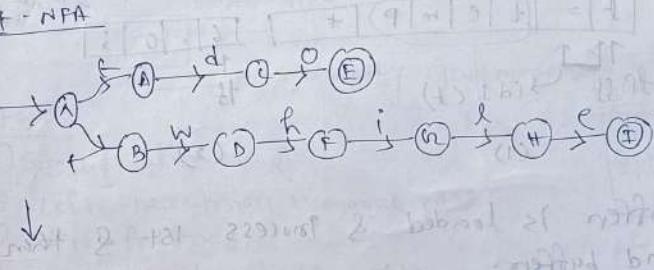
② Construct a DFA to identify integers
constants & floating constant.



③ Construct DFA to accept if-else key word.



④ Construct DFA to accept do-while keywords.



[Every word/alphabets are l/p here but we know from F-NFA or can see from F-NFA that for which l/p the state is giving o/p state]

[So, no need to think of other l/p's except the l/p's on the working state for which we have to show o/p's] [If there are any common b/w 2 keywords, then the rule isn't applicable]

TO Tokenization Process

→ lexemes to tokens
Input buffering

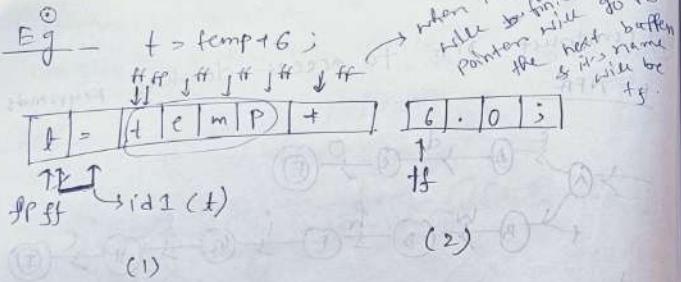
• Buffer pair

• Two pointers fp & tp

• fp - initial pointer to the start of the string to be read

• tp - checks end of buffers of next char.

• Pointers that move forward → a pointer that moves ahead to search for the end of a sentence.



If Buffer is loaded & process 1st & then 2nd buffers.

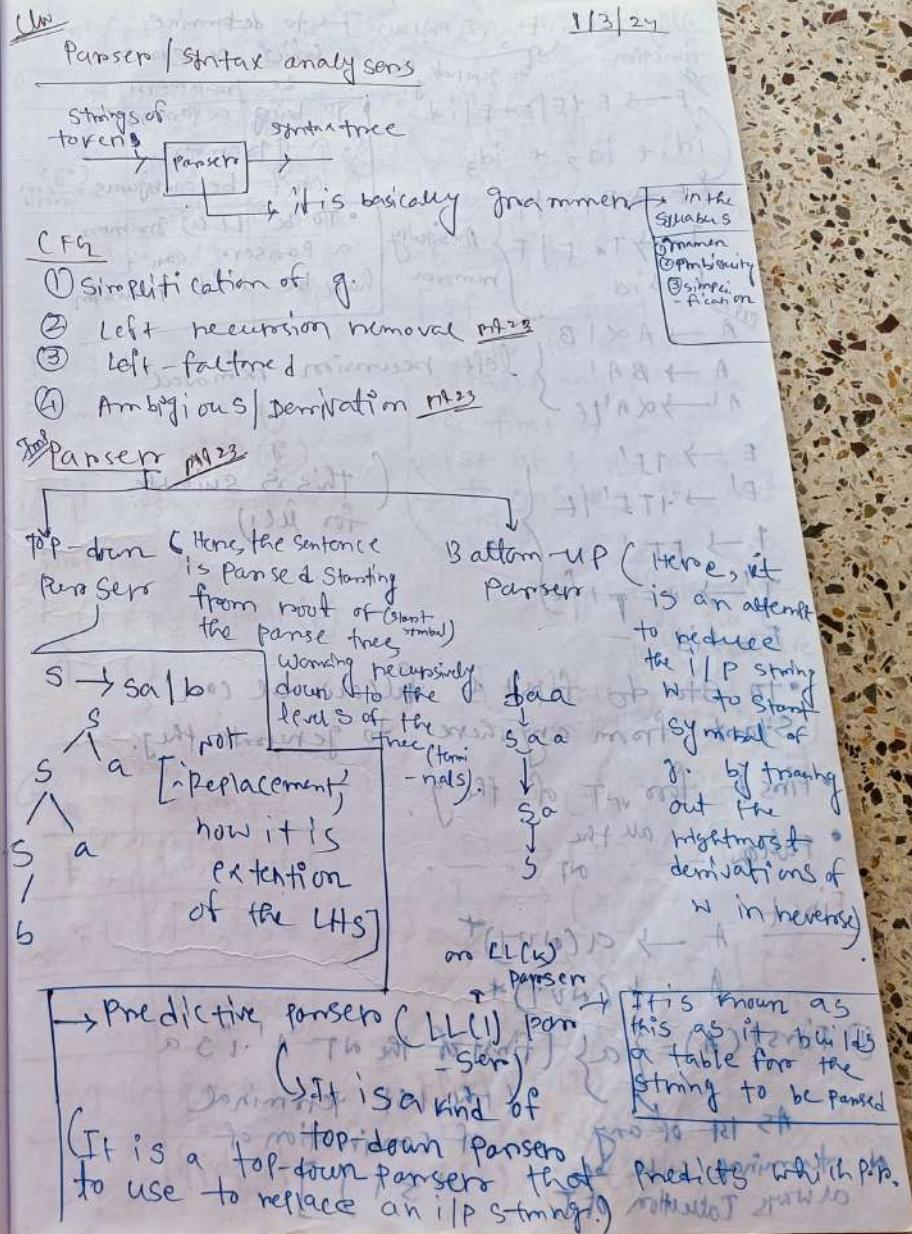
Sentinels - $\begin{array}{|c|c|} \hline \text{10} \\ \hline \end{array}$

Advantage of '10' - less checking time

Disadvantage of '10' - less storage

- Rules of vowels - [aeiouAEOU]
- Rules of consonants - [-aeiouAEIOU]
- Input buffering - It is a technique that allows a compiler to read i/p in larger chunks. It improves the performance by reducing the overhead.

Buffers - Buffers play a key part in I/P buffering. They are typically implemented as 2 buffers, one for reading characters & other for buffering the lexems.



Alg/ ① First, ② Follow
function to generate the

$$E \rightarrow E + E / E * F / id$$

$$(id + id_2 + id_3)$$

$$E \rightarrow E + T / T$$

$$T \rightarrow T * F / F$$

$$F \rightarrow id$$

$$A \rightarrow A \alpha_1 B$$

$$A \rightarrow B A'$$

$$A' \rightarrow \alpha A' \beta$$

$$E \rightarrow T E'$$

$$E' \rightarrow + T E' / t$$

$$T \rightarrow F T'$$

$$T' \rightarrow + F T' / f$$

$$F \rightarrow id$$

• To start do first & follow we can start from anywhere to generate them.

• Find first for α of the g.

• Follow all the NT's.

$$\text{First } A \rightarrow a (NVT)^*$$

$$A \rightarrow (NVT)^*$$

① $\text{First}(A) = \{a\}$ [first of the NT A is a terminal]
As 1st of any combination of terminals will be always followed by $(NVT)^*$

→ to determine which parser can be grammar.

• To bring a parser by a grammar can't be ambiguous.

• To be LL(0) grammar a parser can't have left recursion.

left recursion removal

N	First	Follow
E	{id}	\$, \$, \$
T	{id}	\$, \$, \$
F	{id}	\$, \$, \$
E'	{+, *, \$}	\$, \$, \$
T'	{*, +, \$}	\$, \$, \$

First(E)

= First(+TE') U

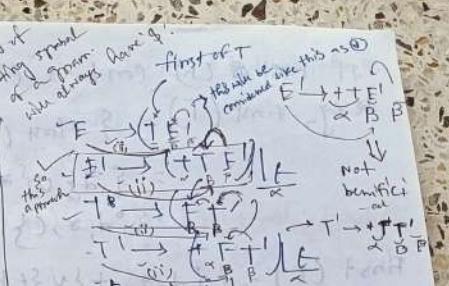
First(E)

= {+, E'}

First(T')

= F (+ET') U First(t)

= {*, +}



[If the first of

F is the 1st +

without any 't' then

the first of F is

1st of + NO need

to consider the next

NT]

If terminal

only no 't'

then applicable

[t is also only

treated as fi, First(E)

③ For practice only,

First

E

F

T

T'

F

E → (TE')

E' → (+TE') | C

T → (FT')

T' → (*FT') | C

F → id | C

F(E) = F(T)

F(T) = F(F)

F(F) = {id, t}

If $\text{First}(n)$ contains ϵ , then,

$$\begin{aligned}\text{First}(T) &= \{\text{First}(F) - \epsilon\} \cup \text{First}(T) \\ &= \{id, *, t\} \cup \{*\} \\ &= \{id, *, t\}\end{aligned}$$

$$\text{First}(E) = \{id, +, *\} \cup \{t\}$$

$id, +, *, t \rightarrow$
 $+ id, +, *, t \rightarrow$
 $AS id B$
 next of first is
 t to last
 t to symbol
 b/w t & t getting
 last abiding of t

$$③ S \rightarrow AAB \mid bBC \mid ABb$$

$$A \rightarrow (B \epsilon) \mid (\epsilon A B)$$

$$B \rightarrow e \mid \epsilon$$

$$C \rightarrow g \mid t$$

$$F(S) = \frac{S}{B}$$

$$B(B \epsilon)$$

$$N \downarrow$$

$$S$$

$$A$$

$$B$$

$$C$$

$$F(B) = \{e, \epsilon\}$$

$$F(C) = \{g, t\}$$

$$F(S) = F(AAB)$$

$$F(bBC) \cup$$

$$F(ABb)$$

$$\{bde, g, \epsilon\} \times \{a, b, d, e, g, \epsilon\} \quad [this g \text{ doesn't generate}]$$

$$\{d, e, g, \epsilon\} \times \{a, b, d, e, g, \epsilon\}$$

$$\{e, t\} = (T) \quad [to \quad \{a, b, d, e, g, \epsilon\}]$$

$$\{g, t\} = (T) \quad [to \quad \{a, b, d, e, g, \epsilon\}]$$

$$\{eb, \epsilon\} = (T) \quad [to \quad \{a, b, d, e, g, \epsilon\}]$$

[AS we are getting a]
 hence (AAB) we
 need to proceed in 3rd
 Prod rule]

First

If: $A \rightarrow \alpha(NUT)^*$ & α is a terminal then,
 $\text{First}(A) = \{\alpha\}$

else: $A \rightarrow N(NUT)^*$

$\text{First}(A) = \text{First}(N)$, if $\text{First}(N)$ doesn't have ϵ .
 else

$$\{\text{First}(n) - \epsilon\} \cup \text{First}(NUT)^*$$

Follow

If: $A \rightarrow \alpha B$
 $\text{Follow}(B)$

$[\alpha = \text{could be NT \& } t]$
 $[\beta = \text{NT}]$
 $[\beta, \text{ & also } \beta \text{ can contain } \epsilon \text{ too}]$

$\text{Follow}(B) = \text{First}(B)$, if $\text{First}(B)$ doesn't have ϵ .

$$(i) \text{Follow}(D) = \text{First}(B) - \epsilon$$

(ii) Everything of $\text{Follow}(A)$ should be copying $\text{Follow}(B)$, i.e. $\text{Follow}(D)$ which contain $\text{Follow}(A)$, $\text{Follow}(D) = \text{Follow}(A)$

$$E^1 \rightarrow + + E^1$$

$$E \rightarrow T E^1$$

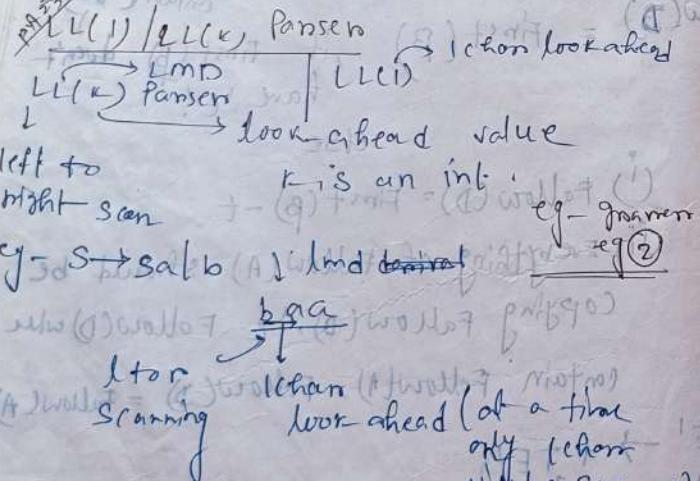
$$\left. \begin{array}{l} \\ \end{array} \right\} \rightarrow T \text{ is upgraded}$$

④ as, $f(E)$ will be in $\text{Follow}(E^1)$, $F(E) = \epsilon$
 i.e. nothing is going to be inputted, & as f then have to remove ϵ (again there won't be

anything) & similarly rule (II) of else token
will not give anything, overall we will not
get anything. That's why.

sentinels - they are used to making a
check, each time when the forward
pointer is converted, a check is
completed, to provide the 1 half
of the buffer has not converted off.
If it is completed, then the others
half should be reloaded.

5/3/24



- When a grammar will be LL(1) parser
- ① There will be no ambiguity (min requirements)
 - ② Left recursion should be removed (necessary requirements)
 - ③ ~~left~~ should be left factored

[As grammar satisfies those 3 conditions, so we can read it
as LL(1) parser]

	+	*	id	3 special chars
E ₁			E → STE'	
E'	E' → E*			E' → E
T	b	F → FT'		
T'	T → E	T' → FT'	T' → E	
F			F → id	

[from which P is
we got the first(N)
we have to write text
P is under the first
of that NT]
[if there is
no E in first
of NT, no need
to calculate follow]

[∴ the first (E)
= first (T) =
first (F) is id]
[then there
will be E at
first of any NT
then we have
to write the
P for which
we got 'E' into
in the entry
it's all falling]

Here, there's no more than
1 element in any entry, so
we can call it LL(1) parser.
i.e., this grammar is
LL(1) parser.

④ In any * entry there should
not be more than 2 # 1

Prod. rule 3. (main refinement)

• If there is left recursion we will not use that
grammar grammar for this (↑) eg (2) grammar by
LL(1)

E → TE	P bi	P bi
E' → T E' t	P bi	P bi
T → F T	P bi	P bi
T' → * F' T' t	P bi	P bi
F → id	P	P

Parses		
Stack	Input	Action
E \$	id + id + id \$	B → T E' (newline \$ by @ T E')
T E' \$	id + id + id \$	T → F T'
E T' E' \$	id + id + id \$	F → id
id T' E' \$	id + id + id \$	POP
(+) T' E' \$	+ id + id \$	T' → E
(+) E' \$	+ id + id \$	E' → + T E
T T E' \$	+ id + id \$	POP
+ T E' \$	id + id \$	T → E'
F T' E' \$	id + id \$	F → id
id T' E' \$	id + id \$	POP
T' E' \$	+ id \$	T' → + F T E'
* F T' E' \$	* id \$	POP
F T' E' \$	id \$	F → id
id T' E' \$	id \$	POP
T' E' \$	\$	T' → E

E' \$	\$	E' → E
\$	\$	
↓ Accepting condition		
Yes, this string can be generated by this grammar (eg ②)		
Action	From a tree by seeing (Scoring) ↓ + from top-bottom	<pre> E / \ T E' / \ F T' id E </pre>
• i/p = id + + id		
Stack	i/p	Action
E \$	id + + id \$	E → S T E I
+ T E' \$	id + + id \$	T → F T I
F T' E' \$	id + + id \$	F → id
id T' E' \$	id + + id \$	POP
T' E' \$	+ + id \$	T' → + T
E' \$	+ + id \$	E' → + T E I
+ T E' \$	+ + id \$	POP
T' E' \$	+ id \$	T' → E
No, this string can't be generated by this grammar.		

• i/p - id id+id		
Stack	i/p	Action
F\$	id + id \$	B → STE1
TE1\$	id + id \$	T → FT1
FT1'E1\$	id + * id \$	P → id
idT1'E1\$	id ** id \$	POP
T1'E1\$	* id \$	T1 → * FT1
* FT1'\$	* id \$	POP T1
FT1'E1\$	* id \$	b1 + b1
no, -		

LL(1) / LL(k) Parser - An LL(1) + Parser is a top-down parser [that uses one-token lookahead] if the lookahead is called an LL(1) parser. If the first L indicates that the i/p is read from left to right. The 2nd L says that it produces a left-to-right derivation. An LL(k) parser is called an LL(k) parser if it uses k tokens of lookahead when parsing a sentence. It means 1 character at a time (i.e., lookahead value).

bit f'FT

answering 2st for between of id + no) point 2 id in

Q) first & follow calculation

$$\begin{aligned} S &\rightarrow DB \mid DBB \mid BA \\ A &\rightarrow da \mid BP \\ B &\rightarrow g \mid f \\ D &\rightarrow h \mid C \end{aligned}$$

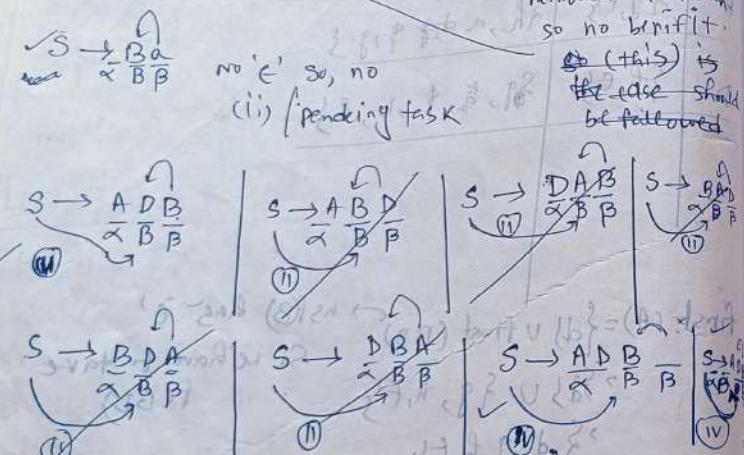
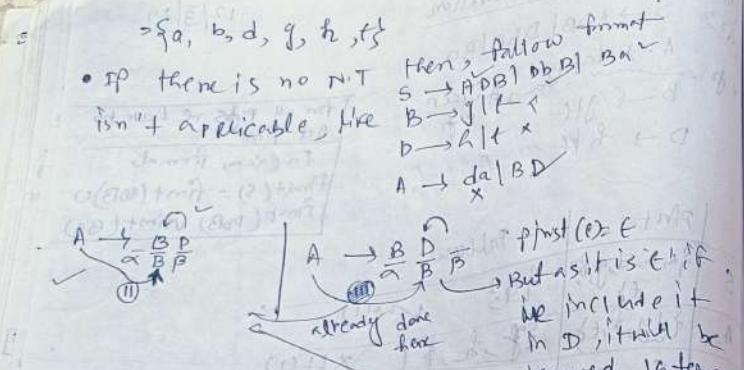
12/3/24

for all P's we have to get first.
In exam, formats
 $\text{first}(S) = \text{first}(ADB) \cup \text{first}(DBB) \cup \text{first}(BA)$

	First	Follow
S	{a, b, d, g, h, t}	{\$, t} (LL(1))
A	{d, g, h, t}	{t, h, \$} (LL(1))
B	{g, t}	{h, a, *, \$, f, g}
D	{h, t}	{a, b, \$, b, h}

$$\begin{aligned} \text{first}(A) &= \{d\} \cup \text{first}(BD) \rightarrow \text{as } f(B) \text{ has } 'e' \\ &\rightarrow \{d\} \cup \{g, h, t\} \rightarrow \text{So we have to take } f(BD) \\ &\rightarrow \{d, g, h, t\} \end{aligned}$$

$$\begin{aligned} \text{first}(S) &= \text{first}(ADB) \cup \text{first}(DBB) \cup \text{first}(BA) \\ &\rightarrow \{f(A) - t\} \cup f(BD) \cup \{f(D) - t\} \cup f(BB) \cup f(A) \cup f(B) \cup f(A) \\ &\text{another approach} \\ &\text{if containing } f(A) - t \rightarrow \text{containing } f(B) - t \rightarrow \text{containing } f(C) - t \\ &\text{if } f(A) \text{ contains } 'E' \rightarrow \text{Rst part contains } 'E' \\ &\text{so instead of } f(A) \text{ we have to take } f(A) - t \text{ (all except } f(A) \text{)} \\ &\text{if } \text{first}(A) \text{ has } 't' \text{ so we take this} \end{aligned}$$



$\checkmark S \xrightarrow{\text{IX}} \frac{D}{\alpha} \frac{B}{\beta} \frac{B}{\gamma}$
 as $f(BB)$ has no
 already done upon
 So this will be has
 no benefits.

\bullet It is not LL(1) parser, as in a 1 cell
 (and there is more than 1 entry) [thick lines]

⑤ $S \rightarrow A \underline{a} \underline{A} \underline{b} \underline{B} \underline{b} \underline{B} \underline{a}$
 $A \rightarrow t$
 $B \rightarrow f$
 St. w/ non-term.
 first follow

			[if it contains t, then check intensi- tion of first & follow , if no 'e' no need to check it is LL(1) parser.]
S	$\{a, b\}$	$\{t\}$	
A	$\{t\}$	$\{a, b\}$	
B	$\{f\}$	$\{b, a\}$	

$\checkmark S \xrightarrow{\text{I}} \frac{A}{\alpha} \frac{a}{\beta} \frac{t}{\gamma}$ $\checkmark S \xrightarrow{\text{II}} \frac{A}{\alpha} \frac{a}{\beta} \frac{B}{\gamma}$
 $\checkmark S \xrightarrow{\text{III}} \frac{B}{\alpha} \frac{b}{\beta} \frac{B}{\gamma}$ $\checkmark S \xrightarrow{\text{IV}} \frac{B}{\alpha} \frac{b}{\beta} \frac{a}{\gamma}$

⑥ $A \rightarrow xB1Dy$
 $B \rightarrow AzD$
 $D \rightarrow zB1tx$
 $B \rightarrow AzD$
 $D \rightarrow zB1tx$
 $B \rightarrow AzD$
 $D \rightarrow zB1tx$
 $A \rightarrow xB1$
 $\checkmark A \xrightarrow{\text{V}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{VI}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{VII}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{VIII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{IX}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{X}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XI}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XIII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XIV}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XV}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XVI}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XVII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XVIII}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XIX}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XX}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XXI}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXII}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XXIII}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXIV}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XXV}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXVI}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XXVII}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXVIII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XXIX}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXX}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XXXI}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXXII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XXXIII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXXIV}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XXXV}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXXVI}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XXXVII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XXXVIII}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XXXIX}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XL}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XLI}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLII}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XLIII}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLIV}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XLV}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLVI}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XLVII}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLVIII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XLIX}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLX}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XLXI}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLXII}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XLXIII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLXIV}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XLXV}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLXVI}} \frac{D}{\alpha} \frac{z}{\beta} \frac{x}{\gamma}$

$\checkmark A \xrightarrow{\text{XLXVII}} \frac{x}{\alpha} \frac{B}{\beta} \frac{1}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLXVIII}} \frac{D}{\alpha} \frac{y}{\beta} \frac{B}{\gamma}$

$\checkmark A \xrightarrow{\text{XLXIX}} \frac{x}{\alpha} \frac{z}{\beta} \frac{D}{\gamma}$
 $\checkmark A \xrightarrow{\text{XLXVII}}$

If there is no t in
 first(n) & follow(n)
 If t then LL(1) parser
 be definitely
 LL(1) parser
 but if t then
 do this

If t occurs
 if there is t in first

Hw Draw Parsing table for Q & Q.

④ Parsing table

	a	b	d	g	h	\$
S	$S \rightarrow Ba$	$S \rightarrow Db$	$S \rightarrow Ab$	$S \rightarrow Bhd$	$S \rightarrow ABP$	$S \rightarrow ABD$
A			$A \rightarrow da$	$A \rightarrow Bd$	$A \rightarrow Bp$	$A \rightarrow BD$
B			$A \rightarrow Bd$	$A \rightarrow Bp$	$A \rightarrow BD$	
D						

more than 1 entry → so
it need to
proceed, it
can't be
LL(1) Bcuse

⑤ Parsing table

	a	b	\$
S	$S \rightarrow Aab$	$S \rightarrow Bba$	
A	$A \rightarrow f$	$A \rightarrow g$	
B	$B \rightarrow f$	$B \rightarrow g$	

⑥ Parsing table

	x	y	z	\$
A	$A \rightarrow zB$	$A \rightarrow y$	$A \rightarrow zy$	
B	$B \rightarrow zD$	$B \rightarrow zD$	$B \rightarrow zD$	

→ this grammar
isn't a LL(1)
more than 1 entry

Q

Bottom-up
parsers

15/3/24

Operators

Precedence

Parsers

Shift-reduce parser
(LR(0))
→ LR(0) Parser
→ LR(0) (SLR(0))
→ CLR(0) (ISLR(0))
→ LALR(0) (LR)
↓
(look ahead)

- LR(0)
 - left to right scan
 - look-ahead value

Closure of items

2) Necessary conditions for LR(0) [at least what are needed]

① There will be no ambiguity

(but it means not that only that is needed)

② $E \rightarrow E + T^*$

③ $T \rightarrow T F^*$

④ $F \rightarrow id$

↳ (Augmented rule)

For the starting symbol

We are using an extra

symbol, if we change the start

symbol

[place dot in front of NT & write it's all p.r. & give it's dot

in front of NT & write it's all p.r. & give it's dot

then it repeat]

LR(0) item sets

• $\text{goto}() \rightarrow$ It is a func that will help to transmit all prod. rules of P_0 .

It's func is to decide when to shift & when to reduce. If ~~is a grammar symbol then moving~~

$I_1 = \text{goto}(I_0, F) \rightarrow$ means in I_0 whenever

there is a \cdot before that T/NF , that \cdot will be shifted/moved after

that T/NF if we have to write that new A.P.

if there is any \cdot before NF here, so we have to do LVC 1st (make an A.P. of that

$I_2 = \text{goto}(I_0, T) \left\{ \begin{array}{l} T \rightarrow T \cdot \# F \\ T \rightarrow T \cdot \# id \end{array} \right.$

$I_3 = \text{goto}(I_0, F) \left\{ \begin{array}{l} T \rightarrow F \cdot \\ T \rightarrow F \cdot \# id \end{array} \right.$

$I_4 = \text{goto}(I_0, id) \left\{ \begin{array}{l} F \rightarrow id \cdot \\ F \rightarrow id \cdot \# id \end{array} \right.$

It will continue until the pos of \cdot 's

When not reach to the end of all

$I_5 = \text{goto}(I_1, +)$

$I_6 = \text{goto}(I_1, \cdot)$

$I_7 = \text{goto}(I_1, F)$

$I_8 = \text{goto}(I_1, id)$

$I_9 = \text{goto}(I_2, +)$

$T \rightarrow T \cdot \# F$
 $F \rightarrow id$

$I_{10} = \text{goto}(I_2, \cdot)$
 $E \rightarrow E \cdot T$
 $T \rightarrow T \cdot F$

$I_{11} = \text{goto}(I_2, F) \rightarrow [T \rightarrow F]$

$I_{12} = \text{goto}(I_2, id) \rightarrow [F \rightarrow id]$

$I_{13} = \text{goto}(I_3, +)$
 $T \rightarrow T \cdot F$

$I_{14} = \text{goto}(I_3, \cdot)$
 $E \rightarrow id$

$I_{15} = \text{goto}(I_3, F) \rightarrow [T \rightarrow T \cdot F]$
 $F \rightarrow id$

$I_{16} = \text{goto}(I_3, id) \rightarrow [F \rightarrow id]$

It generates total 8 elements.

$I_{17} = \text{goto}(I_4, +)$
 $F \rightarrow id$

$I_{18} = \text{goto}(I_4, \cdot)$
 $F \rightarrow id$

$I_{19} = \text{goto}(I_4, F) \rightarrow [T \rightarrow T \cdot F]$
 $F \rightarrow id$

$I_{20} = \text{goto}(I_4, id) \rightarrow [F \rightarrow id]$

If \cdot before $+/\cdot$ giving info of allnts in that step is already finished then stop

rest a new item has been produced that exists so we give the existing one otherwise it will continue indefinitely

it is end so we can understand that it is end as it is generating no new one

$I_{21} = \text{goto}(I_5, +)$
 $E \rightarrow id$

$I_{22} = \text{goto}(I_5, \cdot)$
 $E \rightarrow id$

$I_{23} = \text{goto}(I_5, F) \rightarrow [T \rightarrow T \cdot F]$
 $F \rightarrow id$

$I_{24} = \text{goto}(I_5, id) \rightarrow [F \rightarrow id]$

$I_{25} = \text{goto}(I_6, +)$
 $E \rightarrow id$

$I_{26} = \text{goto}(I_6, \cdot)$
 $E \rightarrow id$

$I_{27} = \text{goto}(I_6, F) \rightarrow [T \rightarrow T \cdot F]$
 $F \rightarrow id$

$I_{28} = \text{goto}(I_6, id) \rightarrow [F \rightarrow id]$

Operator Precedence parsers - It is a bottom-up parser that interprets an operator-precedence grammar. If it is a shift-reduce parser that can be easily constructed by hand. The grammar from which it can be constructed is called operator grammars. It is used to convert from human-readable infix notation, which relies on order of operations, to a format that is optimized for evaluation, such as reverse Polish notation.

Shift-Reduce parsers - It is a table-driven, bottom-up parsing method from computers languages & other notations defined by a grammar. It is also known as bottom up parsing.

LALR(1) / LR(k) / LR(0) Parsers - It is a type of bottom-up parser that uses left-to-right scanning, rightmost derivation (in reverse) & the no. of unconstrained "lookaheads" I/P symbols to make parsing decisions.

SLR(1) Parser - It stands for simple LR parser. It is a bottom-up parser that can be used to parse large classes of CFG. It is same as LR(0) parsing. The only difference

is in Parsing table] to construct SLR(1) Parsing table, we use canonical collection of LR(0) item.

CLP(1) Parsing Parser - CLP refers to Canonical LR. CLP Parsing uses the canonical collection of LR(1) items to build the CLP(1) Parsing table. [It CLP(1) Parsing produces the more no. of states as compare to the SLR(1) Parsing.]

LA LALR(1) parser - It stands for lookahead LR. To construct the LA LALR(1) Parsing table we use the canonical collection of LR(1) items. It is a bottom-up parser used in compilers.

10/3/24

Items↓	+	*	id	\$	E	T	F	Note: Item should be written in table col & across these rows to get Canonical LR P.T.
	s4				1	2	3	
0								
1	s5							
→ 2	r2	s6			r2			
→ 3	r4	r4			r4			
→ 4	r3	r5			r5			
5				s4				
6				r4				
→ 7	r1	s6		#	hi			
→ 8	r3	r3			r3			

s4
 ↓
 r4
 r2
 +
 id
 \$
 E
 T
 F

r2
 r4
 +
 id
 \$
 E
 T
 F

r4
 r2
 +
 id
 \$
 E
 T
 F

r5
 r3
 +
 id
 \$
 E
 T
 F

r3
 r3
 +
 id
 \$
 E
 T
 F

$I_{P_2} = \text{goto}(t_n, a)$
 Entry now cal
 to file

if NT = only no.
 if T = S no.

$E_2 = \text{goto}(T_2, T)$
 $E \rightarrow T$
 $T \rightarrow T + F$

No. of this Prod. rule(2)

102 will be written in

the follow of E

It's SLPC(1) parser as no cell is containing more than 1 entries.

(As here there is no conflicts, so it is SLPC)

parser.) [more clearly it is SLPC parser] 27/3/24

• Parsers reg ex of identifier =

$E \sqcup (n,) \{ \} \{ \}^* [a-z A-Z 0-9]^* [\sqcup \sqcup ; : ;]^*$

But we don't do so (as we can't separate perfectly) [if there is any non expected thing in o/p, then this is the cause, & we have to accept it like this]

• If (0) - Parsing table we can construct, but that is ambiguous.

∵ Accept in item 1, P-1 has id & the rest of P-2
 $E \rightarrow T$ (in P-2)
 ∵ row 2
 cal = E's follow
 (Always in LHS's)
 NT's follow
 & as the no. of pro is so P2

Parses (from LR(1))		(Shift = 1 step Reduce = 2 step (Replacement how stack is tos accept the nation Actions again))	
Stack (State)	Stack	i/p	
\$0 \leftarrow tos	\$	id + id + \$	sy (shift 4)
\$04 \leftarrow tos	\$ id (shift)	+ id + id \$	F $\xrightarrow{\text{F}}$ id $\xrightarrow{\text{F}}$ F $\xrightarrow{\text{id}}$ (S)
\$03 \leftarrow tos deleted	\$ F (Replace PHS with LHS)	+ id + id \$	reduce $\uparrow \rightarrow F$
\$02	\$ T	+ id + id \$	reduce $E \rightarrow T$
\$01	\$ E	+ id + id \$	Sy (shift 5)
\$015	\$ E +	+ id \$	shift 4
\$0154	\$ E + id	+ id \$	reduce $F \xrightarrow{\text{id}}$ pop
\$0153	\$ E + F	+ id \$	reduce $T \rightarrow F$ pop
\$0157	\$ E + T	+ id \$	shift 6
\$01576	\$ E + T +	id \$	shift 4
\$015764	\$ E + T + id	\$	reduce (1. clear to 1. star F $\xrightarrow{\text{id}}$ home)
\$015768	\$ E + T + F	\$	reduce 2 (3 times $\xrightarrow{\text{id}}$ so from to 3 then will be removed)
\$015767	\$ E + T	\$	reduce $E \rightarrow E + T$
\$01	\$ E	\$	accept or ACCEPT

• If got stuck at any point, then rejected.

- If it is forming RMB tree in reverse [LR] PMS in reverse

$i/p = id \# id \$$

We can't construct LR(0) table from LR(1)

By the items $LR(1) \rightarrow LR(0) / LR(1)$ table

By the items $LR(1) \rightarrow (LR(1)) / LR(0)$ parsing table

 $\Rightarrow i/p = id \# id$

SLR(1) Parsing

Stack (State)	Stack	i/p	Actions
\$0	\$	id # id \$	shift 4
\$04	\$ id	# id \$	reduce F \rightarrow id
\$03	\$ F	# id \$	reduce F \rightarrow F
\$02	\$ T	# id \$	shift 6
\$026	\$ T +	# id \$	not accepted

This string can't be generated by this grammar.

LR(1) Parsing

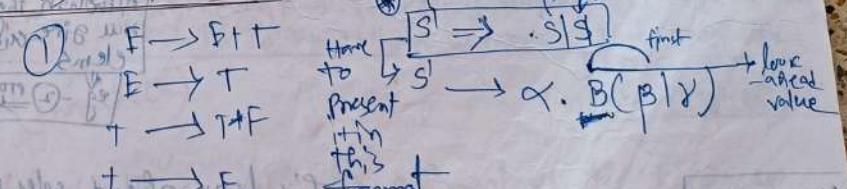
Stack (State)	Stack	i/p	Actions
\$0	\$	id # id \$	shift 4
\$04	\$ id	# id \$	reduce F \rightarrow id
\$03	\$ F	# id \$	reduce F \rightarrow F
\$02	\$ T	# id \$	reduce F \rightarrow T
\$01	\$ E	# id \$	shift 5
\$015	\$ E +	# id \$	not accepted

This string can't be generated by this grammar.

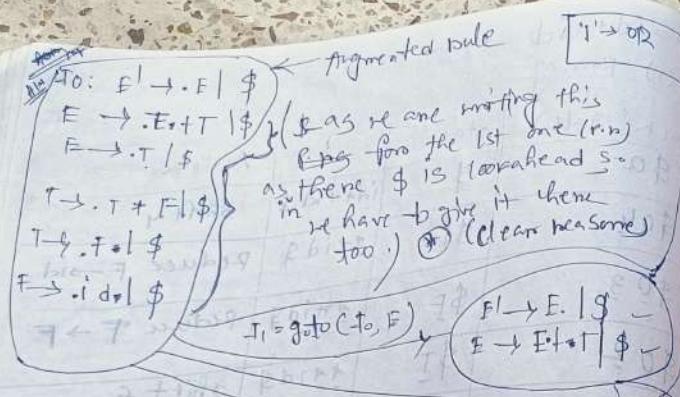
CLR(1)

It will form from LR(1) item sets

LR(1) Item Sets



extra start symbol \oplus first item
look ahead value \times look ahead value
present item \downarrow look ahead value

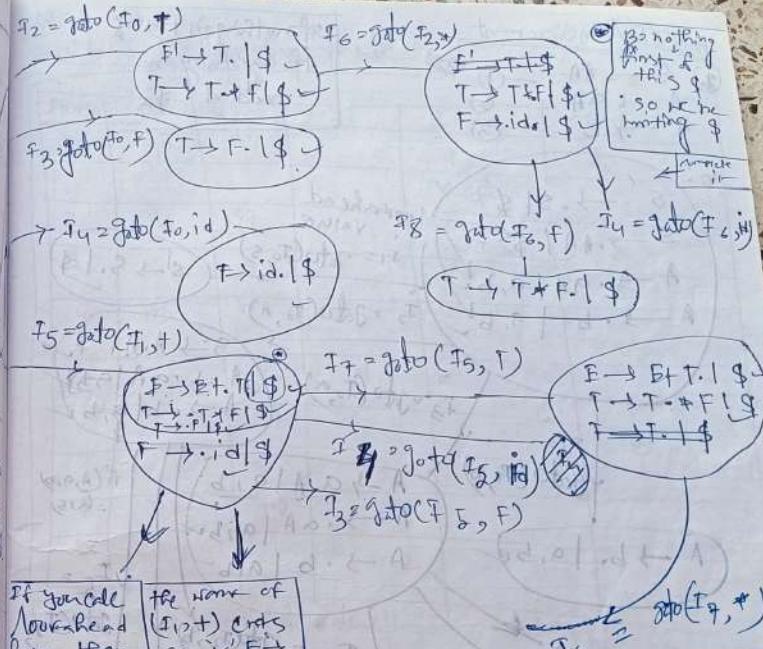


(as we are writing this
E.g. for the 1st one (r.n.)
as there \$ is lookahead \$.
we have to give it where
too.) (clear reason)

It generates total 8 elements
(like previous one).

new PNs may
have to write
(don't think
that same
PNs come
there in any
item) (E.g. for
F, I, \$)
have to do w.r.
for them too
although they
will give all
elements

Eg - (2) \Rightarrow



If you call
lookahead
here, then
the lookahead
of T will
be matched
with it
(so you can
simply take look
from T) (cancel here)

But better is
always calc.
here

Eg
In general ($E \rightarrow \cdot E + T \mid \$$) is shifting
so both lookahead will be same.

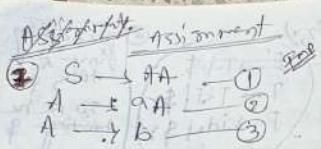
(crosscheck)

After more
than 2 tokens (NT/T)
LR = call Lh
may be \$, or
may be something
else @ is transformed

If need to calc
new lookahead
in that case the
lookahead will be
new (Eg - (2)) \Rightarrow

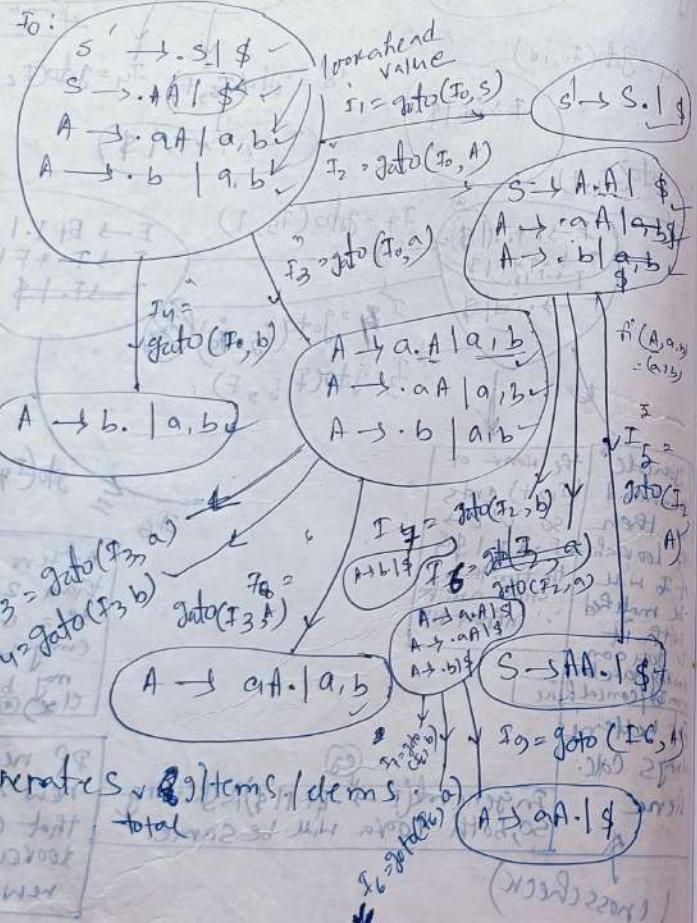
(clear reason)
For 1st, $F^1 \rightarrow \cdot E \mid \$$
This lookahead will be
written all the PNs of E. In these the lookahead
for which we have had

default Lh. Here, as 'T' is informant of F so we
will be dependent on the lookahead of that PN
to write all PNs of E (i.e. $E^1 \rightarrow \cdot E \mid \$$) & like that the
further process will continue.



if nothing in first \$
if anything = no
need of \$

so:



It generates v 8 terms / items

gu or I to ignore (this is recursive)
it's not to break out the dot will not work
it's not to break out it reach the end) so we
set to it with 8 (P1.1. ← / 7.1.1) to 29 is kind of
current the (22) and next thing

CLR(1) Parsing table

Items	a	b	\$	S	A
0	S3	S4	-	-	2
1	-	-	-	-	accept
2	S6	S7	-	-	-
3	S3	S4	-	-	5
4	r3	r3	-	-	8
5	-	-	r1	-	-
6	S6	S7	-	-	-
7	-	-	r3	-	-
8	r2	r2	-	-	-
9	-	-	r2	-	-

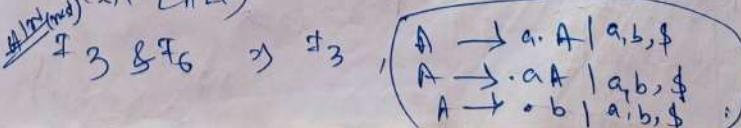
10/4/24

- LR(0) = null value
 - LR(1) → lh values
 - no of items SLR(1) < LR(1) [Because not for all time, but it can happen.]
- ↳ this is LR(1) parser (not full parser)

→ (no first val calc here, write below here, for A → b | a, b (I4))

LALR(1) → somehow an extension of CLR(1)

Same pros, just lh val differently merge them
(in LALR)



$I_7 \Rightarrow I_4$ ($A \rightarrow b$, imp)

$I_8 \& I_9 \Rightarrow I_8$, ($A \rightarrow aA$, a, b, ϵ)
more than
2 elements
items.
meaning
is also
possible)

- No of states
 $SLR(1) = LALR(1)$
- $SLR(1)$ & $LALR(1)$ acts similarly, just
in $LALR(1)$ there is no need to calc.
 f_i & f_o .
- $CLR \geq (SLR = LALR)$ (from the point
of 'no. of states')

LALR(1) Parsing table

i	a	b	ϵ	S	A
0	s_3	s_4		1	2
1					accept
2	s_3	s_4			5
3	s_3	s_4			6
4	r_3	r_3	r_3		7
5					8
6	r_2	r_2	r_2		9
7					10
8					11
9					12

Annotations:

- $s \rightarrow s$ conflict \rightarrow doesn't exist
- $s \rightarrow R$ conflict
- $R \rightarrow R$ conflict
- If generally shown in LALR(1), both can also consist in CLR & SLR
- If it is common, it can come in LALR, CLR or SLR (and generally)
- ϵ reduction is more than 2 elements items.
- ϵ meaning is also possible

Parsers

Top down

$S \rightarrow \text{input/string}$

\hookrightarrow Predictive parser
 $\text{LL}(1)$ / $\text{LR}(1)$ parser

Bottom up

$\text{input/string} \rightarrow S$

\hookrightarrow Operator Precedence parser

\hookrightarrow Shift Reduce parser
 \hookrightarrow LR(0) parser
 \hookrightarrow SLR(1) parser
 \hookrightarrow CLR(0) parser
 \hookrightarrow LALR(1) parser

Reduction is written below T always
because T one replaced to get
a NF (reduced)

- From LR(0) parsing table, if $A \rightarrow b$, then write the rule no. (for reduction) below every T.s, so, there is more chances of happening conflicts, So it is not used / less imp. and more time.

Operator Precedence Parser

operator grammar

Consecutive NT's are not possible here, then must have to be a T b/w NT's, this is operator grammar.

($S \rightarrow ABCX$)
($B \rightarrow + - * /$)
 $S \rightarrow A + C | A - C | A * C$ (operator grammar)

($* \rightarrow \text{operator}$)
 $(\text{operator})^*$

P1/2
0. P-table & operation relationship table

LHS	+	-	*	/	id	\$	PITS
+	(from the precedence relationship)	\Rightarrow	\sim (less than)	\leq	\leq	\cdot	Left is combined with PITS
-	\geq	\geq	\sim	\leq	\leq	\cdot	Left is 1st than RHS and vice versa
*	\geq	\geq	\geq	\sim	\sim	\cdot	Left is precedence operator than old operator \$
/	\geq	\geq	\geq	\sim	\sim	\cdot	
id	\geq	\geq	\geq	\geq	\geq	\cdot	
\$	\leq	\sim	\sim	\sim	\sim	\cdot	Point of view

Left is combined
with
PITS
Left is 1st
than RHS
and vice versa
Left is
precedence
operator
than old
operator \$

P.O. →
Point of
view
 $+ \sim \rightarrow$
 $L \rightarrow R$
associativity
 $I = P \rightarrow L$

P2/3
0. Grammars

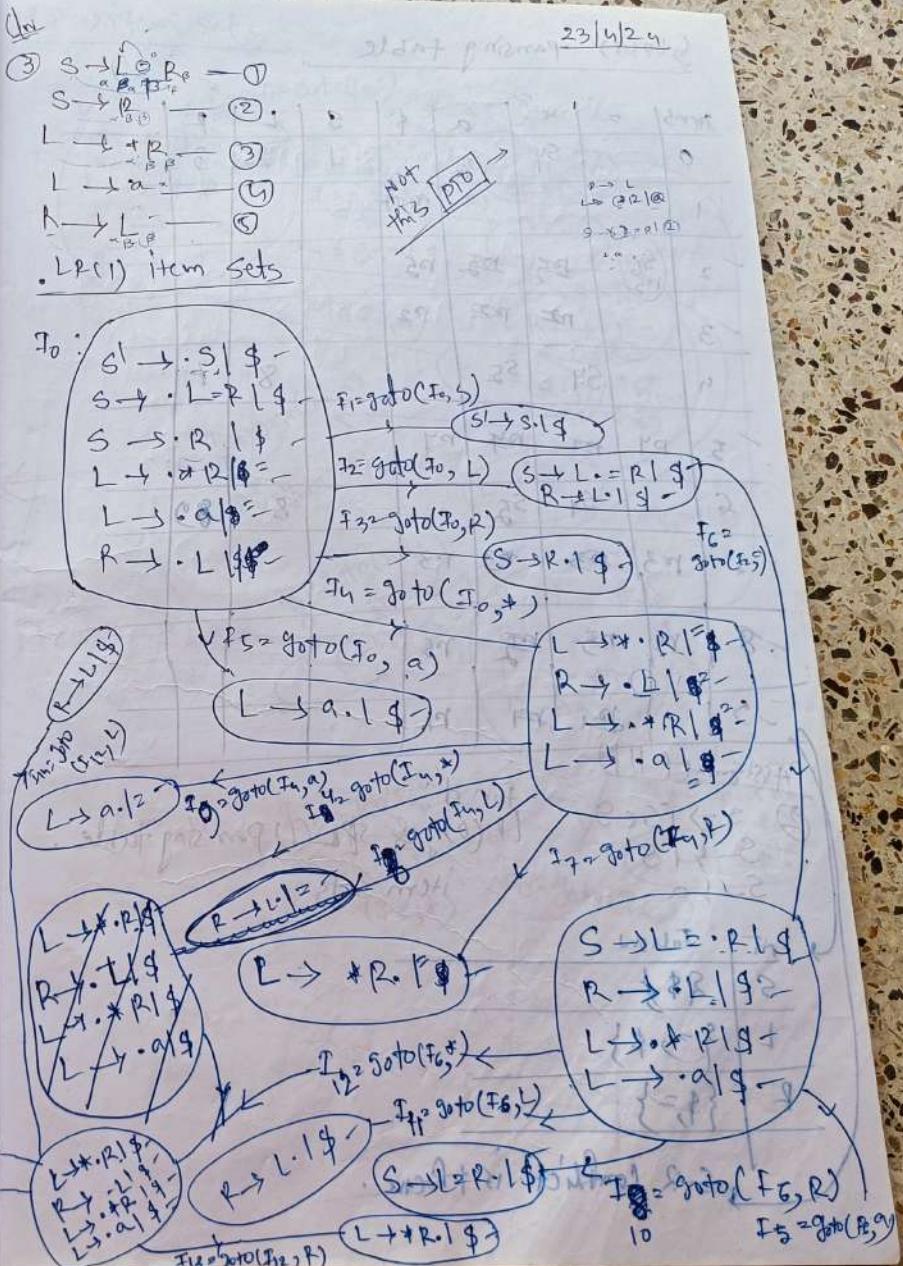
$E \rightarrow E + E \quad E * E \quad E / id$

can 'id + id' be generated from this? ?

(i/p) 0. P. Parsing

Stack	i/p	Action
\$	id + id \$	S LHS & RHS shift P
\$ id \$	id \$	shift R
\$ E	id \$	Reduction of \$ id \$
\$ F +	id \$	shift S
\$ F + id	\$	shift R
\$ E + E	\$	Reduction of \$ E + E
\$ E	\$	Accept

$I_2 = \text{goto}(S_1, *)$
 $I_5 = \text{goto}(S_1, a)$



SPL(1) parsing table

Items	=	x	*	a	\$	3	L	R
0							1	2
1							3	
2	S_6 r_6	P_5	P_2	P_5				
3		P_2	P_2	P_2				
4		S_4	S_5				8	7
5	P_4	P_7	P_7	P_4				
6		S_4	S_5				8	9
7	P_3	P_3	P_3	P_3				
8	P_3	P_5	P_2	P_5				
9		P_1	P_1	P_1				

Assignment (H1)

② $S \rightarrow i \cdot S e s$

$S \rightarrow i \cdot S t o p$

$S \rightarrow a$

find:
 $iP(0)$ & $SPL(1)$ parsing table.

item sets

NT 9 · follow 2

$SP1.2\$\$$

$L1\$1\$\$$

$R\$\$$

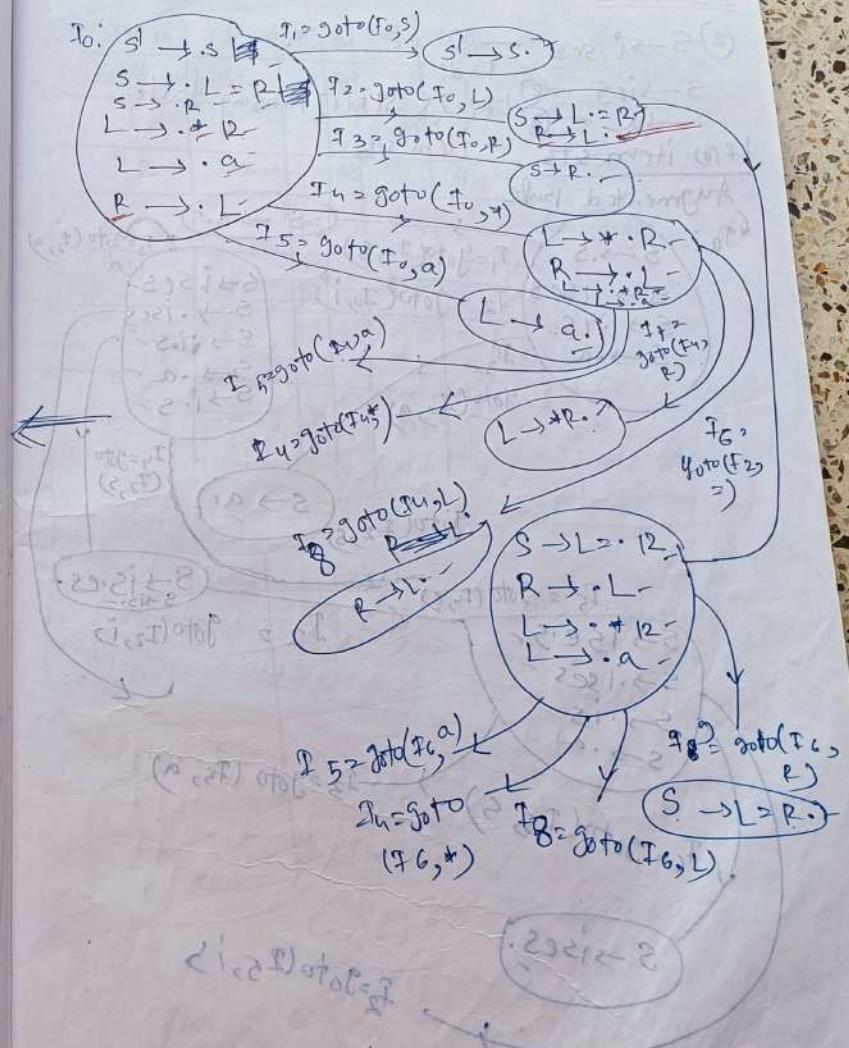
(9,2) \rightarrow S-R conflict is there.

01

$SP1.2\$\$$

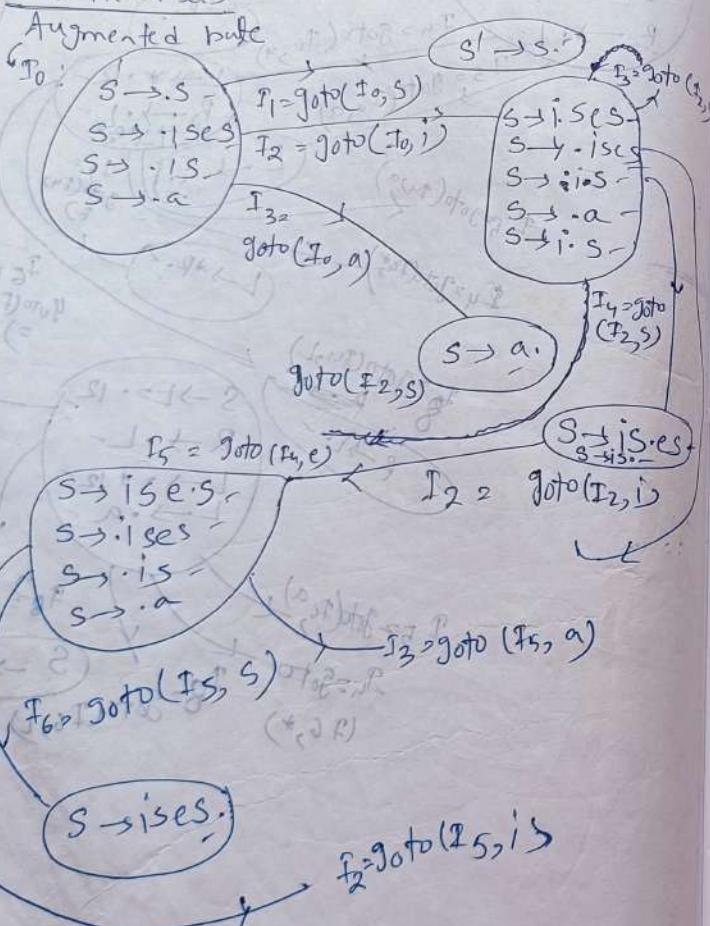
(9,1)

LR(0) item sets



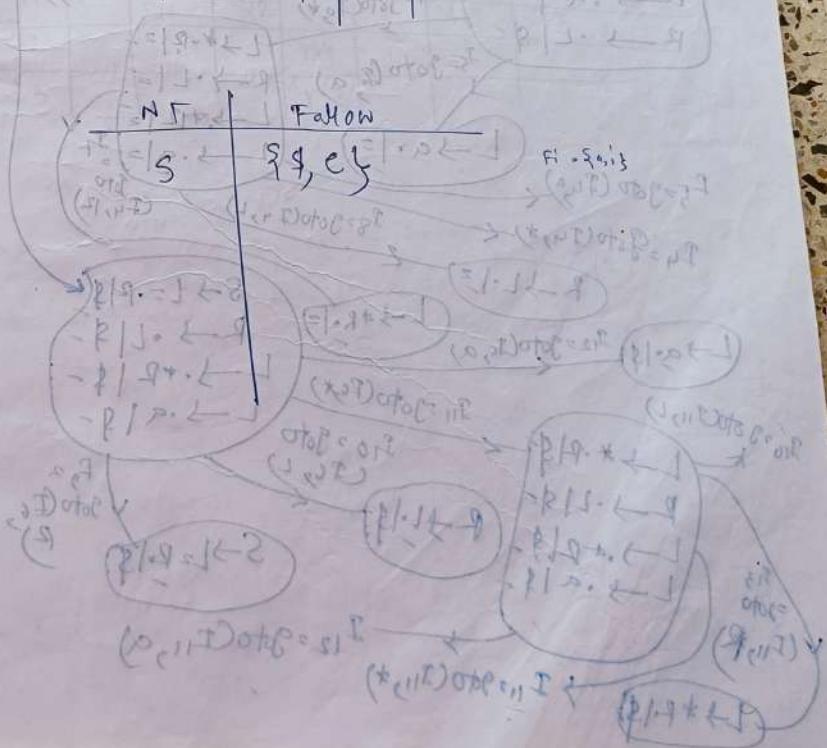
Assignment

(2) $S \rightarrow i \cdot S e s$ — ①
 $S \rightarrow i \cdot S e s$ — ② Find!
 $S \rightarrow i a$ — ③ LR(0) & SLR(1) Parsing table.
 L(0) item sets item sets



SLR(1) Parsing table

Item	i	e/a	\$	s
0	S_L	S_S		1
1			Accept	
2	S_2	S_3		4
3		R_3		
4		S_5	R_2	
5	S_2	S_3		6
6		R_1		



③ LR(0) item sets & SLL CLR(0) Parsing table

$$S \rightarrow L = R - \varnothing$$

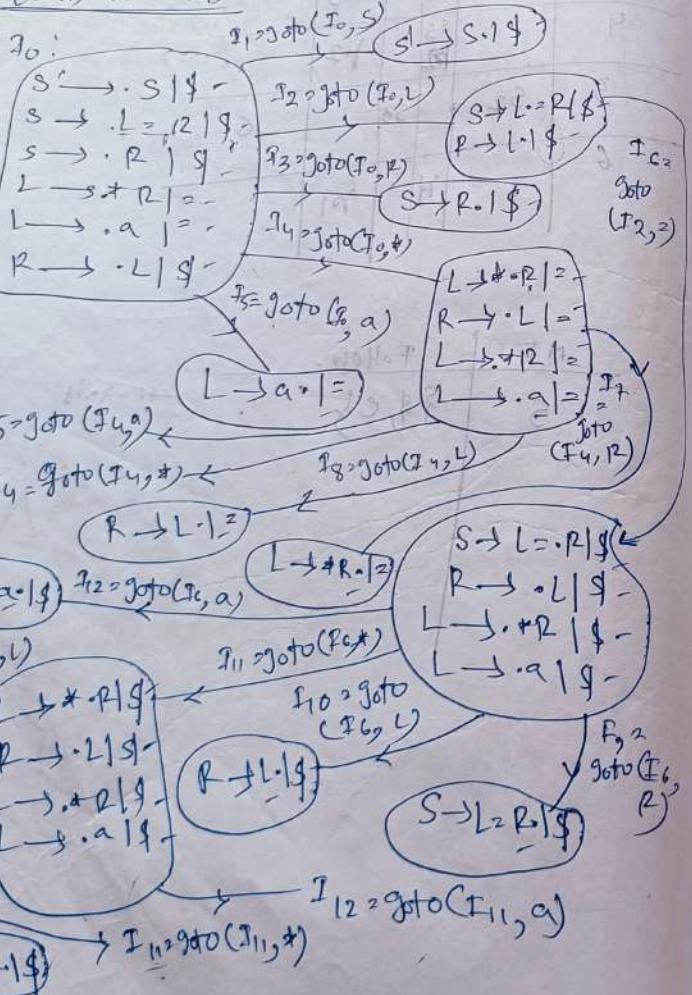
$$S \rightarrow R - \textcircled{1}$$

$$L \rightarrow *12 - \textcircled{2}$$

$$L \rightarrow a - \textcircled{3}$$

$$R \rightarrow L - \textcircled{4}$$

LR(0) item sets



CLR(0) parsing table

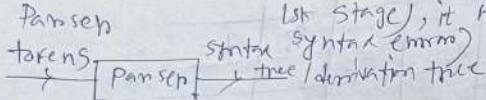
Symbol	*	a	\$	S	L	R
0	S4	S5			2	3
1				1		
2	S6			accept		
3				r5		
4		S4	S5			
5	r4				8	7
6		S11	S12			
7	r3				10	9
8	r5					
9				r1		
10				r2		
11		S11	S12		10	13
12				r4		
13				r3		

24/4/24

C/I

Semantic analysis

tin x; → syntax error.
Identifier (at 1st stage no errors)
int ion's x L.A. → generates meaning for
words (so no errors in
1st stage), it will be in



I am waiting → no syntax error
But it is semantic error
(error in meaning)

SDD & SDT

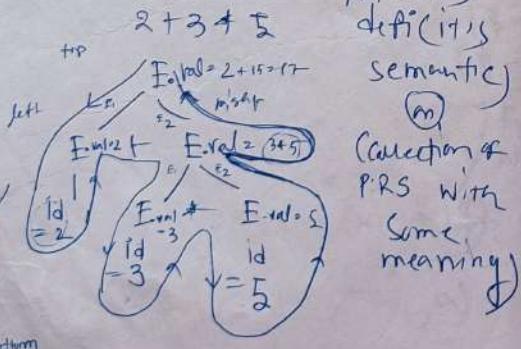
↓
Syntax directed
derivation ↓
Syntax directed
translation

SDD ↓
(CFG + definition/meaning) → (A CFG with attributes
(grammar) (meaning))
& rules

e.g.
 $E \rightarrow E_1 + E_2 \quad \{E_1.val = E_1.val + E_2.val\}$
 $E \rightarrow E_1 * E_2 \quad \{E.val = E_1.val * E_2.val\}$
 $E \rightarrow id \quad \{E.val = id.lexval\}$

If you don't give definition for everything, it's not SDD

give it otherwise
it's not SDD
formatting
following
Starting
from left
bottom & then
up



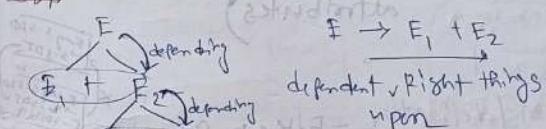
(Collection of PRS with some meaning)

- Attributes - Attributes are characteristics that determine the value of a grammatical symbol. Eg = type → value etc.

SDD → synthesized attribute

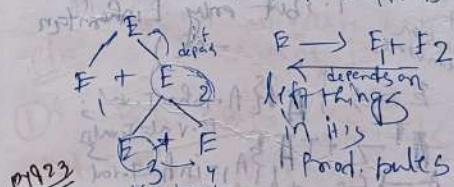
P23 Inherited attribute

Synthesized attribute - If any attribute is depending upon its parent or its children, then it is called synthesized attribute.



P23 Inherited attribute -

itself & its parents/siblings are inherited attribute.



① $A \rightarrow BDC \quad \{D.type = A.type\}$
inherited (B's type depends upon its left)

② $A \rightarrow BDC \quad \{A.type = D.type\}$
synthesized (A's type depends upon its right)

front a, b, c depends (inherited)

SDI = THIS
 Collection of SDD → BDC⁹ A. for Data type;
 2 types
 S-attributed SDT / definition
 + L-attributed SDT / definition

P.3
S-attributed SDT - only synthesised SDDs are written here / the concept where only synthesised SDDs are written then, it is S-attributed SDT. (It uses only synthesised attributes)

$$E \rightarrow E+E$$

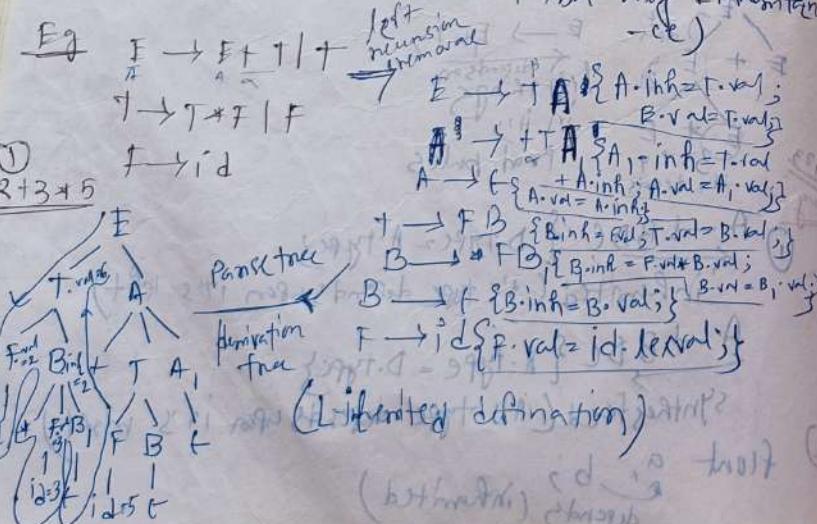
$$E \rightarrow E*E$$

$$E \rightarrow id$$

L-attributed SDT / definition - E like to S only

but only left-side inheritance. (All S-attributed SDTs are L-attributed SDT, but reverse isn't correct).

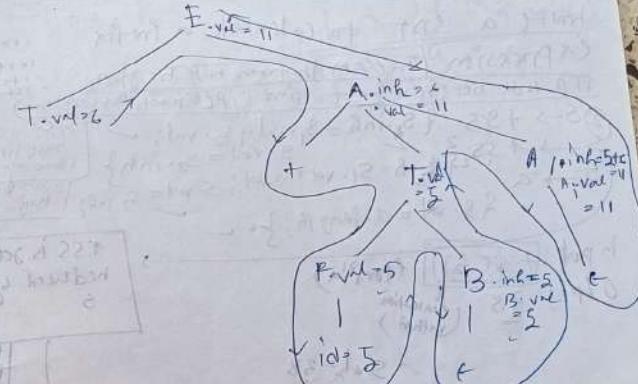
$A \rightarrow BDC^9$ D.type = C.type; } → not L-attributed SDT inherited but not left-side i. (It uses either son i. but only L-inheritance)



$$B_i = 34 \quad B.ihA = 3+2=6$$

$$B.val = 6$$

+ P.3
 + P.3
 + P.3



Shortcut

$$2+3*5$$

$$+ (2*3)*5$$

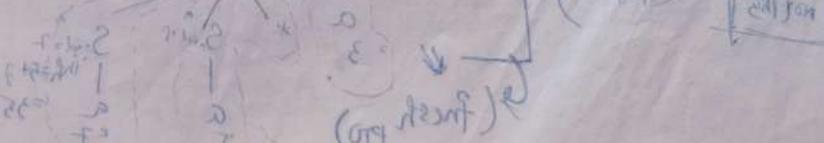
$$11 \text{ (Consistency check)}$$

Ans

All S-attributed SDTs are L-attributed, but the rev isn't correct - An L-attributed SDT can be given S. in. with the restriction that the inherited attributes can only inherit vals from L-siblings. But S-attributed can have only S-SDDs. So, the statement is true. 26/4/24

① $A \rightarrow BCD$ { B.i = A.i, D.i = C.i }
 not L/S-attributed \rightarrow (R-hand Inherited)

② $A \rightarrow BCD$ { B.i = A.i, A.type = C.type }
 L-attributed



Applications of SDT

① Arithmetic expression calculation: $2 + 3 * 5 = 17$

② Calculating result of Prefix expression = $+3*5$

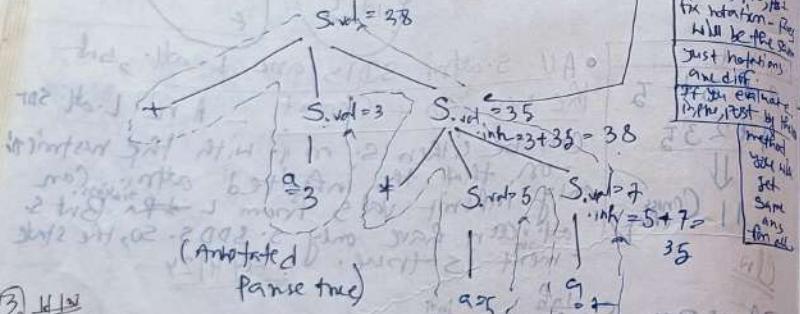
③ Write a SDT to calculate a Prefix

Expression grammar will be given, ITP will be given, just find o/p (Infix Parsing tree)

④ $S \rightarrow +SS \quad \{ S_inh = S_1.val + S_2.val; \}$
 $S \rightarrow *SS \quad \{ S_val = S_1.val * S_2.val; \}$
 $S \rightarrow a \quad \{ S_val = a.length; \}$

Input: $+3*5$ (Prefix)
O/P: 38 (Evaluation method)

*SS is acting
preceded by
S



⑤

To Postfix exp (do same)

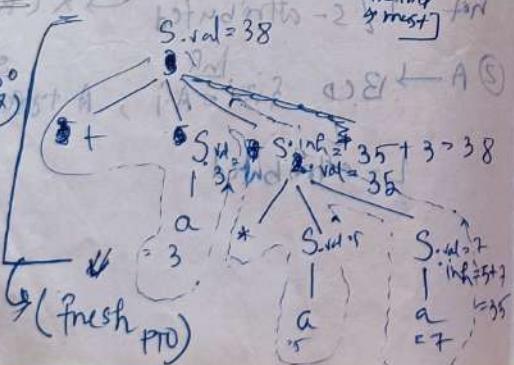
HP: $+3*5$

O/P: $[3 5] +$ (Postfix)

O/P: 38

evaluation method

not this



Type checking

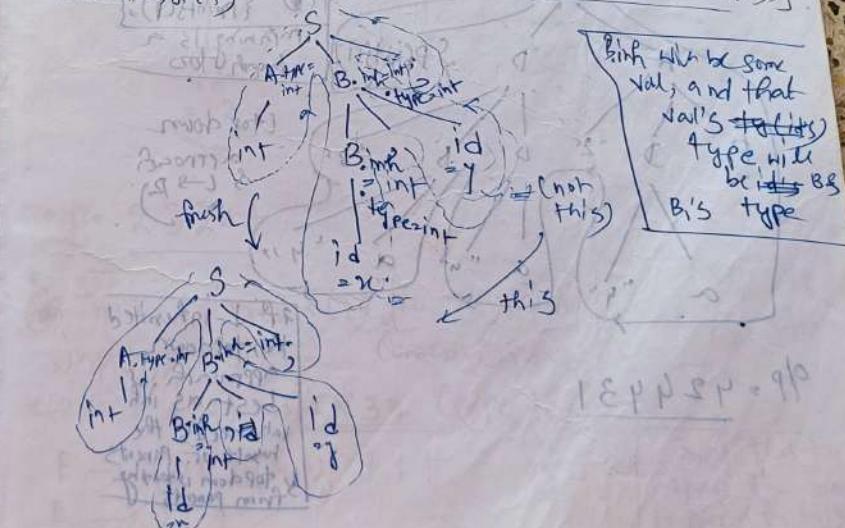
$S \rightarrow AB; B.inh = A.type ;$
A \rightarrow int. & A.type = int ;

A \rightarrow float & A.type = float ;

B $\rightarrow id. \& addType(B.inh, id.type);$

B $\rightarrow B_1.id \& B_1.inh = B.inh; addType(B.inh, id.type);$

(like int, id;)



EPPN 90

~~(3+5)*7~~
for postfix
exp calc.

$$i/p = 357+7$$

$$o/p = 38$$

SDT

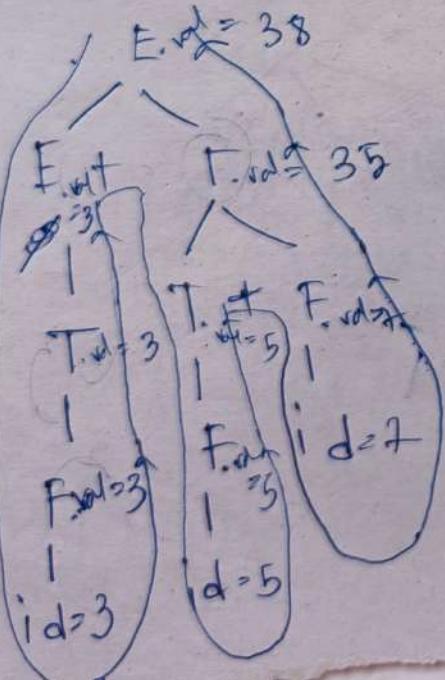
$$E \rightarrow E_1 + T \quad \{ E.\text{val} = E_1.\text{val} + T.\text{val}; \}$$

$$E \rightarrow T \quad \{ E.\text{val} = T.\text{val}; \}$$

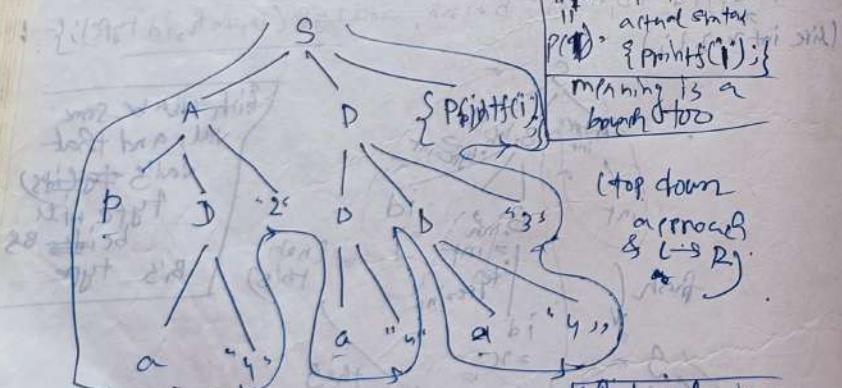
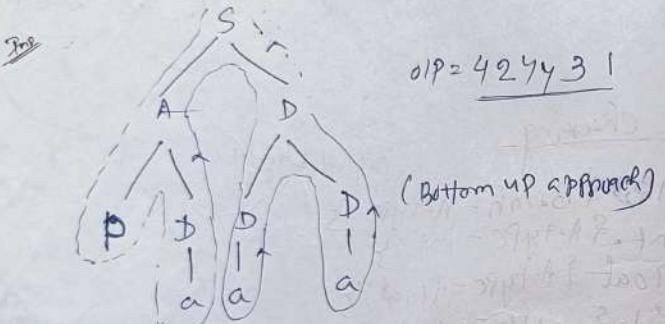
$$T \rightarrow T_1 * F \quad \{ T.\text{val} = T_1.\text{val} * F.\text{val}; \}$$

$$T \rightarrow F \quad \{ T.\text{val} = F.\text{val}; \}$$

$$F \rightarrow \text{id} \quad \{ F.\text{val} = \text{id}.lexval; \}$$



S → A B { print("1"); }
 A → P D { printf("2"); }
 D → D D { printf("34"); }
 D → C A { printf("4"); }
 IP = ~~para~~
 O/P = ? (What will be the output using this SDT)



If I intended the topdown approach is best as it needs the result of parents & topdown is starting from parents

S.h. → bottom up parsing (best)
 inh. → top down .. (best)

(2) Infix to Postfix conversion

Input: $2+3*5$

O/P: $(2+35)* \rightarrow 235**$ (expected)

E → E++ { printf("++"); }

E → + { printf("+"); }

T → T* F { printf("*"); }

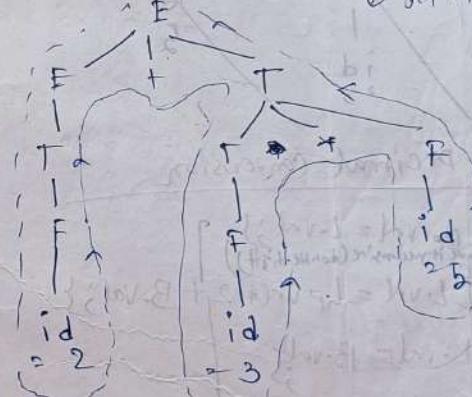
T → F { printf("F"); }

F → id { printf("id. lexical"); }

Bottom up approach

{ do nothing }

get it by parsing



O/P = 235**

(2) Infix to Prefix (\rightarrow) [do the same] conversion

Input: $2+(3*5)$

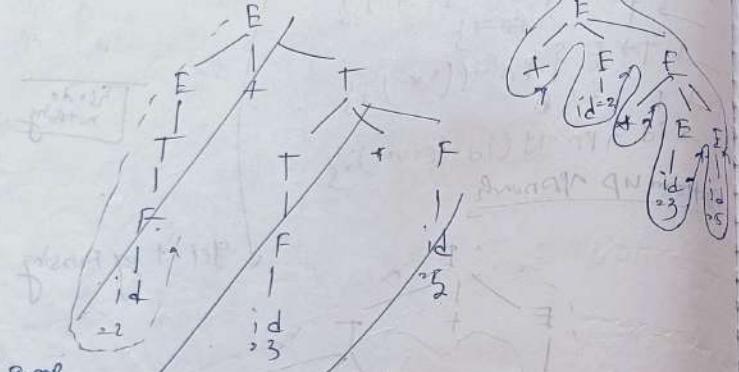
O/P: $2*(35) \rightarrow 2*35$ (expected)

E → { printf("E"); } E++

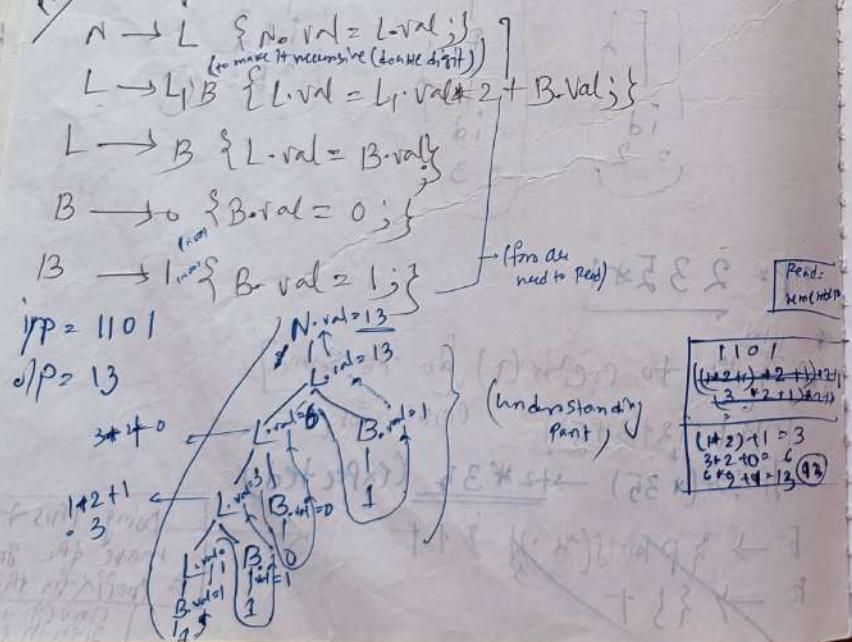
E → { printf("E"); } * T

Doing this to make the grammar a prefix for this conversion which will be given in the exam.

$T \rightarrow S$ $\text{printf}("i");$
 $T \rightarrow S; F$
 $F \rightarrow S$ $\text{printf}(* id, \text{level})$ } id
 Bottom-up approach



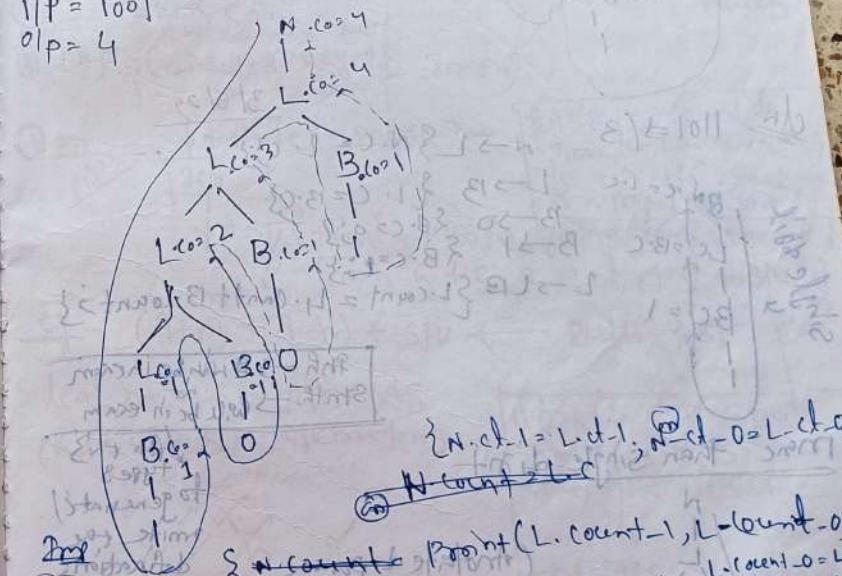
Binary → Decimal conversion



HM Tree Count binary digits
 is found the calc. the length of binary num (same g.)
 if no. of 1's & 0's calculate r (same g.)

① $N \rightarrow L \quad \{ N \cdot \text{count} = L \cdot \text{count}; \}$
 $L \rightarrow L_1 B \quad \{ L \cdot \text{count} = L_1 \cdot \text{count} + B \cdot \text{count}; \}$
 $L \rightarrow B \quad \{ L \cdot \text{count} = B \cdot \text{count}; \}$
 $B \rightarrow 0 \quad \{ B \cdot \text{count} = 1; \}$
 $B \rightarrow 1 \quad \{ B \cdot \text{count} = 1; \}$
 (even) (written)

$$\begin{aligned} dp &= 1001 \\ dp &= 4 \end{aligned}$$



② $N \rightarrow L \quad \{ N \cdot \text{ch-1} = L \cdot \text{ch-1}; \quad N \cdot \text{ch-0} = L \cdot \text{ch-0}; \}$
 if $dp = 1101$
 $L \rightarrow L_1 B \quad \{ L \cdot \text{ch-1} = L_1 \cdot \text{ch-1} + B \cdot \text{ch-1}; \quad L \cdot \text{ch-0} = L_1 \cdot \text{ch-0} + B \cdot \text{ch-0}; \}$
 $L \rightarrow B \quad \{ B \cdot \text{ch-1} = 0; \quad B \cdot \text{ch-0} = 1; \}$
 $B \rightarrow 0 \quad \{ B \cdot \text{ch-1} = 0; \quad B \cdot \text{ch-0} = 0; \}$
 $B \rightarrow 1 \quad \{ B \cdot \text{ch-1} = 1; \quad B \cdot \text{ch-0} = 0; \}$

L_{left} Σ B_{left}

For $in \rightarrow PBT \rightarrow$ Evaluation { for
Conversion } Both
grammars same
def' diff.

as both parsing is
bottom up parsing

For $in \rightarrow PNC \rightarrow$ Evaluation { for both grammars
Conversion } & def. both are
diff. as evaluation is,
bottom up parsing & conversion is top down parsing

$ct - 0 = L - ct$

$L - count$

$L - count - 0$

$L - count + B - co$

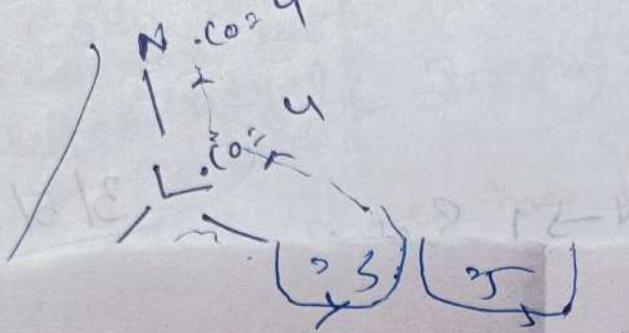
$L - count - 0$

$nt - 0 = 1$

$nt - 0 = 0^2$

$$\frac{I}{O}P = 1001$$

$$O/I P = 4$$



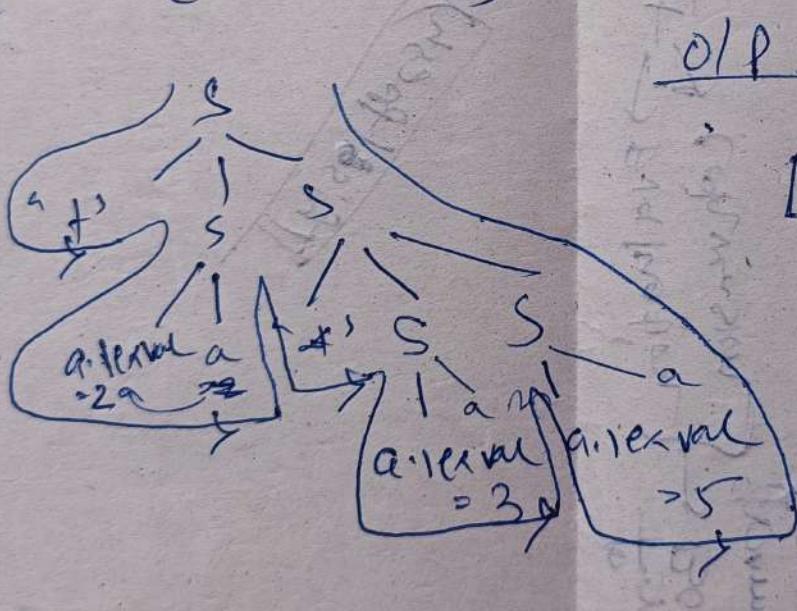
$$S \rightarrow \{ \text{Profit}(+) \}, S_1, S_2$$

$$S \rightarrow \{ \text{Profit}(*) \}, S_1, S_2$$

$$S \rightarrow \{ \text{Profit}(\alpha\text{-level}) \}, a \quad \underline{O/I P = 2+3+5}$$

~~Bottom up~~

Show



$$\underline{O/I P = 2+3+5}$$

$$\begin{bmatrix} '+' \rightarrow P(+) \\ '*' \rightarrow P(*) \end{bmatrix}$$

Read = number

0 1
1 2 3 4 5 6 7 8 9
+ + + + + + + + + +

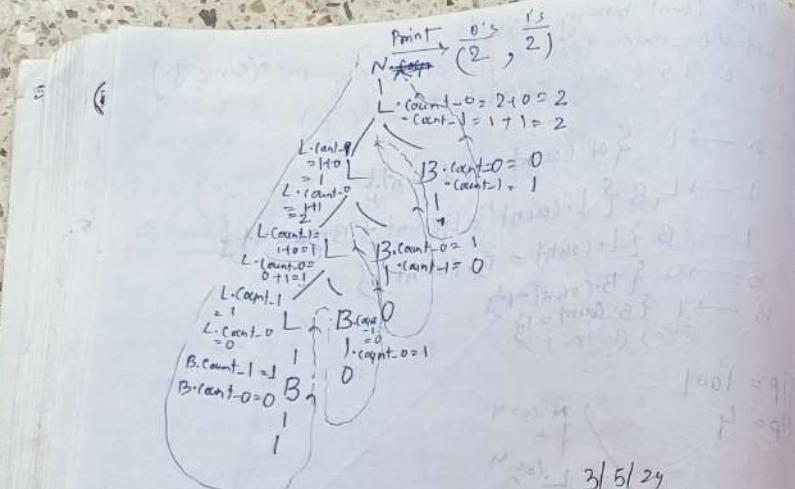
(1)

$$L \rightarrow 0 \quad 0$$

$$B \rightarrow 0 \quad 0$$

$$B \rightarrow 1 \quad \{ B.\text{Count} - 1 \}$$

1 1

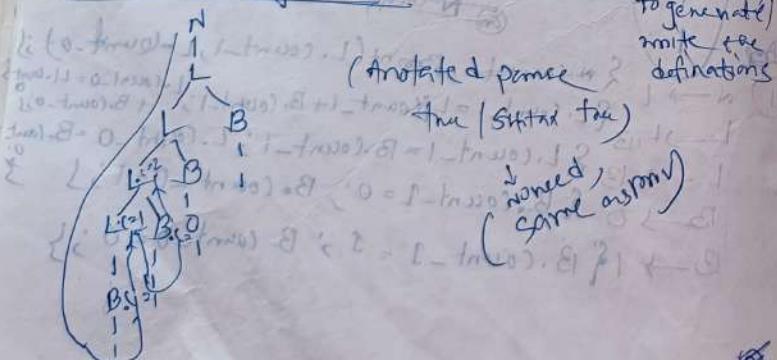


DN

1101 ÷ 3
 $n \rightarrow L \quad \{ N.C = L.C \}$
 $B \quad \{ C = B.C \}$
 $L \rightarrow B \quad \{ L.C = B.C \}$
 $B \rightarrow B \quad \{ B.C = B.C \}$
 $2 \rightarrow LB \quad \{ L.count = 4 \cdot count + B.count \}$

single digit

more than single digit



(4) Intermediate Code generation - m/c independent Code

(2) Code generators - m/c dependent

(6) Code optimization

(4) Intermediate co. g.

The code is of assembly type.

Way to write the code:

Will be generated from
Statement + Syntax rules

(1) AST (Abstract Syntax tree)

(2) 3-address Code

(3) DAG (Directed Acyclic Graph)

AST

M123

Annotated Parse tree

TCG

AST is a data structure used in c.g. to rep. the structure of a prog. / code snippet.

AST (this is also a syntax tree, generated from G + S. R. but it's abstract)

DAG

eq -

$(9+8) + (9-8) + 5/0 \leftarrow \text{Build AST}$

leaf nodes = operands
child nodes = operators

for t/t/s
types to generate

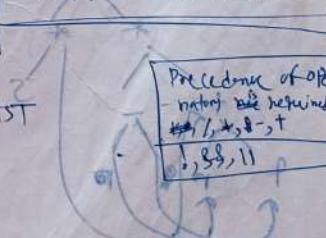
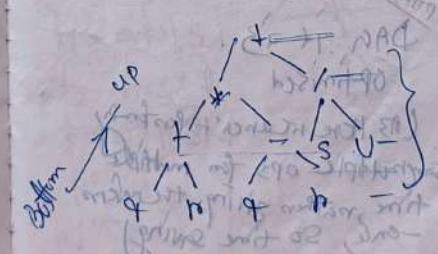
write tree definitions

AST is a tree rep.

of a Prog.'s Source

Code that helps the

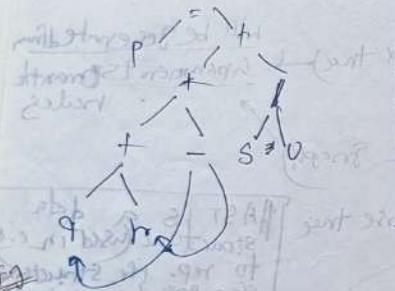
Compiler to understand its structure.



② DAG

Common operations will not be performed multiple times, in this place we'll calculate the references / give the references (like the direction)

$$E.g. P = (q+r) + (q-n) + s/u$$

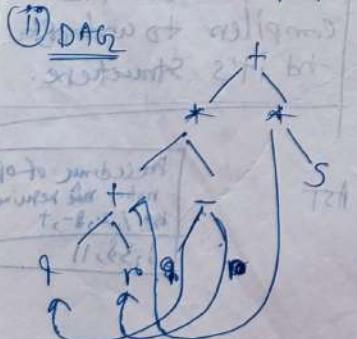


$$① (q+r) * (q-n) + (q+n) * S \rightarrow DAG_1$$

② AST

AST

It is not optimized.
(As here we are performing same ops for multiple times, so time consuming)



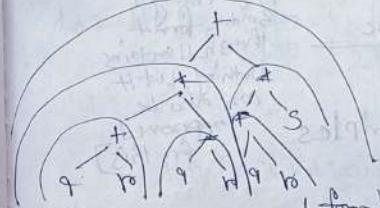
DAG, it is
Optimized

(As here we can't perform same multiple ops for multiple times, rather giving the reference, so time saving)

DAG is a data structure used in C, C++ to visualize the flow of values b/w basic blocks. Rep. the structure of basic blocks & provide optimization techniques.

3-address code

operators (3 instruction)



$$t_1 = q + r;$$

$$t_2 = q - n;$$

$$t_3 = t_1 + t_2;$$

$$t_4 = q + n;$$

$$t_5 = t_4 * s;$$

$$t_6 = t_3 + t_5;$$

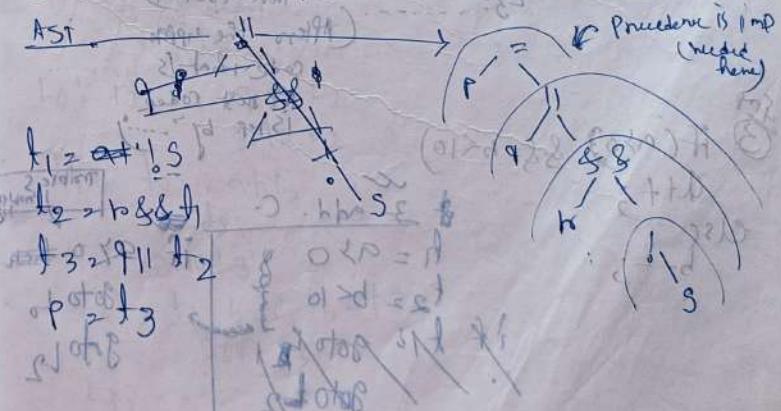
$$E.g. ① if (a > 0) \{ smp \} a++;$$

else

b--;

② $P = q || r \&& (!s)$ write 3-address code for it.

AST



at least 2 things will be there

① 2 address will be

② in 1 exp there

will not be more than 1

operations

(A + LHS there will be an oper. dependent operand & RHS there will be 1 or 2 or 3 operands & etc.)

forming from AST, so it's not optimistic, if it was DAG then we can use it again as reference.

It is a type of intermediate code that makes use of at most 3 add. & r operators to rep. an expression & the value computed at each instruction is stored in temp. variable generated by compiler.

Write 3-address code for it.

• $t_1 = a+t_3 \rightarrow$ this is also a 3 address
 $a+i?$ is a single operator. [if $t_1 + t_3$ this
 is generally a
 2 add. format
 though it contains
 nothing, but it
 can also be
 approached
 for this]

Intermediate prep. of code

- ~~Quadruples~~
- triples
- Indeed indirect triples

(1) if ($a > 0$) } 3-address code

else } $t_1 = a > 0$
 $b - ;$ if t_1 , goto L_1

 { (not true) goto L_2
 $t_1 = a > 0$ (true)
 $t_2 = b - 1;$ $t_2 = a + t_3;$
 $t_3 = b - 1;$ goto L_3 (1)

$L_2: t_3 = b - 1;$
 $b = t_3;$

goto L_3 → (if not written
then also correct)

$L_3:$...
(After the upper
code what is
the next code
is rep. by "...")

(2) if ($a > 0$) & $b < 10$)

$a++;$

else

$b - ;$

if $t_1 > 0$ goto L_1
else goto L_2

3 add. C.

$t_1 = a > 0$

$t_2 = b < 10$

$t_3 = t_1 & t_2$

if $t_3 > 0$ goto L_1

else goto L_2

if t_1 , goto L_0 goto L_2	$L_0: if b < 0$, goto L_1 goto L_2
$L_1: t_2 = a + t_1;$ $a, t_2;$ goto L_3	$L_1: t_2 = a + t_1;$ $a = t_1 - ;$ goto L_3
$L_2: t_3 = b - 1;$ $t_3 = t_1 & t_2$ goto L_3 (1) $L_3: ---$	$L_2: t_3 = b - 1;$ $b = t_2$ goto L_3
$L_3: ---$	$L_3: ---$

else
 $t_1 = a > 0$ goto L_1
 $t_2 = b < 10$ goto L_2
 $t_3 = t_1 & t_2$ goto L_3
if t_1 , goto L_1
goto L_0
if t_2 , goto L_2
if t_3 , goto L_3
 $L_0: if t_2$, goto L_1
goto L_2
 $L_1: t_2 = a + t_1;$
 $a, t_2;$
goto L_3
 $L_2: t_3 = b - 1;$
 $b = t_2$
goto L_3
 $L_3: ---$

⑤ while($n > 0$)

$$\{ \quad h = n \& 10;$$

 n = n / 10;

 if t_1 goto L_1 ,

 goto L_2

3-address code

$$l_0: t_0 = n & 10$$

 if t_1 goto L_1 ,

 goto L_2

$$l_1: t_2 = n / 10$$

 n = t_2 ;

$$t_3 = h \& 10 \& n / 10;$$

 goto L_0

$L_2: \dots$

Intermediate rep. of code used during various stages of compilation process

① quadriples - rep. of statements in a prog. with 4 fields, typically consisting of an operator & 3 operands.

② Triples - similar to quadruples, but with 3 fields, typically consisting of an operator & 2 operands.

③ Indirect triples - Triples that involve indirect addressing or references, usually for complex exp's or memory operations.

$t_0 = st + 1 \& 10$

$t_1 = st$

$t_2 = t_1 : 10$

$t_3 = t_2$

$t_4 = t_3$

$t_5 = t_4$

$t_6 = t_5$

$t_7 = t_6$

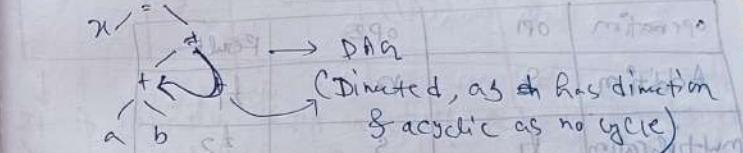
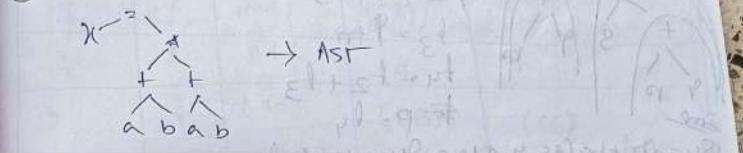
$t_8 = t_7$

$t_9 = t_8$

$t_{10} = t_9$

10/5/24
• DAG is also a kind of AST (but with no same task perform)

⑦ $x = (a+b) * (c+d)$

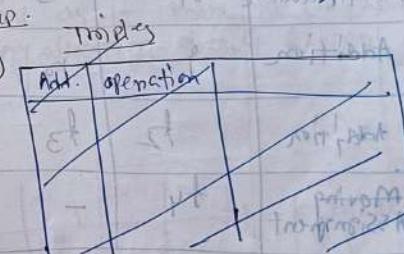


3-address triples rep:

⑥ If ($a > 0$) {
 $t_1 =$

 else {
 $t_2 =$

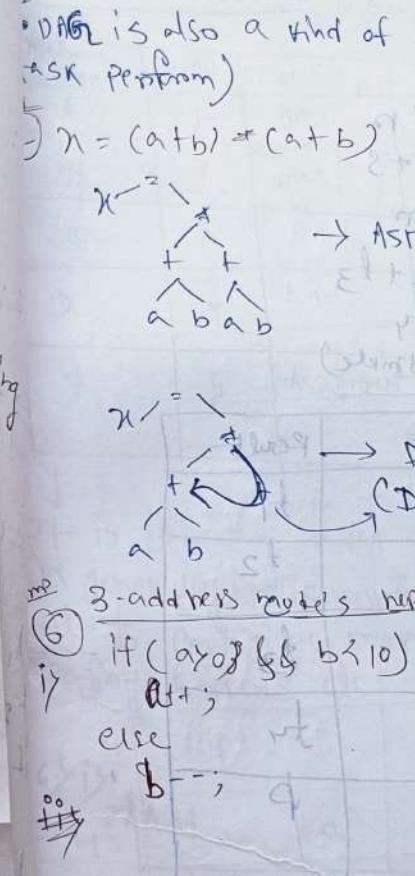
 }
}



operation	op1	op2	result
Boolean (greater than)	$a = t_0 \& 10$	$t_0 / 10$	$t_1 = t_0$
Boolean (less than)	$b = t_0 / 10$	t_0	$t_2 = t_0$
Boolean (greater than)	$a = t_0 / 10$	t_0	$t_0 = t_0$
Boolean -	$-$	$-$	$goto L_2$
Boolean (less than)	$b = t_0 / 10$	t_0	$goto L_1$
-	$-$	$-$	$goto L_2$

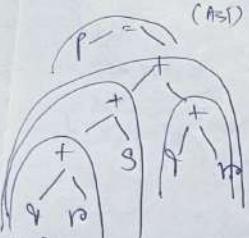
operation	OP1	OP2	result
greater than	a	o	t ₁
less than	b	lo	t ₂
Boolean t ₁	-	-	goto L ₀
-	-	-	goto L ₂
L ₀ Boolean t ₂	-	-	goto L ₁
-	-	-	goto L ₂
L ₁ Add a	a	l	t ₂
Assignment t ₂	-	-	a
-	-	-	goto L ₃
L ₂ Sub b	b	l	t ₃
Assignment t ₃	-	-	b
-	-	-	goto L ₃
L ₃ ---	---	---	---

↳ quadruples by 1st way



operation	OP1	OP2
Boolean (greater than)	a	hhhh
Boolean (less than)	b	ssss
Boolean (greater than)	a	ssss
Boolean	-	-
Boolean (less than)	b	ssss
-	-	-

$$6. (1) p = (q+r)*st (q+rn)$$



$$t_1 = q + r$$

$$t_2 = t_1 + s$$

$$t_3 = q \cdot r$$

$$t_4 = t_2 + t_3$$

$$p = t_4$$

Quadruples (as 4 cols so Quadruples)

	operation	OP1	OP2	Result
1	Addition	q	r	t ₁
2	multiplication	t ₁	s	t ₂
3	Addition	t ₁	r	t ₃
4	Addition	t ₂	t ₃	t ₄
5	Moving Assignment	t ₄	-	p

Triples (3 cols, so triple)

	operation	OP1	OP2 / Result	Address
1	Addition	q	r	1 (As we can't get space to write here, so moving to next row)
2	mul	(1)	s	2 (As we can't get space to write here, so moving to next row)
3	Add	2 q	r	30 (As we can't get space to write here, so moving to next row)
4	Add	(2)	(3)	4 (As we can't get space to write here, so moving to next row)
5	Assignment	t ₄)	P(result)	5

Indirect triples (As we use address of address,
so indirect (like indirect addressing mode))

Address of Address	Address	operation	OP1	OP2 / result
A	1	Add	q	r
B	2	mul	((A))	s
C	3	Add	q	r
D	4	Add	((B))	((C))
E	5	Assignment	((D))	p

It is logically most useful. But it is not used in generally. It is useful as it know in computer always contiguous memory allocation doesn't happen, Paging happens etc.

L1	⇒ Add	a	1	multib	t ₁	(moved)
	Assignment	t ₁	-	a		
	-	(1)	-	-	30 to L ₃	
L2	⇒ Subtraction	b	1		t ₃	0 (try)
	Assignment	t ₃	(0)	-	a	Others may
	-	(1)	-	-	30 to L ₃	S (the 1st way)
L3	EI

Triples

Address	Operation	Op1	Result / Op2
120	Boolean greater than	a	0
2	Boolean	(1)	goto L0 goto L2
1	greater than	a	0
2	less than	b	10
3	Boolean	(1)	goto (5)
4	-	-	goto (70)
5	Boolean	(2)	goto (7)
6	-	-	goto (70)
7	Addition	a	0
8	Assignment	(7)	a transferred
9	-	-	goto(13)
10	Sub	b	0
11	Assignment	(70)	b transferred
12	-	-	goto(13)
13	EOP	-	-

(7) $f(n, i=0, j=n, i+1)$
 $a = at[5];$

3-add. Code

$i_2 = i < n \}$ assignment

if i_1 , then goto L0
goto L1

L0: $t_2 = at[5]$

$a = t_2$

$t_3 = i + 1$

$i = t_3$

goto L2

L1: -----

(8) do

{

$n = n / 10;$

$n = n / 10;$

} while($n > 0$)

3-add. Code

$l_0: t_1 = n / 10;$

$n = t_1$

$t_2 = n / 10 \}$

$n = t_2$

$t_3 = n > 0$

if t_3 , then goto L0

goto L1

⑨ while ($n > 0$)

{
 $n = n / 10;$
 $t_1 = n$;

3-add. code

$t_1 = n > 0$

if t_1 goto L_0
goto L_1

$L_0: t_2 = n / 10;$

$n = t_2;$

goto L_2

⑩ switch (ch):

{ Case 1: $a = a + b;$
 break;

Case 2: $a = a + b;$
 break;

} Case 3.

if $ch == 1$ goto L_1
if $ch == 2$ goto L_2
 if $ch == 3$ goto L_3

$L_1: t_1 = a + b$
 $a = t_1$ } (As targeting first so this format
otherwise it is already in
3-add. format)

$L_2: t_2 = a + b$

$a = t_2$

goto L_3

$L_3: \dots \dots \dots$

If no 'break';
then return
and goto L_2
instead of goto
or, if no
need to write it as it
will automatically go to
with automatically go to

2nd way

if $ch == 1$ goto L_1
 goto L_2 (if not L_1 or L_2 then
 automatically in L_4)

$L_2: if ch == 2$ goto L_3
 goto L_4

$L_1: t_1 = a + b$
 $a = t_1$
 goto L_4

$L_3: t_2 = a + b$
 $a = t_2$
 goto L_4

$L_4: \dots \dots$
HW Assignment 2

⑪ while ($n > 0$)

{ if ($a > 5 \& b < 11 \& c == 10$)
 $t_2 = p + f$; $t_3 = q + f$
else
 $t_2 = q + f$;
 $t_3 = p + f$;

$t_2 = a + b$ goto
 $t_3 = b + c$ goto

$L_0: t_1 = n > 0$
if t_1 goto L_1
 goto L_1'

$L_1: if t_2$ goto L_2
 goto L_4

$L_2: if t_3$ goto L_3
 goto L_4

$L_3: t_5 = p + 1$
 $p = t_5$
 goto L_6

$L_4: if t_6$ goto L_3
 $t_4 = p$
 goto L_5

$L_5: t_6 = q + 1$
 $q = t_6$
 goto L_6

$$L_G: t_7 = n - 1$$

$$n = t_7$$

Goto L_0

$L' \Rightarrow \dots$

Quadruples

operation	op1	op2	Result
Greater than	a	t ₅	t ₂
Less than	b	t ₆	t ₃
Equal to	c	t ₁₀	t ₄
Greater than	n	t ₁₀	t ₁
Boolean	t ₁	-	goto L ₁
-	-	-	goto L'
Boolean	t ₂	-	goto L ₂
-	-	-	goto L ₄
Boolean	t ₃	-	goto L ₃
-	-	-	goto L ₄
Add	p	t ₁	t ₅
Assignment	t ₅	-	p
-	-	-	goto L ₆
Boolean	t ₄	-	goto L ₃
-	-	-	goto L ₅
Add	t ₆	t ₁	t ₆
Assignment	-	-	goto L ₆

$L_6 \Rightarrow$	Assignment	t ₆	-	q
-	-	-	-	goto L ₆
sub	n	1	t ₇	t ₇
Assignment	t ₇	-	-	n
-	-	-	-	goto L ₀
$L' \Rightarrow$	-	-	-	-

Address	operation	op1	op2 / Result
1	Greater than	a	t ₅
2	Less than	b	t ₆
3	Equal to	t ₁₀	t ₁₀
4	Greater than	n	t ₁₀
5	Boolean	t ₁ (4)	goto L ₇
6	-	-	goto (2)
7	Boolean	t ₂ (1)	goto (9)
8	-	-	goto (14)
9	Boolean	t ₃ (2)	goto (11)
10	-	-	goto (14)
11	Add	p	t ₅
12	Assignment	t ₅ (11)	p
13	-	-	goto (19)
14	Boolean	t ₄ (3)	goto (11)
15	-	-	goto (16)
16	Add	q	t ₆
17	Assignment	t ₆ (16)	q
18	-	-	goto (14)
19	Sub	n	1
20	Assignment	t ₇ (19)	n
21	-	-	goto (14)
22	$L' \Rightarrow$	-	-

13/5/29

(1) $i = 0; i < n; i++$

if ($x \neq 0 \wedge j < 10$)
 else
 $j++$

3-add. Code

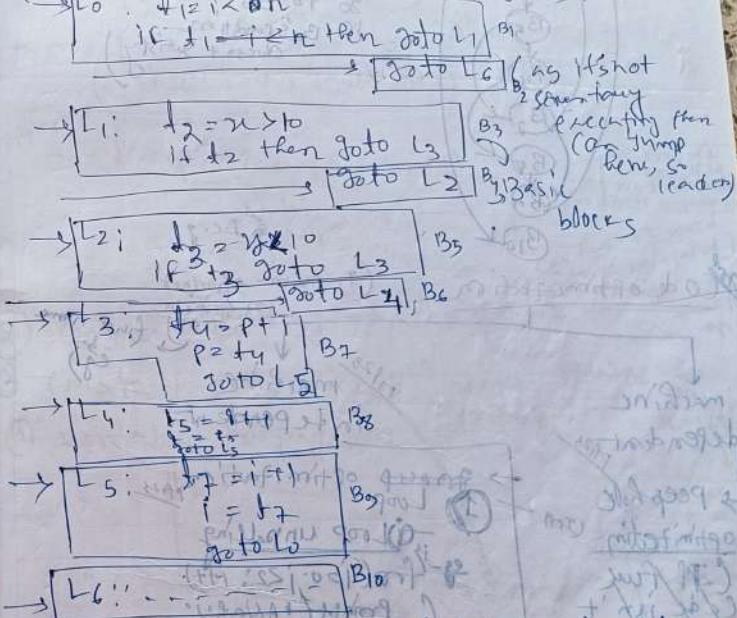
$t_1 = f_0 = 0$	180	read x	270	return
$L_0: t_1 = i < n$	d	read x	c	return
if t_1 then goto L_1	3	if $x \neq 0$	8	
goto L_6	s	read x	12	
$L_1: t_2 = x \neq 0$	180	read x	12	
if t_2 goto L_3	1	read x	12	
goto L_6	1	read x	12	
$L_2: t_3 = j < 10$	180	read x	12	
if t_3 goto L_3	1	read x	12	
goto L_4	1	read x	12	
$L_3: t_4 = p + 1$	180	read x	12	
$p = t_4$ goto L_5	1	read x	12	
goto L_6	1	read x	12	
$L_4: t_5 = q + 1$	180	read x	12	
$q = t_5$ p	1	read x	12	
goto L_5	1	read x	12	
$L_5: t_6 = f + 1$	180	read x	12	
$i = t_6$	1	read x	12	
goto L_6	1	read x	12	
$L_6: \dots$	180	read x	12	

Control flow graph (CFG) - A graphical rep. of all paths that a program may take during execution.

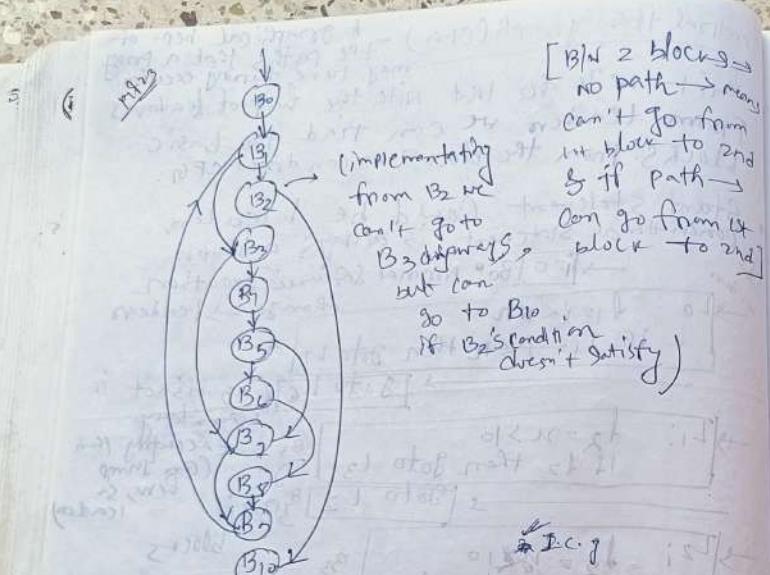
With we'll do that with the help of leaders. From leaders we can find the basic blocks. From the blocks we can draw CFG.

- Start statement Could be 1 leaders.
- Conditional Statement is always a leader.

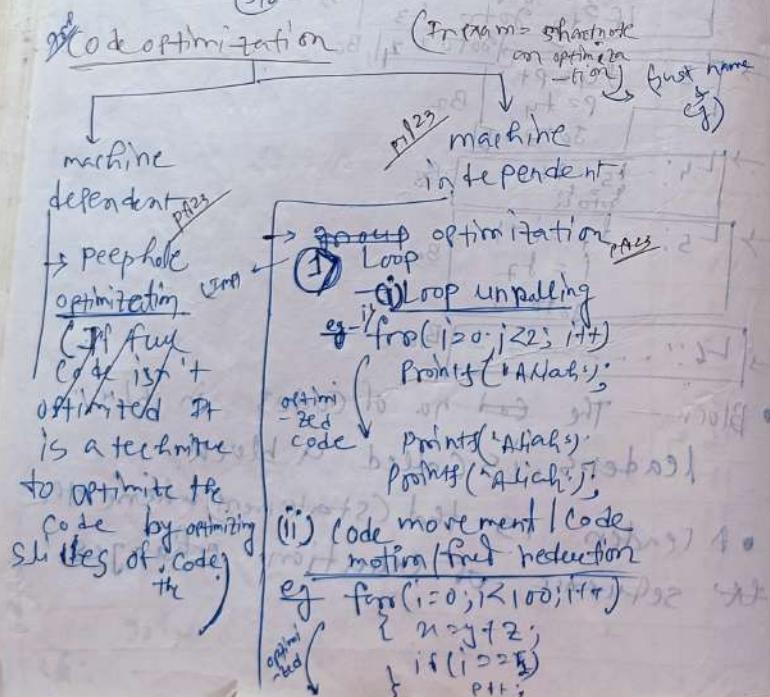
$\rightarrow L_0: f_0 = i < n$ Normal sequence of execution
 change \Rightarrow leaders



- Block — The no. of codes in block
- Leaders is called a block.
- A leader is that (statement) where the sequence of execution changes.



Code optimization



[B/N 2 blocks → NO path → many]

can't go from 1st block to 2nd & if path →

can go from 4th block to 2nd

→ $x = j + 2^i \rightarrow$ (it is used for reduction as, if it will be in the code, it's freq(the no. of times) is creating a doo, but here freq = 1)

Loop jamming / fusion

eg - `for(i=0; i<100; i++)
 n++;`

optimized
→ `for(j=0; j<100; j++)
 y++;`

→ `for(l=0; l<100; l++)
 z++;`

we know 100 values, if the max is well known limit it, otherwise

use n^2 need to be calculated

as freq n^2 need to be calculated

② constant folding → $n = \frac{22}{7} \pi r^2 \rightarrow n = 3.14 \times r^2$

③ constant propagation → $n = 12; \rightarrow n = 12; \rightarrow n = 16;$

④ variable propagation → $n = 12; \rightarrow n = 12; \rightarrow n = 16;$

where we know it will not be changed later, it's fixed

⑤ common subexpression elimination (like variable propagation)

eg → $a = n * y \rightarrow$ value don't change, if we unchanged not this

$c = a + d \rightarrow$ variable propagation

$c = a + d \rightarrow$ value don't change, if we unchanged not this

$c = a + d \rightarrow$ variable propagation

$c = a + d \rightarrow$ value don't change, if we unchanged not this

① Peephole optimization

i) Strength reduction

$$n^2 \geq n + n = 2n$$

$n + \lceil \frac{n}{2} \rceil \times 1 = n + n$ [as ' $\lceil \cdot \rceil$ ' is comparatively easy to compile]

ii) unreachable / dead code elimination

e.g. switch (ch)

dead code
case never execute

Profit ("Alike") X → optimiz.

Case 1: a = ab

bneq,

Case 2: a = aab

bneq,

iii) Replace slower instruction with faster one

ADD R2, #1 → slow ins.

optimized → INR R2 → fast

iv) Redundant load store removal

② Parallel execution of operation

a) Pipelining Optimization

b) Distributed computation

c) Cache optimization

p/q 23

Q-A

Q-B
Determine the strings generated by this regular expression: $a(bc)^*d$

ad

abcd

Q-B

Q-C
calculate LR(1) item sets of the grammar given below -

S → aAd | bBd | aBcBac

A → q

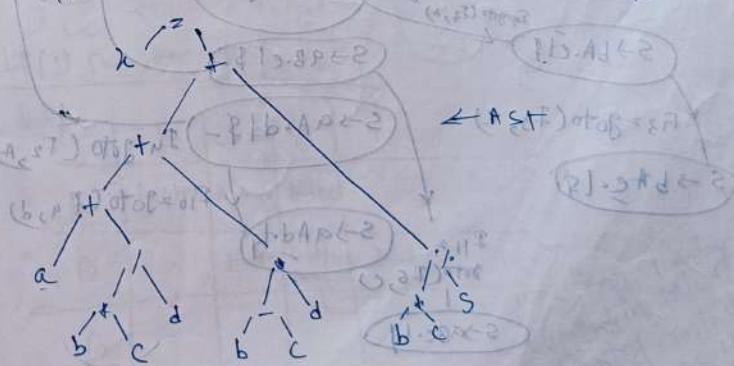
B → q

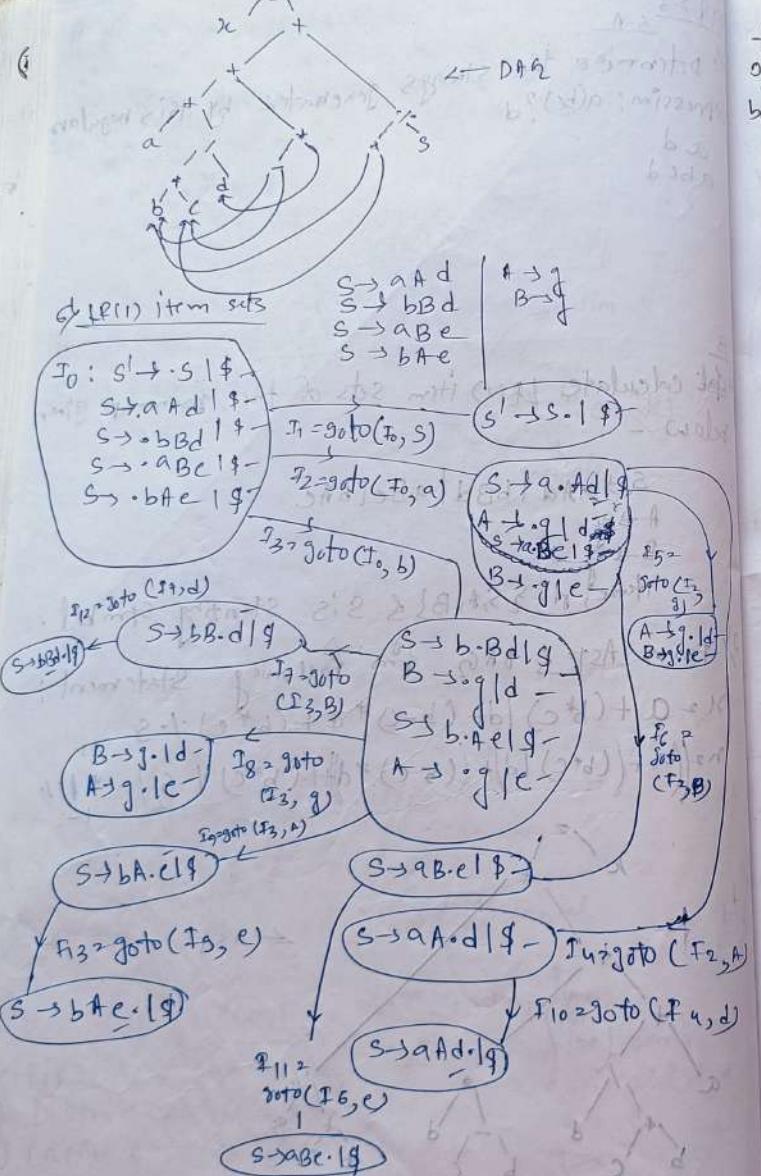
Here, N = {S, A, B} & S is starting symbol.

Q-D
From AST & DAG for following statement:

$$x = a + (b+c) | df (b-c)^* d + (b+c) \cdot s$$

$$n = ((a + (b+c)) | df ((b-c)^* d) + ((b+c) \cdot s)) ^*$$





→

Q) Conclude whether the following grammar could be considered as LL(1) parser or not.

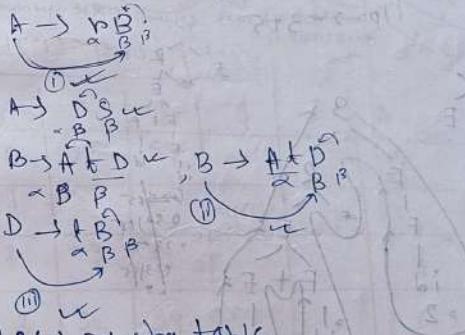
$$A \rightarrow aB \mid DS$$

$$B \rightarrow AtD$$

$$D \rightarrow tBlE$$

Here $N = \{A, B, D\}$, S . A is start symbol

NT	fi	fo
A	{ a, t, S }	{ $\$, t, S$ }
D	{ t }	{ $\$, \S }
B	{ a, t, S }	{ $\$, \S }



LL(1) Parsing table

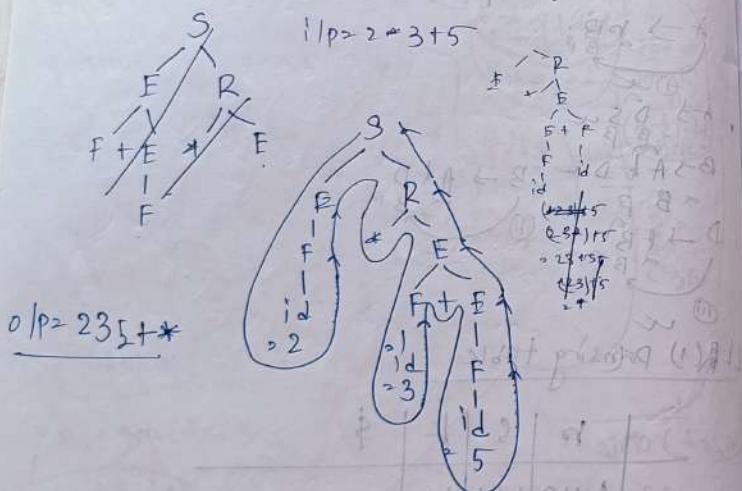
	r	s / t	\$
A	$A \rightarrow aB$	$A \rightarrow D$	$A \rightarrow DS$
D B		$D \rightarrow t$	$D \rightarrow tB$
B		$D \rightarrow t$	$D \rightarrow t$

more than 1 entry
elements in this
entry. So it can't
be LL(1) parser.

Q. (i) Consider the syntax directed definitions given below. Using the SDDs, what will be the o/p printed by a bottom up parser for I/P : $2 * 3 + 5$?

$$\begin{aligned}
 S &\rightarrow ER \{ \} \\
 R &\rightarrow * E \{ \text{printf}(“*”) \} \\
 R &\rightarrow E \{ \} \\
 E &\rightarrow P + B \{ \text{printf}(“+”) \} \\
 E &\rightarrow F \{ \} \\
 F &\rightarrow (S) \{ \} \\
 F &\rightarrow \text{id} \{ \text{printf}(“ “, id, level) \} \\
 \end{aligned}$$

Here N = {S, E, F, P, +, (,), id, level}.



I/P: $2 * 3 + 5$

Bottom up parser
using SDDs

Q. (ii) Find all LR(0) item sets for the following grammar:

$$\begin{aligned}
 S &\rightarrow AA \cdot - \text{(1)} \\
 A &\rightarrow AA \cdot - \text{(2)} \\
 A &\rightarrow b \cdot - \text{(3)} \\
 \end{aligned}$$

Here N = {S, A, P} & S is start symbol.

LR(0) item sets

$$\begin{aligned}
 I_0: S \cdot \rightarrow \cdot S & \xrightarrow{f_1 = \text{goto}(I_0, S)} S \cdot \rightarrow S \cdot \\
 S \cdot \rightarrow AA \cdot & \xrightarrow{f_2 = \text{goto}(I_0, A)} \\
 A \rightarrow AA \cdot & \xrightarrow{f_3 = \text{goto}(I_0, A)} \\
 A \rightarrow b \cdot & \xrightarrow{f_4 = \text{goto}(I_0, b)} \\
 \end{aligned}$$

b) Construct SLR(1) Parsing table

Items	a	b	\$	S	A
0	S3	S4		1	2
1			accept		
2	S3	S4			5
3	S3	S4			6
✓ 4	r3	r3	b3		
✓ 5			r1		
✓ 6	n2	n2	r2		

If S is start symbol.

(i) Explain full parsing mechanism for an i/p : abab. Is it accepted or not?

Stack(state)	Stack	i/p	Actions
\$0	\$	abab\$	shift 3
\$03	\$a	bab\$	shift 4
\$034	\$ab	ab\$	reduce $A \rightarrow b$
\$036	\$A	ab\$	reduce $A \rightarrow aa$
\$02	\$	b\$	shift 3
\$023	\$a	b\$	shift 4
\$0234	\$ab	\$	reduce $A \rightarrow b$
\$0236	\$aa	\$	reduce $A \rightarrow aa$
\$025	\$A	\$	reduce $S \rightarrow AA$
\$01	\$	\$	accept

Yes, it is accepted.