

Name - Rajasree Loha

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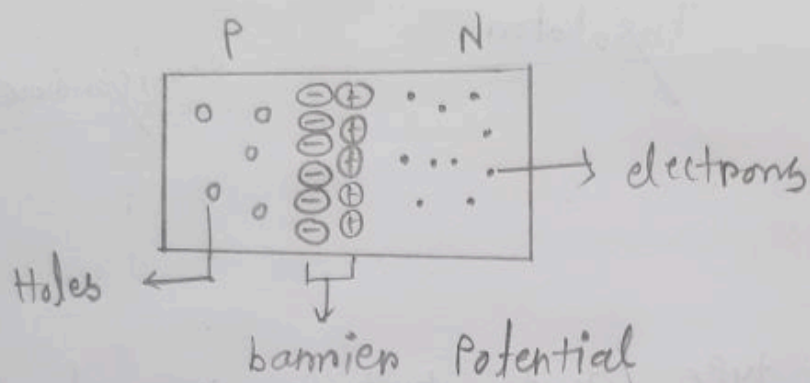
~~Sec~~

1) i) metal                      Insulator                      Semiconductor  
ii) This can

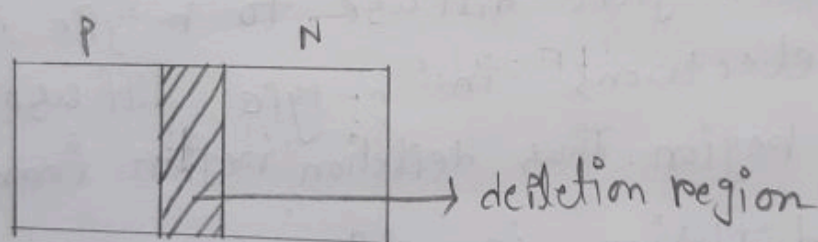
5) i) when p-type and n-type materials are joined, diffusion occurs. Excess holes in p-type region diffuse to n-type, and excess electrons in n-type diffuse to p-type region. Thus depletion region formed.

ii) This diffusion is opposed by the resulting electric field of the uncovered ionic charges. The positive ion cores in the n-type region oppose the diffusion of the p-type carriers from the p-type region and vice versa. This resulting electric field which opposes diffusion is called barrier potential  $V_0$  in p-n junction diode, under equilibrium.





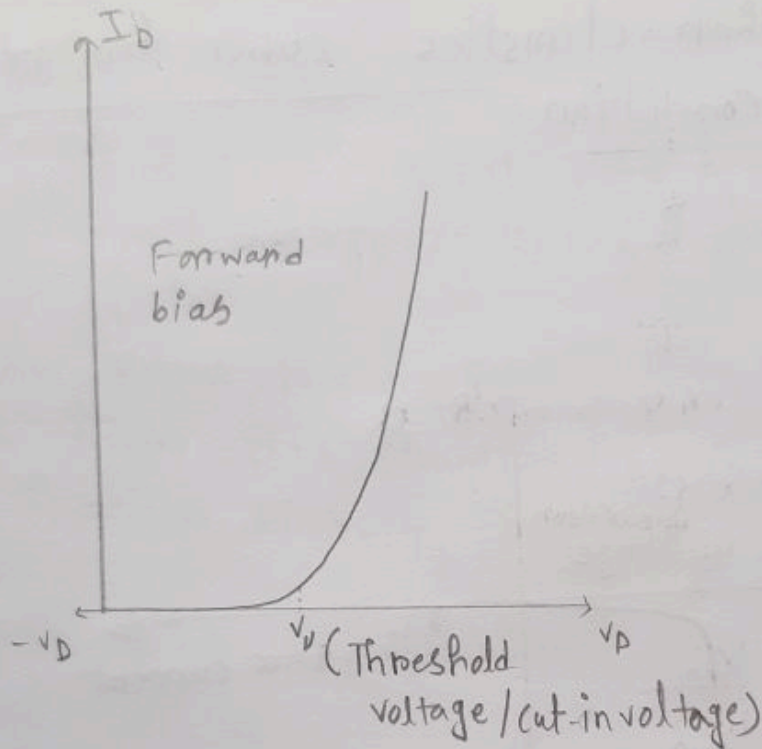
P-N junction diode



P-N junction diode

(ii) Explanation of the forward and reverse bias characteristics curve of P-n junction diode from Shockley diode equation.

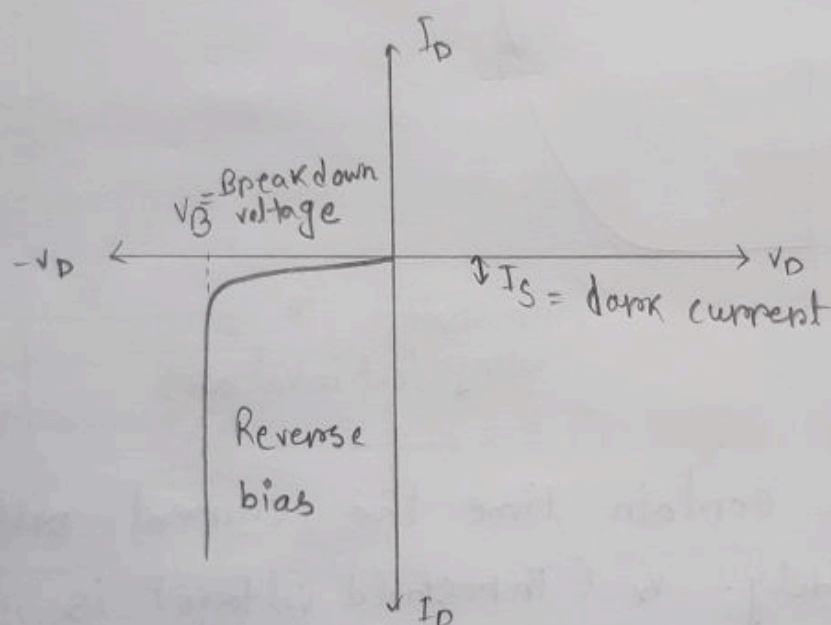
V-I<sub>v</sub> curve for forward bias condition characteristics



After a certain time the current will increase so rapidly.  $V_D$  (Threshold voltage) is also called cut-in / cut-off voltage. Cut-in voltage is different for different semiconductor. Like, cut-in voltage of Ge is 0.2V, whereas cut-in voltage of Si is 0.6V. After some time (cut-in voltage) the diode will be a short circuit.



V-I characteristics curve for reverse bias condition



After a certain time gap the breakdown occurs in the diode in reverse bias condition.

This breakdown is called 'Avalanche breakdown / high voltage break down'.  $I_S$  is called dark current here.

From the equation,  $I_D = I_S (e^{\frac{V_D}{nV_T}} - 1)$  — (1)

Where  $I_D$  = the current across the diode  
 $I_S$  = Reverse Saturation current



$V_D =$  Applied voltage

$\eta =$  Ideality factor ( For Ge,  $n=1$   
for Si,  $n=2$  )

$V_T =$  Thermal voltage ( 26mv at room temperature )

We can know the <sup>Explanation of</sup> above characteristics curve from this equation [Eqn ①]. Here for forward bias,  $I_D \approx I_S e^{\frac{V_D}{\eta V_T}}$  and for reverse bias,  $I_D \approx -I_S$ .

Q) Q-point - The operating point of a device, also known as a bias point, quiescent point or Q-point, is the steady-state DC voltage or current at a specified terminal of an active device, such as a transistor with no signal applied.  
input

As the value of  $\beta$  and the value of  $V_{BE}$  are not same for every transistors, whenever a transistor is replaced, the Q-point tends to change. So it is necessary to stabilize the Q-point.



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(i)

$$I_B = \frac{V_{BB} - V_{BE}}{R_B}$$

$$= \frac{10 - 0.7}{360 \times 10^3} \quad [\because V_{BE} = 0.7V \text{ we know}]$$

$$= 2.58 \times 10^{-5} \text{ A}$$

$$= 25.8 \mu\text{A}$$

$$I_C = \beta I_B$$

$$= \frac{V_{CC}}{R_C}$$

$$= \frac{10}{2 \times 10^3}$$

$$= 5 \times 10^{-3} \text{ A}$$

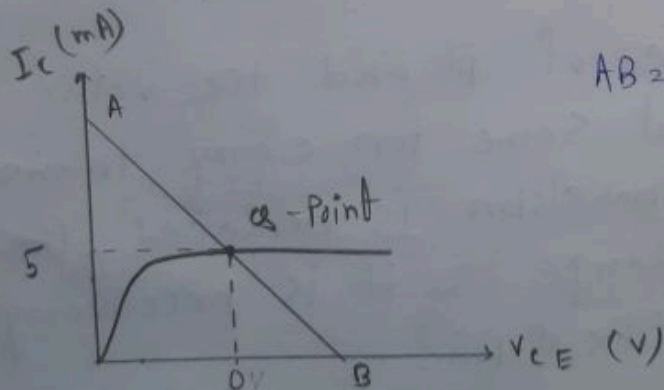
$$= 5 \text{ mA}$$

$$V_{CE} = V_{CC} - I_C R_C$$

$$= 10 - 5 \times 10^{-3} \times 2 \times 10^3$$

$$= 0 \text{ V}$$

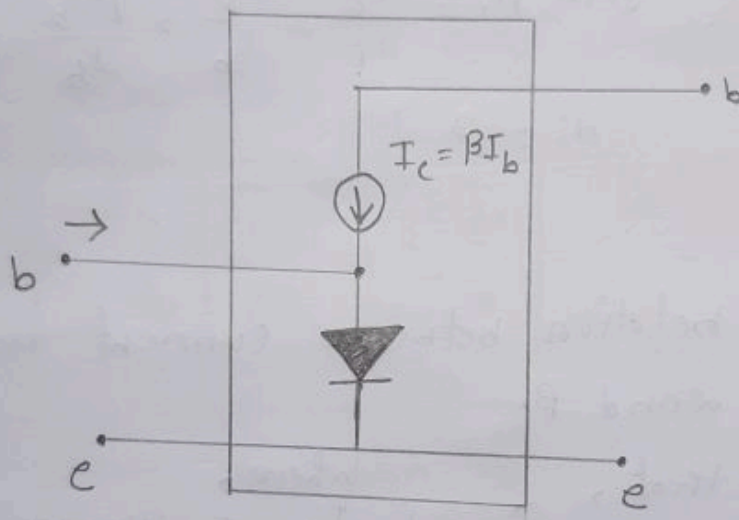
(ii)



AB = loadline

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(i) Small signal re model for CE mode BJT amplifier -



(ii) a) Input impedance -  $Z_i = \frac{V_i}{I_i} = \frac{V_{be}}{I_b}$

$$Z_i = \frac{V_{be}}{I_b} \approx \frac{\beta I_b r_e}{I_b}$$

$$\therefore Z_i \approx \beta r_e$$

[where,  $\beta$  = current amplification of CE mode

$r_e$  = forward resistance]



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(b) output impedance,  $Z_o = r_o$ (c) voltage gain,  $A_v = \frac{V_o}{V_i} = \frac{-R_L}{r_{e_i}} \left[ \begin{array}{l} V_o = -I_o R_L = -I_c R_L \\ = -\beta I_b R_L, V_i = I_i Z_i \\ = I_b \beta r_e \end{array} \right]$ (d) current gain,  $A_i = \frac{I_o}{I_i} = \frac{I_c}{I_b} = \frac{\beta I_b}{I_b} = \beta$ 

$$A_i = \beta$$

8) (ii) The relation between current amplification factors  $\alpha$  and  $\beta$  -

we know that,

$$I_E = I_c + I_B$$

where,

$$\left[ \begin{array}{l} I_E = \text{Emitter current} \\ I_c = \text{Collector current} \\ I_B = \text{Base current} \end{array} \right]$$

$$\Rightarrow \frac{I_E}{I_c} = \frac{I_c}{I_c} + \frac{I_B}{I_c}$$

$$\Rightarrow \frac{1}{\alpha} = 1 + \frac{1}{\beta}$$

$$\Rightarrow \frac{1}{\alpha} = \frac{\beta + 1}{\beta}$$

$$\Rightarrow \alpha = \frac{\beta}{1 + \beta}$$

$$\text{and, } \frac{1}{\alpha} - 1 = \frac{1}{\beta}$$

$$\Rightarrow \frac{1 - \alpha}{\alpha} = \frac{1}{\beta}$$

$$\Rightarrow \beta = \frac{\alpha}{1 - \alpha}$$

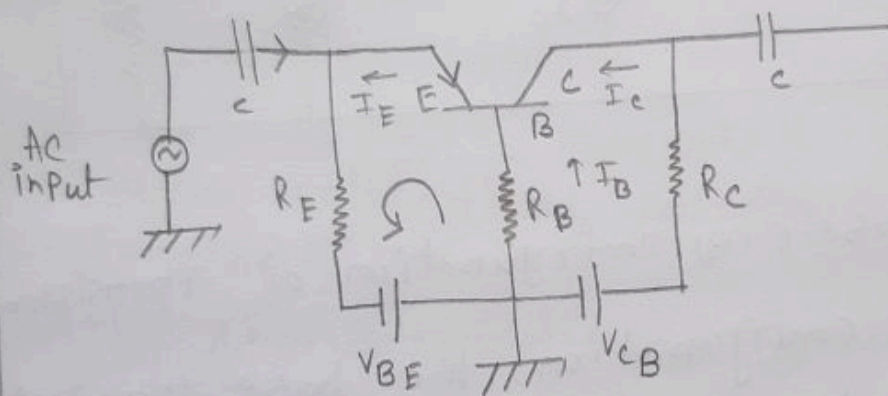
$$\left[ \therefore \beta = \frac{I_c}{I_B}, \alpha = \frac{I_c}{I_E} \right]$$



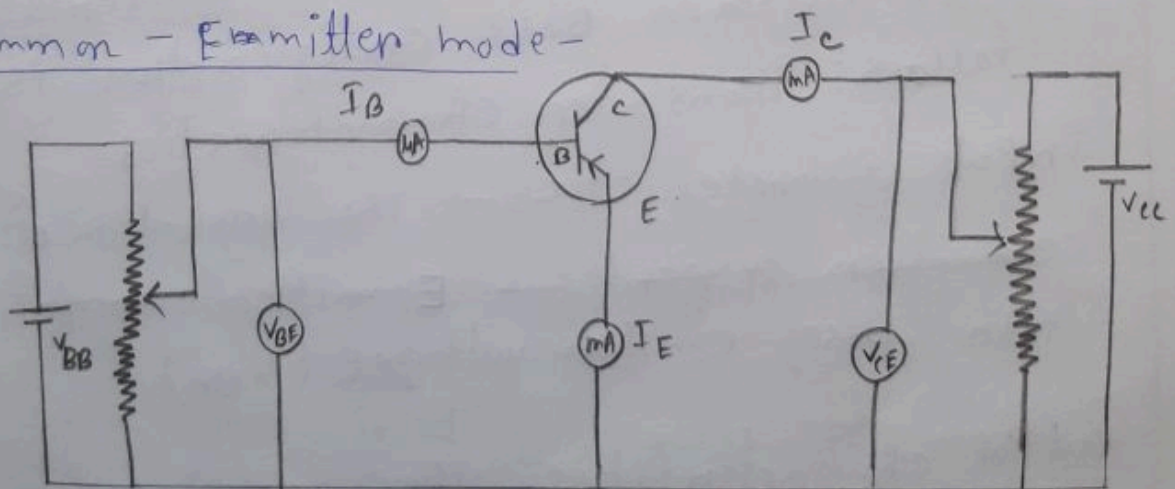
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(iii) The configuration of CB, CE and CC mode operation in a transistor -

Common - Base mode -

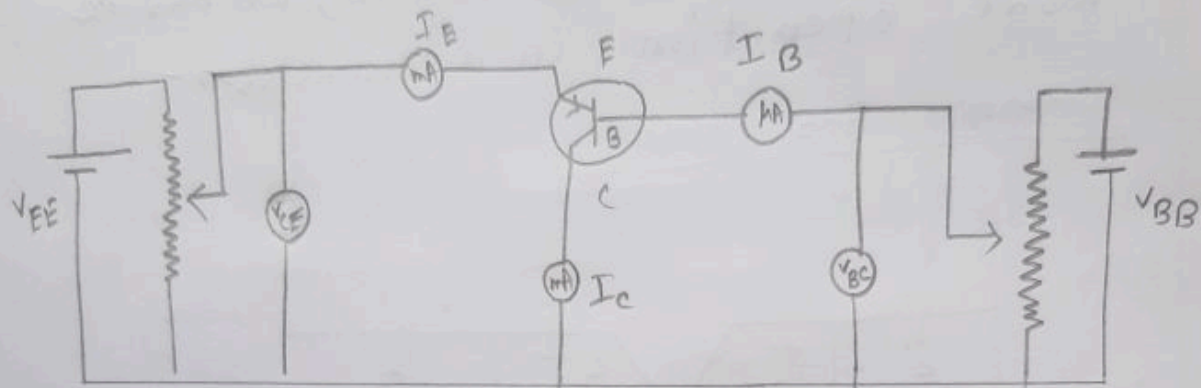


Common - Emitter mode -





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Common-collector mode -Common Base (CB) configuration of Transistor -

In CB configuration, the base terminal of the transistor will be connected common between the output and the input terminals. The transistor characteristic under Common Base configuration is as follows: Transistor characteristics.

Input characteristics - The variation of emitter current ( $I_E$ ) with Base-emitter voltage ( $V_{BE}$ )

Output characteristics - The variation of collector current ( $I_C$ ) with Base-collector



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voltage ( $V_{CB}$ )

CE configuration of transistor - The CE mode is the most widely used transistor configuration. The CE amplifiers are used when large current gain is needed. The input signal is applied between the base and emitter terminals while the output signal is taken between the collector and emitter terminals.

Input characteristics - The variation of  $I_B$  (Base emitter current) with  $V_{EB}$  (Collector-emitter voltage)   
 Base

Output characteristics - The variation of  $I_C$  (Base collector current) with  $V_{CE}$  (Collector-emitter voltage)   
 emitter

CC configuration of transistor - In CE mode, the collector terminal of the transistor will be connected common between the output and input terminals. The variation of emitter current with collector-base voltage, keeping collector-base voltage constant.



Input characteristics - The variation of Collector-Emitter current ( $I_E$ ) with the Collector-Emitter voltage ( $V_{CE}$ ).

output characteristics - The variation of Base current ( $I_B$ ) with the collector-Base voltage ( $V_{BC}$ ).

- (i) Different current components in P-n-p transistor in active region with a suitable diagram - In a P-n-p transistor, the reverse saturation current ( $I_{CBO}$ ) will comprises of the current due to the holes passing through the collector junction from the base to collector region ( $I_{hco}$ ) and the current due to the electrons which are passing through the collector junction in the opposite direction. we know about some current components like,  $I_C$ ,  $I_B$ ,  $I_E$ ,  $I_{CBO}$ ,  $I_{CEO}$  etc. There are some of relations between them. Like -



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$$I_E = I_C + I_B \quad \left[ \begin{array}{l} I_E = \text{Emitter current} \\ I_C = \text{Collector " } \\ I_B = \text{Base " } \end{array} \right]$$

$$I_C = \alpha I_E \quad \left[ \alpha = \text{current amplification factor in CB mode of transistor} \right]$$

$$I_C = \alpha I_E + I_{CB0}$$

$$I_C = \frac{\alpha I_B}{1-\alpha} + \frac{I_{CB0}}{1-\alpha}$$

$$I_{CE0} = \frac{I_{CB0}}{1-\alpha}$$

$$I_{CE0} = (\beta + 1) I_{CB0} \quad \left[ \because \frac{1}{1-\alpha} = \beta + 1 \right]$$

$$I_{CB0} \cong \beta I_{CB0}$$

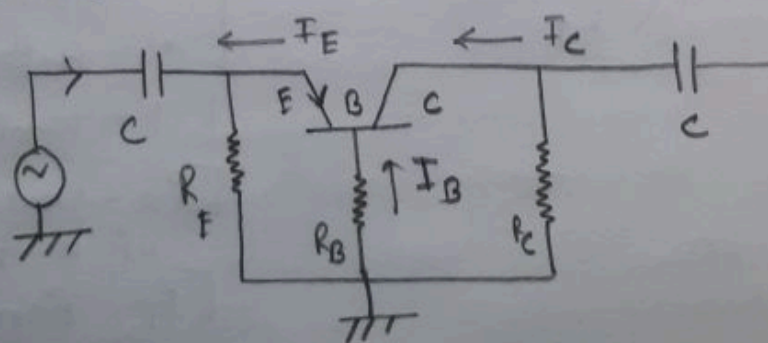
$$I_C = \beta I_B$$

$$I_C = \beta I_B + 1 + I_{LE0}$$

$$I_C = \beta I_B + (1 + \beta I_{CB0})$$

$$I_E = I_B (\beta + 1)$$

Diagram - (for CB mode of transistor)





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4) (i) the equilibrium concentrations of electrons ( $n_0$ ) and holes ( $p_0$ ) in a semiconductor can be expressed as -

$$(a) \quad n_0 = N_c e^{-\left(\frac{E_c - E_F}{kT}\right)} \quad (b) \quad p_0 = N_v e^{-\left(\frac{E_F - E_v}{kT}\right)}$$

we know that the probability of getting electrons -

$$n(E) dE = [Z_c(E) d(E)] [F_c(E)]$$

where,

$[n(E) dE = \text{probability of getting electrons}]$

$[\text{occupation of electrons} = F_c(E)]$

Hence,  $n_0 = N_c e^{-\left(\frac{E_c - E_F}{kT}\right)}$  [If  $E > E_F$   
then  $n_0$  decrease]

$[N_0 = \text{concentration of electrons}]$

$[N_c = \text{numbers of density of bands}]$

$[k = \text{Boltzmann Constant}]$

$[T = \text{temperature}]$

$[p_0 = \text{concentrations of holes}]$

$[E_F = \text{fermy level}]$

$$p_0 = N_v e^{-\left(\frac{E_F - E_v}{kT}\right)}$$



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(ii) For n-type material the minority hole concentration -

$$p_n = \frac{n_i^2}{n} \approx \frac{n_i^2}{N_D} \quad [N_D = \text{donor ions concentration}]$$

For p-type material the minority electron concentration

$$n_p = \frac{n_i^2}{p} \approx \frac{n_i^2}{N_A} \quad [\text{acceptor ions concentration} = N_A]$$

we know that -

$$\text{Now, } n_0 = n_i e^{(E_f - E_i)/kT}$$

$$\text{and } p_0 = n_i e^{(E_i - E_f)/kT}$$

$$\therefore n_0 p_0 = n_i \cdot n_i e^{(E_f - E_i + E_i - E_f)/kT}$$

$$n_0 p_0 = n_i^2 \quad (\text{Proved})$$

The product of electron and hole concentrations of ~~electrons~~ under equilibrium is constant and can be expressed as -  $n_0 p_0 = n_i^2$



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(ix) Distinguish between insulator, semiconductor and metal on the basis of energy band diagram -

metal - A crystalline solid is called a metal. If the upper energy band i.e. conduction band is fully filled, or the upper most filled band and the next unoccupied band overlap in energy.

Here the electrons in the uppermost band find neighbouring ~~vacant~~ vacant states to move in, and thus behave as free particles. ~~It~~ This free particles (e) Produce electric current. metal is a good conductor of electricity.

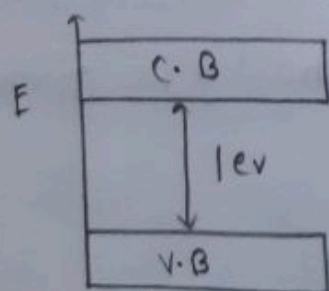
Insulator - In some crystalline solids, the forbidden energy gap between the uppermost filled band, called the valance band, and the lowermost empty band called the conduction band, is ~~very~~ very large. In this such solids, at ordinary temperatures only a few electrons can acquire



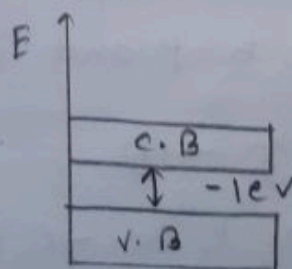
## ECE VGE Sol

enough thermal energy to move from the valance band into the conduction band. since only a few electrons are available in conduction band, an insulator is a bad conductor of electricity.

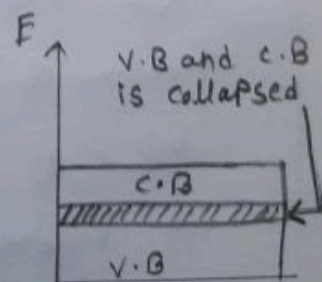
semiconductor - A material for which the width of the forbidden energy gap between valance and the conduction band is relatively small ( $\sim 1\text{eV}$ ) is referred to as a semiconductor. As the forbidden gap is not very wide, some of the valance electrons acquire enough thermal energy to go into the conduction band. These electrons then become free and can move. This electrons are cause of current conduction in semiconductor. semiconductor is not a good ~~so~~ current conductors like ~~me~~ metal but not such ~~as~~ bad current conductors like insulator.



Insulator



Semiconductor



metal



[V.B = valance Band  
C.B = Conduction Band]

(iii) Einstein relation - the relation in which the mobility of charges in an ionic solution or semiconductors is equal to the magnitude of the charge times the diffusion coefficient divided by the product of the Boltzmann constant and the absolute temperature.

The relation is - 
$$\frac{D_n}{\mu_n} = \frac{D_p}{\mu_p} = \frac{kT}{e}$$

(ii) Let, PQ is a conductor of length 'l' and a potential difference 'V' is applied across PQ. An electric field is developed directing from Q to P and its intensity is given

by,  $E = \frac{V}{l}$  - (1)

We know,  $\vec{F} = -e\vec{E}$



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If 'm' be the mass of the electron, then its acceleration,

$$\vec{a} = \frac{\vec{F}}{m}$$

$$\Rightarrow \vec{a} = \frac{-e\vec{E}}{m}$$

$\therefore$  the average thermal velocity of electronic electrons is zero.

$$\therefore u = 0$$

$\therefore$  Therefore using first equation of motion ( $v = u + at$ ) we have

$$\vec{v}_d = 0 + \left( \frac{-e\vec{E}}{m} \right) \tau$$

$$\Rightarrow \vec{v}_d = \left( \frac{-e\vec{E}}{m} \right) \tau \quad \text{--- (11)}$$

numerically we can write,

$$|\vec{v}_d| = \frac{eE}{m} \tau$$

~~Compari~~  $\therefore$  From equation (1),  $E = \frac{V}{\lambda}$

$$\therefore v_d = \frac{e\tau}{m} \frac{V}{\lambda} \quad \text{--- (11)}$$

Now,  $\vec{v}_d = -\frac{e\tau}{m} \vec{E}$

$$\Rightarrow \vec{v}_d = -\mu_n \vec{E}$$



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$$\Rightarrow |\vec{v}_d| = \mu_n |\vec{E}|$$

$$\Rightarrow v_d = \mu_n E$$

$$\left[ \frac{e\tau}{m} = \text{constant term} \right]$$

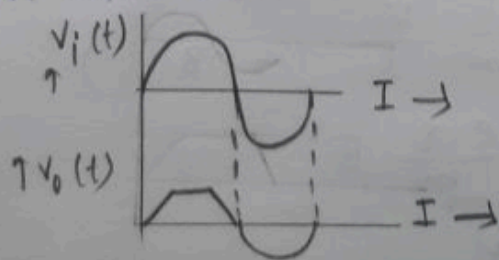
$\mu_n = \text{mobility of a given metal}$

$\left[ \tau = \text{the characteristics of specific metal} \right]$

$\left[ e = \text{constant} \right]$

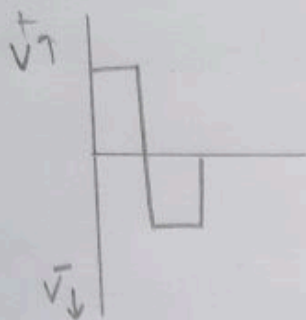
So the equation relation between drift velocity, mobility and electric field for an electron in a semiconductor under thermal equilibrium -  $v = \mu_n E$  is established.

Q.ii) The clipper is a wave shaper. It is used to shape the waves. It is one type of diode. In this fig. 1 the diode is in forward bias, and the battery is also on positive side. So, how the output diagram will be -



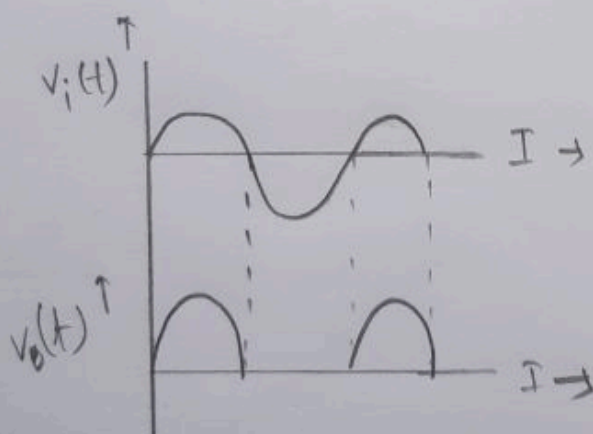


(ii)



In 1st circle the diode will be off and in 2nd circle the diode will be on. So we get no voltage in 1st half cycle and we get a voltage in 2nd half cycle (sample is also a wave shape).

(iii)



It is the half-wave rectifier output waveform.

The amplitude of cosine even harmonic components of it - it's ~~harmonic~~ <sup>100</sup> this is 85 times of it's amplitude.