## Assignment - 4

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	,,,,,,	rage No					
	Explain CPU-Input loutput burs	st cycle					
	& Ilo wait The state of proces	under exicution is with					
frocess exicution consists of a cycle of CPU exicution is  so Ilo wait. The state of process under exicution is  cpu burt of the state of process under Ilo request							
	Dimensional Mariala Latinos	n there two states proces					
<u>P</u>	CALLUTION HOOFING COLLEGIES OF LOTT PLI	$\mathbf{r}(+, \mathbf{m}) = \mathbf{r}(+, \mathbf{m}) = \mathbf{r}(+, \mathbf{m})$					
J	To burst which is followed b	y another cru burt the					
- la	nomer 110 hurst and 30 on t	ventually the final ceup.					
6	nds with a system request to	terminate execution					
	toad store	n to wited many my					
	Mand Store	cpu Bwst					
	Read from file						
	wait for I/o store increment	7 Ilo Burst					
	•	CPU Burst					
	Write indexe write for I/o						
	load store	Ilo Burst					
	Add store	CPV Burst					
	Read From file	CPO DWJ7					
	Wait for I/o }	The Durel					
	,	Ilo Burst					
AT							
<u> </u>	e process during execution in	ndergoes two phases CPU					
- 11	+ & Ilo Bunt						
3) WY	ren a process is allocated con	S & other resources bis					
6260	uling, this is the CPU hurst pl	rare of the process					
3) Wh	en a process wait for some	Ilo operation or some					
other	other task to be completed, this is the Ilo burst phase of						
the	process						
4) The	execution of a process cons	sists of an alternation of cell					
bursts	\$ Ilo bursts	9 24 27 27 27 27 27 27 27 27 27 27 27 27 27					

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s) A process begins & ends with a cru burst. In beth cru activity is suspended whenever an Ilo operation is needed

5) loud store data add store data read from file store increment, write to file, delete daty from a file add increment & other operations like these are known as cru burst.

7) Ilo burst contains any input & output. The whole process as from the beginning of a cru burst & ending with the system request to close execution is known as cru-Ilo burst cycle.

A process schedules different processes to be assigned to the CPU based on particular scheduling algorithms.

There are six popular process scheduling algorithms.

1) First-Come, First-served (FCFS) scheduling.

2) Shortest-Job-Next (SJH) scheduling.

- 3) Priority Scheduling
- 4) shortest Remaining Time
- 5) Round Robin (RR) scheduling
- 6) multiple-level Queues scheduling

These algorithms are either non-preemptive or preemtive.

Hon-preemptive algorithms are designed so that once a

process enters the running state, it cannot be preempted

until it completes its allotted time of preemptive scheduling is
based on prioprity where a scheduler may preempt a low

priority running process any time when a high priority process
enters into a ready state.

1) first-come, first-served (FCOFS)

1) Jobs are executed on first come, first serve basis

2) It is a non-preemptive, pre-emptive scheduling algorithms

	3) Fary to understand & implement.  3) Fary to understand & implement.  1) Fary to understand be implement.					
-		1 maderi	tand & implementation to have as a very	ETEO CIVEU	0	
	3) [	ary to united	hion I haved al	and the	20	
	<u> </u>	tr implemente	ion li hair di	ge war in	The Trappal	
	5) P	oor in persuit		o Service 1	inat	
-	- 10	en Arrival Ti	me Execute Tim	6 26,416	11.6	
	Proc		5	The second secon		
_	Po		3	5		
_	Pı	2	8	8		
	P2.	3	6	16		
_	l P3					
		O PI P	2 / 13	+		
	P	0 111			The state of the s	
	0	5 8	16 22	1		
A 7	0	Time	e = service Time -	Arrival Time		
	Proce		0-0=0		Average:	
	Po Pi		5-1=4		=0141619/	
-	F <sub>2</sub>				= 2.12	
			8-2=6		-3.0	
	P3		16-3=13			
	11					
	11	test Job Next 1		1 1 1 1 1 1 1 1 1	4 1	
	11		as shortest job 1			
8 4 1	2) This i	Janon-pree	mptive, pre-em	phive schedi	uling algorithm	
	3) Best a	pproach to m	unimize waitin	ig time		
1,	4) Faly	to implement	in Batch lyste	on where r	equired CPU	
	time is	known in ad	vance	4.5	_	
	The state of the s		ement in interc	uchino with	me whom -	
	required	CPU time 15	not known	TOTAL TOTAL	NO WINE	
	e) The processer should know in advance how much fir				h time	
The Prof	processa	ill beko	IN MIOW IN ANI	rance how	Much III	
	211	Arrival Time	Tuganta Tima		0.1	
	Po	O O			10	
	Pi		5	0		
	P2	2	3	5		
		3	8	14		
	P3	ی	0	8		

11			
J,	Process Arrival Time Execute Time	service Time	(0,3,8,16,22)
	Process minyal lillier	3	
	(C)	0	
1000		16	
	C2 2	A service section in the section of	
	S S	5	

a) Priority Based scheduling

Priority scheduling is a non-preemptive algorithm & one of the most common scheduling algorithms in batch systems Neuch process is assigned a priority Process with highest priori is to be executed fint \$ so on

3) Processes with same priority are executed on first come

first served busis

4) Priority can be decided based on memory requirements. time requirements or any other resource requirements

		Li - Timo	Principy	Sprice. Tim
Process	Arrival Time	Execution Time	Inonia	301710
		S	1	0
PO !			2	11
PI		3		
	2	8		14
12		6	3	5
P3	3	450		

1	8					
		A . ( - 1 Tree 6	Execute Time	Priority	service Time	
3,14,22	Process	nrnya IIne	Execute Time		9	
	Po	Q	3		6	
THE REAL PROPERTY.	Pi		3		14	
appropriate to the second	P2	2	with the Property	1151 1 341	alm of leading	-
4	00	3	6	3	0	
	1 2					

4) Shortest Remaining Time

1) shortest remaining Time (SRT) is the preemptive version the ITH algorithm.

3) The processor is allocated to the job closet to complet

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The Land	but it can be preempted by a newer ready job with shorter time to completion
	shorter time to completion.
in Sec	3) Impossible to implement in interactive system.
20	required cru time is not known
	required cru time is not known where there here to give preference.
4	need to give preference.
	and the state of t

s) Round Robin scheduling

Dround Robin is the preemptive process scheduling ale e) rach process is provided a fix time to execute itis a quantum

3) once a process is executed for a given time period preempted & other process executes for a given time per solution of preempted solves to save states of preempted processes

	Process	Wait Time = Service Time - Arrival Time	
	Po	(0-0) + (12-3) = 9	0 0
1	Pi	(3-1)=2	Average: 9121
	P2	(6-2)+(14-9)+(20-17)=12	285
	P3	(9-3) + (17-12) = 11	1

	Po	Pı	Pz	<b>f</b> <sub>3</sub>	Po	P2	P <sub>3</sub>	PL		entite on	1	
0	3	6	9	12	_ 10	1	2	0 2	2	e e		1

6) Multiple - level Queues scheduling

multiple-level queues are not an independent schedulie algorithm They make use of other existing algorithm by & schedule jobs with common characteristics ismultiple queues are maintained for processes with comme characteristics

2) Fach queue can have its own scheduling algorithms

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	3) Priorities are assigned to e	ach queue						
	Ilo bound jobs can be	scheduled in one queue & all reve. The process scheduler then each queue & assigns them to rithm assigned to the queue.						
$\xrightarrow{3}$	Difference between preempt	ive on non-preemptive schedule						
	Preemptive Scheduling	Non-preemptive scheduling						
	to a process for a long time period	once resources are assigned to a process, they are held until it completes its burst period or changes to the waiting state.						
2	in the middle of the execution	when the processor starts the process execution, it must complete it before executing the other process \$ it may not be interrupted in the middle						
	s) When a high-priority process continuously comes in the ready queue a low-priority process can statue.	uses a CPU, another process will						
	4) It is flexible	It is rigid						
	s) It is cost associated	It does not cost associated						
	6) It has overheads associate with process scheduling.	d It doesn't have overhead						

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<u> </u>	It affects the design of the operating system kernel	01 110
1/	Its cpu utilization is very high.	Its CPU utilization in
11	e.g-Round Robin (n shortest Remaining Time First	eg-FCFS \$ SJF are eg preemptive scheduling
10) T	t is expensive in nature	It is not expensive in
	in the second se	12711900 200
	The state of the s	off the albein off
	t trap a series of the last	gard oner
	A STAN COLORATE TO	74
(3)		The same of the same to