M RAJESH KANNAN

Senior Analyst, Capgemini

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EXPERIENCE

PROJECT INTERN

Research Centre Imarat

Feb 2022 – May 2022 Hyderabad, India

- Analysis and Visualization Tools for Bus Data in Avionic Systems.

INTERN

Maven Silicon

June 2021 – August 2021 Hyderabad, India

- Overview of VLSI Design Flow, SoC design, differences between ASIC and FPGA. Steps in VLSI design Flow, FSM designs and Verilog HDl programming.

EVENT CO - ORDINATOR

Robotics Club, VIT Chennai

June 2019 – August 2020 Chennai, India

- Event Coordinator for the Roboprix All Terrain Vehicle Event.
- Volunteered for the management of the Roboprix Drag Race

STRENGTHS

- Hard Worker
- Adaptive
- Fast Learner
- Inquisitive
- Diligent

TECHNICAL SKILLS

- Python, PyQt, LTSpice, ML, C, C++, C-Sharp, JAVA, Verilog, R, MATLAB

CERTIFICATIONS

- Fundamentals of Parallelism on Intel Architecture
- VLSI Design Methodologies.
- IoT Domain Analyst
- Emerging Technologies in Electronic Devices, Systems and Computational Techniques.

SOFTWARE TOOLS

- Xilinx, Keilμvision
- NetSim, RStudio, Tinkercad
- NodeRed, FRED
- Jupyter Notebook

INTERESTS

- Physics, Astronomy, History.
- Technology, Gaming, TV Shows & Movies.
- Cricket, Football, Table Tennis & Volleyball.

EDUCATION

Bachelor of Technology (Electronics & Communication)

Vellore Institute of Technology

2022 Chennai

- CGPA: 8.95

Senior Secondary (TBSIE)

Gyanavapi Junior College

2018 Hyderabad

- 93.6%

Secondary School (CBSE)

Brahm Prakash DAV School

2016 Hyderabad

- CGPA: 9.6

PROJECTS

Species Customized Smart Plant Watering System For Horticulture Development

- The objective of the project was to identify the plant species using machine learning model from an existing database and water the plant accordingly to save water and mitigate stagnation.

Image and Text Steganography

- Aim of the project was to hide the data in image using least significant bits substitution algorithm using Python and MATLAB.

FPGA Implementation of Point Operations over Prime field for Elliptic Curve Cryptosystem

- The purpose of the project was to implement point operations in the encryption process onto the FPGA for improving the security in power constraint applications.

Hearing Cap

- The aim of the project was to develop an alerting system using sound sensors and visual cues for the deaf.

Bluetooth Controlled Bot Using Intel MCS-51

- The aim of the project was to control the movement of the bot using Bluetooth.

LANGUAGES

- English, Hindi - Full Professional Proficiency
- Tamil, Telugu - Native or Bilingual Proficiency
- Spanish - Elementary Proficiency