

# AVNEESH YADAV

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## EDUCATION

Degree	Institute/Board	CGPA/Percentage
B.Tech (ECE)	Indian Institute of Information Technology ,Guwahati	7.50 Till 5 <sup>th</sup> sem
Intermediate	U.P board	78%
High School	CBSE	10 CGPA

## SKILL

Verilog, C Programming Language, OOPs Using C++

## COURSEWORK

Computer programming in C

Digital Design

CMOS Digital VLSI Design

Physical Design Flow

Clock Tree Synthesis

STA

Low Power Design

## ACTIVITIES

Playing Cricket,

Watching movies , listening song , sketching.

## **CERTIFICATION**

Udemy Certify in VSD- Static Timing Analysis

## **PROJECT**

Learning based QRS complex ECG signal for Tachycardia patients .

## **Strength**

Team Player, Problem solver- whatever the complexity, Confident,  
Positive Approach towards Every Aspect of life.

## **Declaration**

*I hereby declare that the information mentioned above is correct to the best of my knowledge and I bear the responsibility for the correctness of the same.*

**AVNEESH**

**YADAV**