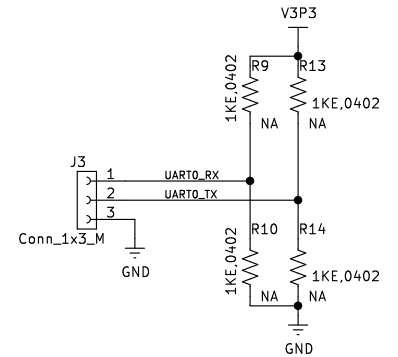
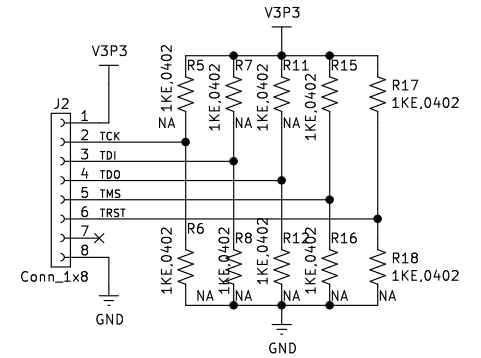
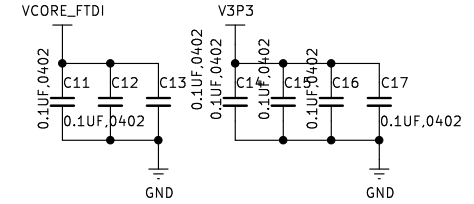
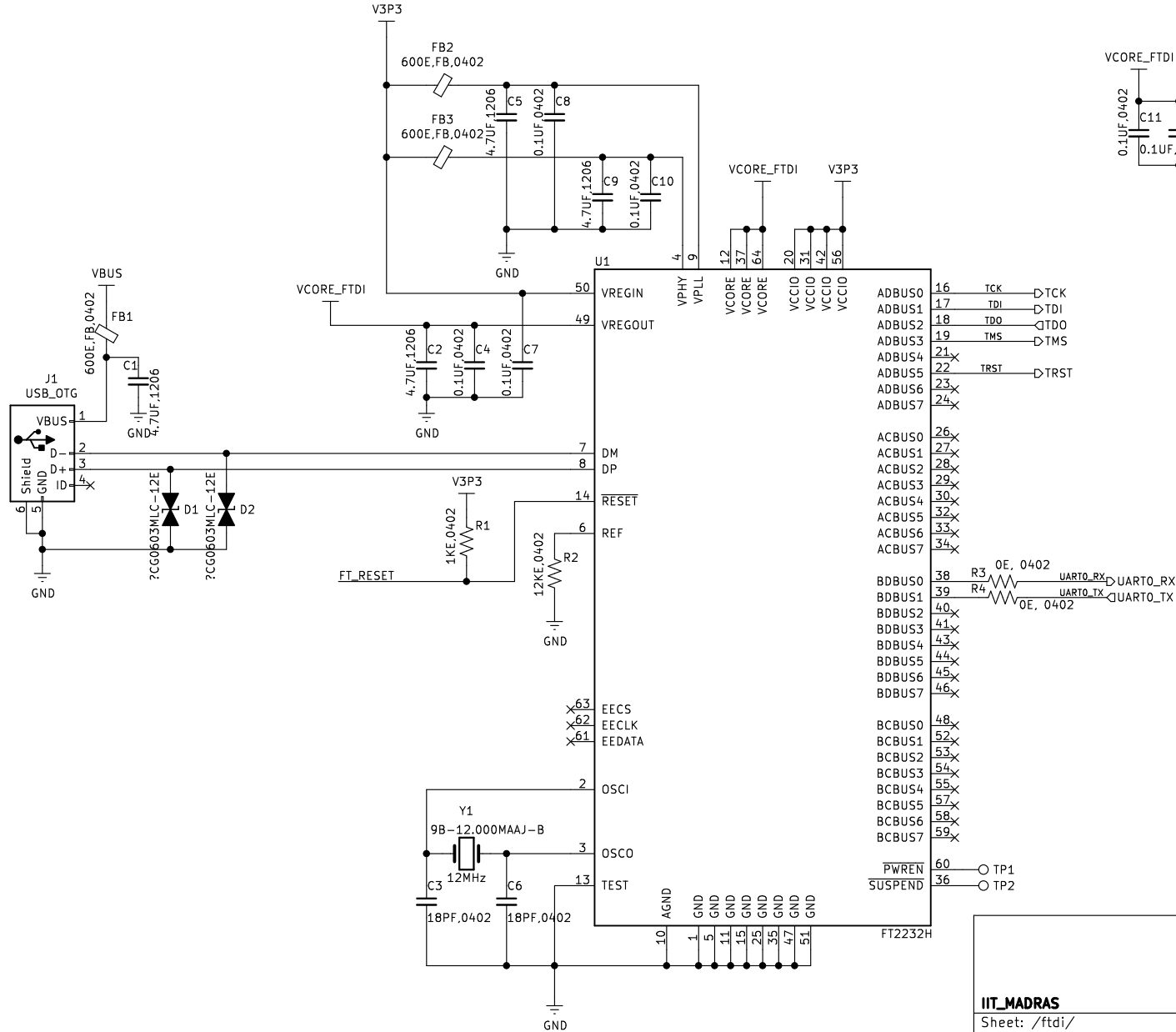


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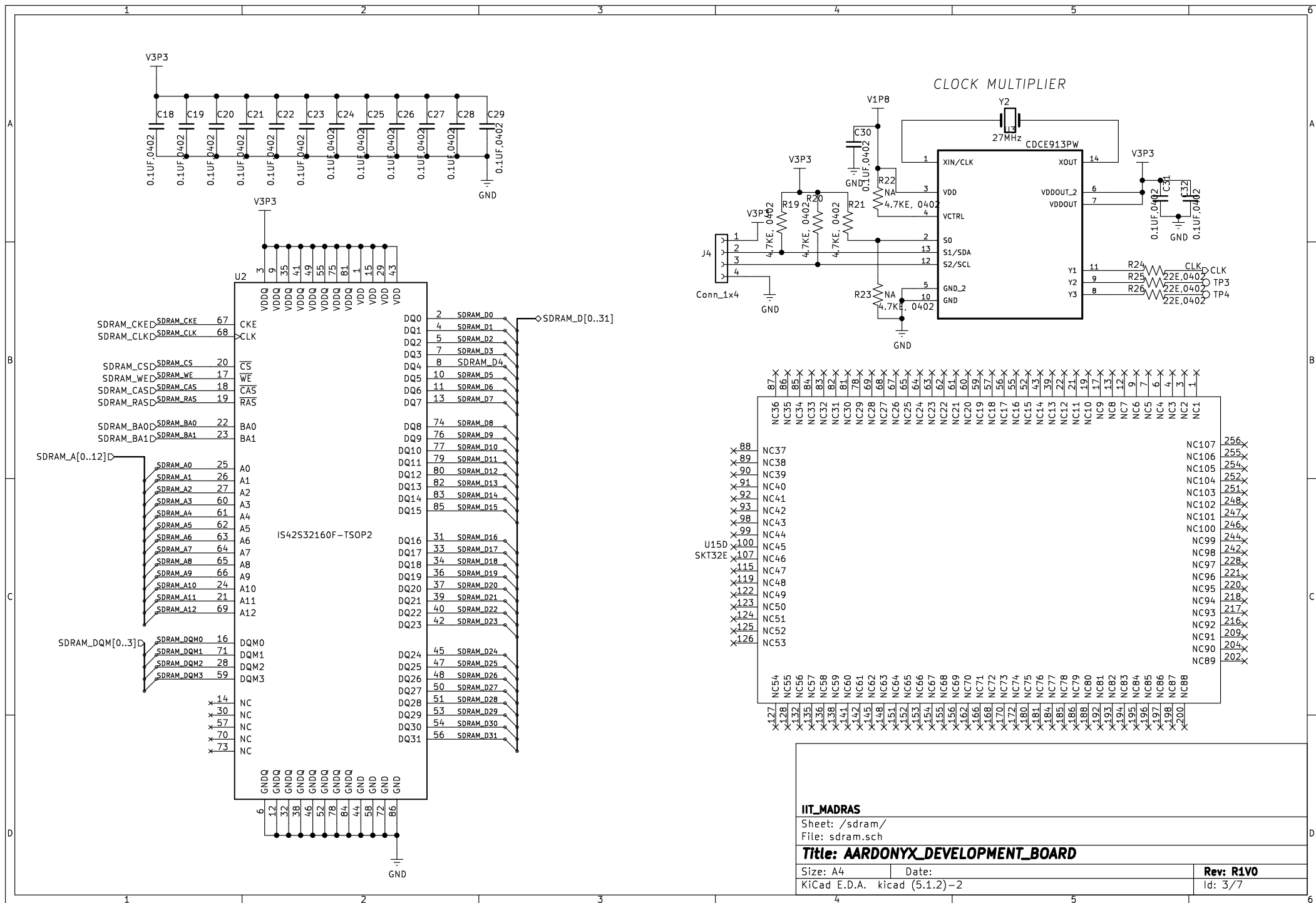
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File: ftdi.sch

## Title: AARDONYX\_DEVELOPMENT\_BOARD

Size: A4  
KiCad E.D.A. kicad (5.1.2)-2

Date:  
Rev: R1V0  
Id: 2/7



IIT\_MADRAS

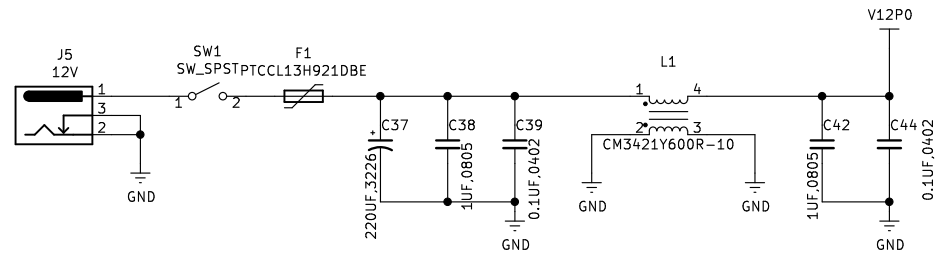
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File: sdrum.sch

**Title: AARDONYX DEVELOPMENT BOARD**

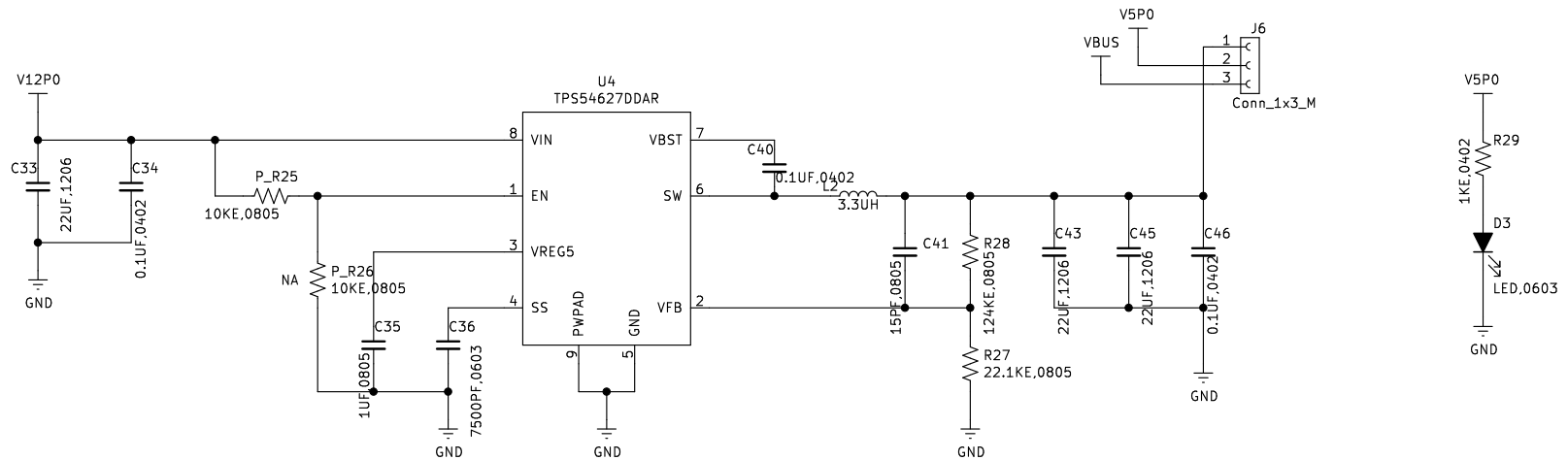
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KiCad E.D.A. kicad (5.1.2)-2

Date:  
Rev: R1V0  
Id: 3/7

# DC JACK POWER SUPPLY



# SWITCHING REGULATOR FOR V5P0 POWER RAIL



IIT\_MADRAS

Sheet: /power\_1/  
File: power\_1.sch

**Title: AARDONYX\_DEVELOPMENT\_BOARD**

Size: A4  
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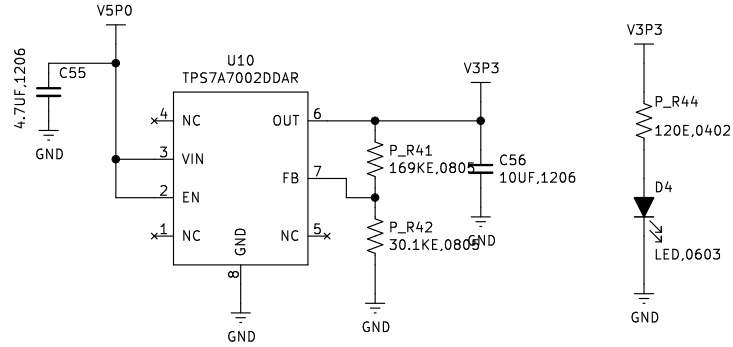
Date:

Rev: R1V0

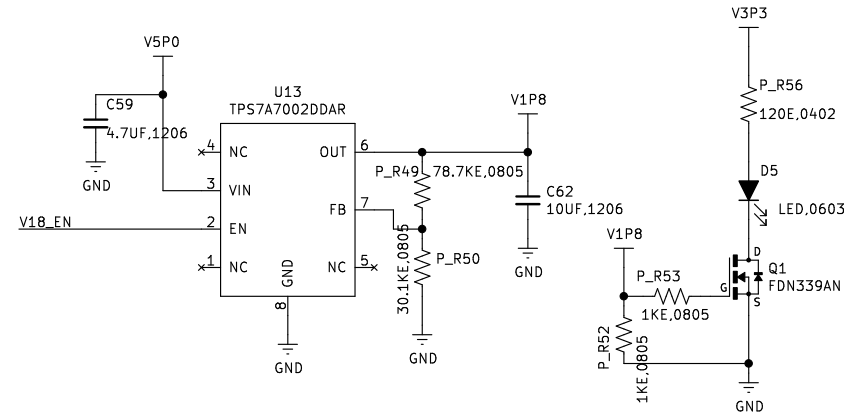
Id: 4/7



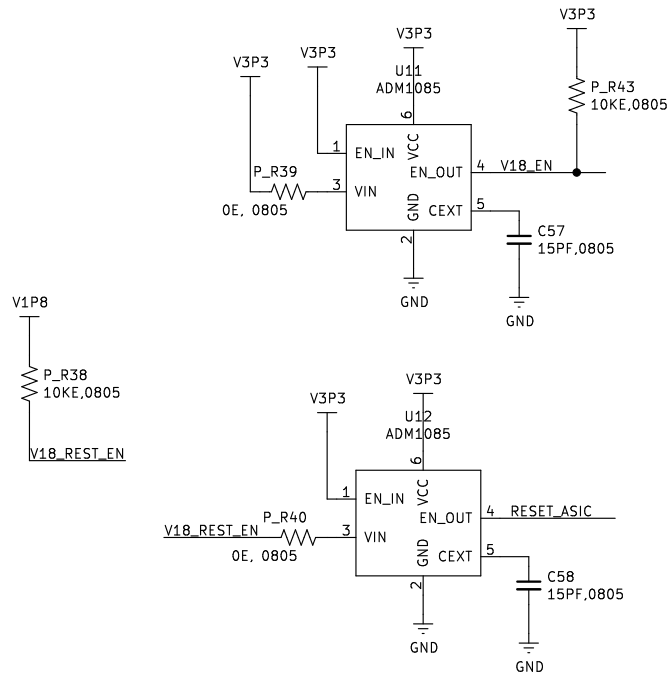
# LDO FOR V3P3 POWER RAIL



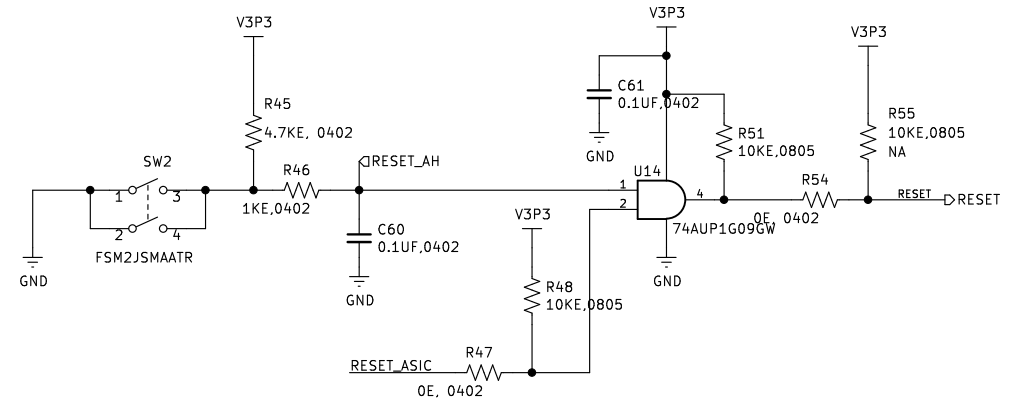
# LDO FOR V1P8 POWER RAIL



# VOLTAGE SEQUENCER



# RESET FOR ASIC



IIT\_MADRAS

Sheet: /power\_2/  
File: power\_2.sch

Title: AARDONYX\_DEVELOPMENT\_BOARD

Size: A4  
KiCad E.D.A. kicad (5.1.2)-2

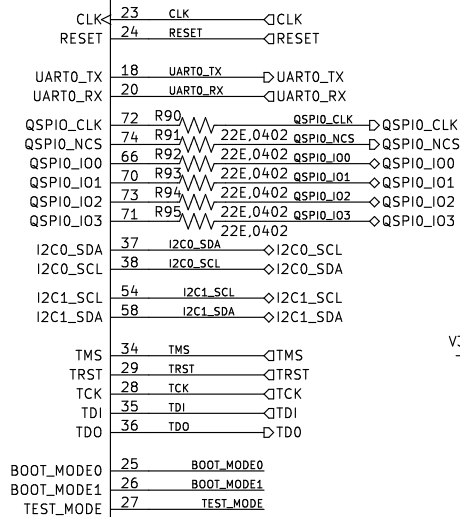
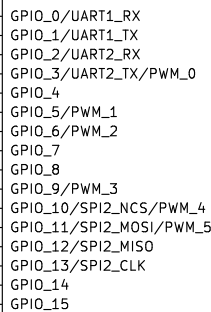
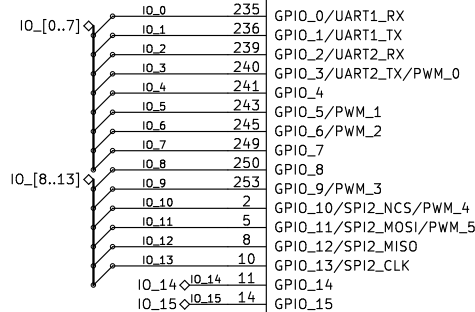
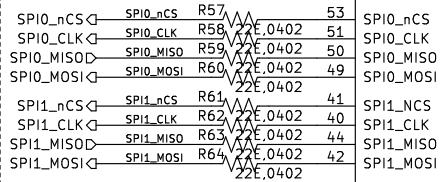
Date:

Rev: R1V0

Id: 6/7

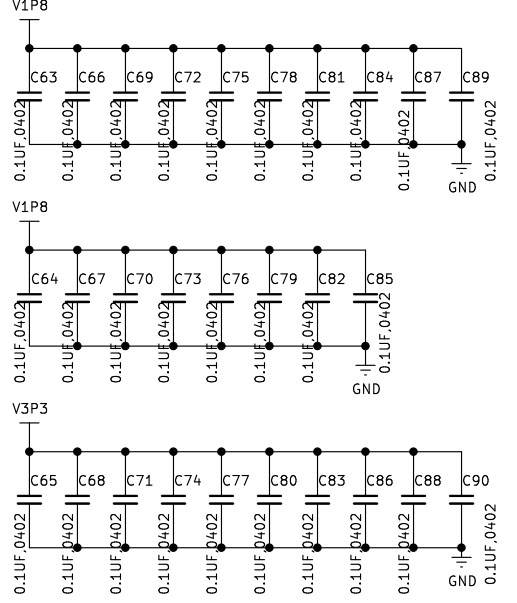
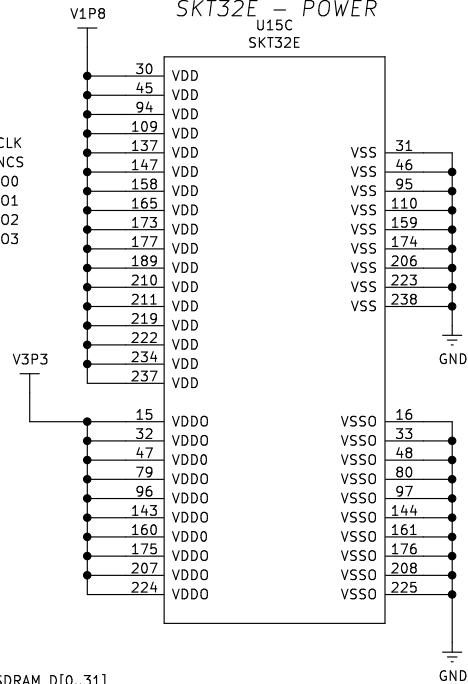
# SKT32E - PERIPHERALS

U15A  
SKT32E



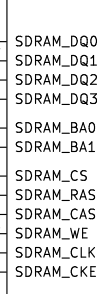
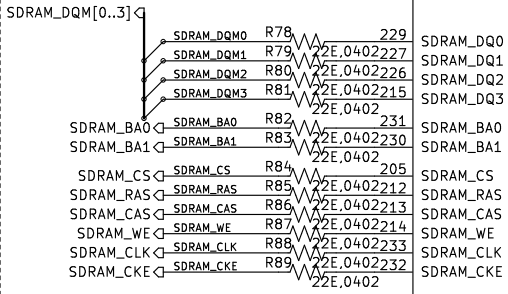
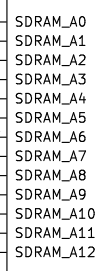
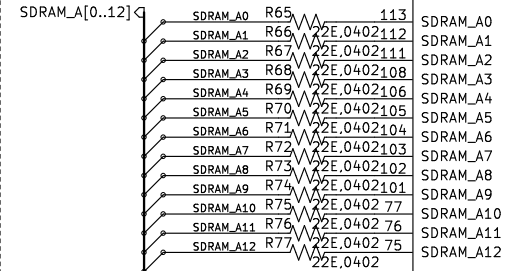
# SKT32E - POWER

U15C  
SKT32E

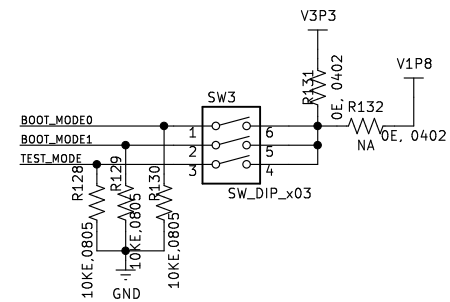


U15B  
SKT32E

# SKT32E - SDRAM



# MODE SELECT SWITCH



# IIT\_MADRAS

Sheet: /soc/  
File: soc.sch

# Title: AARDONYX\_DEVELOPMENT\_BOARD

Size: A4  
KiCad E.D.A. kicad (5.1.2)-2

Date:  
Rev: R1V0  
Id: 7/7