STATE BOARD OF TECHNICAL EDUCATION, BIHAR

Scheme of Teaching and Examinations for

IV SEMESTER DIPLOMA IN ELECTRONICS ENGINEERING

(Effective from Session 2016-17 Batch)

THEORY

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME			EXAMI	NATION - SCH	EME			
			Periods per Week	Hours of Exam.	Teacher's Assessment (TA) Marks (A)	Class Test(CT) Marks (B)	End Semester Exam. (ESE) Marks (C)	Total Marks (A+B+C)	Pass Marks ESE	Pass Marks in the Subject	Credits
1.	Digital Electronics– I	1621401	04	03	10	20	70	100	28	40	04
2.	Electronics Components and Materials	1621402	03	03	10	20	70	100	28	40	03
3.	Advance Electronic Devices and Circuits	1621403	04	03	10	20	70	100	28	40	04
4.	Network and Lines	1621404	03	03	10	20	70	100	28	40	03
5.	Basic Comm. Techniques & Sound Engineering	1621405	03	03	10	20	70	100	28	40	03
		Total	:- 17				350	500			

PRACTICAL

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME		EXA	MINATION -	SCHEME		
			Periods per	Hours	Practica	al (ESE)	Total	Pass Marks	Credits
			Week	of Exam.	Internal (A)	External (B)	Marks (A+B)	in the Subject	
6.	Electronic Construction and Repair Lab.	1621406	06	03	30	70	100	40	03
		Tota	al:- 06				100		

TERM WORK

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME		EXAMINATION	XAMINATION – SCHEME			
			Periods per week	Marks of Internal Examiner (X)	Marks of External Examiner (Y)	Total Marks (X+Y)	Pass Marks in the Subject	Credits	
7.	Electronics Circuit Lab.	1621407	05	30	70	100	40	02	
8.	Digital Electronics Lab.	1621408	05	15	35	50	20	02	
		To	tal:- 10		1	150			
Tot	al Periods per week Eac	ch of duratio	n One Hours	= 33		Total	Marks = 750	24	

DIGITAL ELECTRONICS - I

		Theory		No of Period in one	sessio	n: 50	Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
	L	T	P/S	ESE	:	70	04
1621401	04	_	_	TA	:	10	04
				CT	:	20	

Rationale:-

Digital System has made great in roads in the field of Electronics. The use of Digital Circuits is rapidly increasing in all most all the electronic applications, to be it microprocessors, Computers, Communications, Measuring instruments and others.

Objectives:-

This paper is to deal with the basics of Digital System. The students are expected to learn the Binary System, Conversions from one System to another, the various Logic Circuits, Digital ICs and connected basic Digital Circuits used in Electronic field.

	Contents: Theory	Hrs/week	Mark
Unit -1	Binary System 01.01 Transistor in cut off and saturation. 01.02 Binary Numbers. 01.03 Number Base Conversion. 01.04 Hexadecimal. 01.05 Compliments: 1's, 2's, 9's and 10's compliments, its application, signed binary number, weighted and non-weighted codes. 01.06 Codes: Weighted and non-weighted codes.	[08]	
Unit -2	Boolean Algebra and Logic Gates 02.01 Basic Definition of Boolean Algebra, Axioms of Boolean Algebra. 02.02 Basic theorem and properties of Boolean Algebra. 02.03 Boolean functions, Canonical and standard forms. 02.04 Logic Gates, Universal logic gates and its application.	[06]	
Unit -3	Simplification of Boolean Function 03.01 Theorem and K-map methods up to variables. 03.02 Product of sum and sum of product simplification. 03.03 NAND and NOR implementation. 03.04 Don't care conditions.	[05]	
Unit -4	Digital Integrated Circuits 04.01 Introduction to following: RTL, DTL, TTL, ECL, MOS, CMOS, Transmission gate circuits.	[06]	
Unit -5	Combinational Logic 05.01 Half Adder, Full Adder. 05.02 Half and Full Subtractor. 05.03 Code Conversion. 05.04 Binary Adder and Subtractor. 05.05 Magnitude Comparator. 05.06 Decoder and Encoder. 05.07 Multiplexer and Demultiplexer.	[12]	
Unit -6	Multi-vibrator and Synchronous Sequential Logic 06.01 Transistor/IC based multivibrator circutes. 06.02 Flip Flop (RS, JK, T, D, Master Slave type) 06.03 Triggering of flip flops.	[06]	
Unit -7	Shift Registers and Counters 07.01 Registers. 07.02 Shift Registers using different types of flip flops. 07.03 Ripple Counter, Synchronous and Asynchronous counter.	[07]	
	Total	50	

Reference Books:-

1	Digital Design	-	Maho
2	Design Principle Application	-	Malvino and Mano
3	Digital Computer System	-	Malvino
4	Digital Circuits and Logic Design	-	Lee

ELECTRONIC COMPONENTS AND MATERIALS

		Theory		No of Period in one	e sessio	n:50	Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
1621402	L	T	P/S	ESE	:	70	02
1621402	03	_	_	TA	:	10	03
				CT	:	20	

This subject is being introduced in the Electronics/Electronics and Tele-communication diploma technical programme to prepare a strong base for the students to understand the subjects of electronics that they will have to come across in their higher stage of learning.

The topics and sub-topics are being included which will help the students to:

- Know the characteristics of different electronic components and materials.
- Understand their principles, characteristics, functions and use.
- Develop skill to apply the knowledge in proper selection and use of the electronic components and materials.
- Able to distinguish different types of resistors, capacitors etc. through their color codes.
- Understand the principle of soldering.

	Contents : Theory	Hrs/week	Marks
Unit -1	Resistor 01.01 Characteristics 01.02 Classification: Fixed resistors, Metal film, Carbon film, Wire wound, Variable resistors, Rheostat, Chip resistors, Thermistors, and Varistors. 01.03 Color coding (with simple problem)	[11]	
Unit -2	Capacitors 02.01 General description and characteristics of capacitor 02.02 Classification: Fixed capacitors, Mica of capacitors, Paper capacitors, Plastic film capacitors, Ceramic glass capacitors, and Electrolytic capacitors. 02.03 Color Coding.	[09]	
Unit -3	Transformer and Chokes 03.01 Applications and general principles of operation of transformer. 03.02 Types of magnetic circuits. 03.03 Materials for cores and manufacturing of stacked cores.	[05]	
Unit -4	Induction Coils 04.01 Classification and characteristics. Types of Core. 04.02 Inductance of a coil. 04.03 Methods used to decrease Skin Effect. 04.04 Eddy Current Loss. 04.05 Dielectric loss and distributed capacitances in coils.	[06]	
Unit -5	P.C.B. Construction Materials 05.01 Base Materials. 05.02 Metal Foil. 05.03 Types of Boards. 05.04 Methods of Fabrications. Taping materials	[13]	
Unit -6	Electronic packaging parts	[06]	
	Total	50	

1	Radio Circuit Construction, Mir Publication.	-	A.T. Belesvtsev
2	Hand Book for Electronic Engineering Technician	-	Milt Mafman and Arthur H. Seedman
3	Electronic Assembly and Fabrication	-	Goshan J. Wheeler

ADVANCE ELECTRONIC DEVICES AND CIRCUITS

		Theory		No of Period in one	sessio	n: 60	Credits
Subject Code	No.	of Periods Per V	Week	Full Marks	:	100	
1621403	L	T	P/S	ESE	:	70	04
1621403	04	_	_	TA	:	10	04
				CT	:	20	

Rationale

This paper is meant to make the students familiar with widely used IC chips and the solid state devices such as FETs. The utility of Electronic Devices depends on circuits. Students are to study amplifier and oscillator circuits of different type meant for various applications and specific uses.

	Contents: Theory	Hrs/week	Mark
Unit -1	Transistor Biasing	[07]	
	01.01 Introduction Transistor, basic operation.		
	01.02 Output Characteristics of CE Amplifier.		
	01.03 Operating Point. 01.04 Bias Stability.		
	01.04 Blas Stability. 01.05 Types of Biasing.		
	01.05 Types of Biasing. 01.06 Bias Compensation.		
	01.00 Bias Compensation. 01.07 Thermal Sunway.		
	01.07 Thermal Sunway.		
Unit -2	Transistor as Amplifier	[07]	
-	02.01 Hybrid Circuits	[~,]	
	02.02 Z, Y & h Parameters of Two Port Networks.		
	02.03 Equivalent Circuit of Transistor at low and medium frequencies.		
	02.04 Analysis of voltage gains, current gain, power gain, input impedance and		
	output impedance for h-parameter.		
Unit -3	Coupled Amplifiers	[09]	
Omt -3	03.01 Cascading of Amplifier.	[07]	
	03.02 Principles of R-C, D-C and Transformer Coupling.		
	03.03 Gain Bandwidth consideration.		
	03.04 Effects of coupling on amplifier performance.		
	03.05 Changes in frequency response and due to effects on coupling.		
	03.06 High frequency considerations.		
	03.07 Compensation of amplifier for high and low frequency tuned circuit.		
TT. 4 4		[10]	
Unit -4	Feed-Back Amplifiers 04.01 Classification concept.	[10]	
	04.01 Classification concept. 04.02 Gain with feedback, input resistance, B.W.		
	04.02 Gain with reedback, input resistance, B. w. 04.03 Current Series and Current Shunt Feedback Circuits.		
	04.04 Voltage Series and Voltage Shunt Feedback Circuits.04.05 Voltage Shunt Feedback Circuits with Frequency Response.		
		54.07	
Unit -5	Oscillators 05.01 Principle of Oscillators.	[10]	
	05.02 Effect of feedback on Amplifier Bandwidth.		
	05.03 Gain and Phase Margin.		
	05.04 Wein Bridge Oscillator (Basic idea).		
	05.05 Crystal Oscillator.		
	05.06 Frequency Stability.		
Unit -6	FET Amplifiers	[09]	
	06.01 Construction and operation of FETs biasing.		
	06.02 Parameters of FETs, MosFET, D.MosET, E-MosFET biasing, JFET		
	amplifier.		
	06.03 UJT, equivalent circuit of UJT, characteristics and its application.		

Unit -7	Operational Amplifiers	[80]	
	07.01 Basic Operational Amplifier (OP-AMP).	[]	
	07.02 Differential Amplifier.		
	07.03 Operational Amplifier Parameters.		
	07.04 Parameters Measurement.		
	07.05 Basic Circuits: Subtractor, Adder, Integrator, Differentiator circuits using		
	Operational Amplifier (OP-AMP).		
	Total	60	

1	Integrated Electronics	-	Millman and Halkias
2	Electronics Devices and Circuits	-	John D. Ryder
3	Electronics Devices and Circuits	-	Millman and Halkias
4	Linear Integrated Circuits		Byan
5	Principle of electronics		V.K Mehta
6	Basic electronics		B.L. Thereja

NETWORK AND LINES

		Theory		No of Period in one	Credits		
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
· ·	L	T	P/S	ESE	:	70	03
1621404	03	_	_	TA	:	10	03
				CT	:	20	

Rationale:

	Contents: Theory	Hrs/wee	Marl
Unit -1	Network Parameters	[07]	
	01.01 Active and Passive Elements.		
	01.02 Linear and non-linear elements.		
	01.03 Unilateral and Bilateral Elements.		
	01.04 Lumped and Distributed Elements.		
	01.05 Ideal and Practical Voltage and Current Sources.		
	01.06 Concept of Nodes, Mesh, Branch, Loop etc.		
Unit -2	Two Port Network	[12]	
	02.01 Introduction to Z, Y and ABCD parameters.		
	02.02 Equivalent Circuits in Z, Y, ABCD, h parameters.		
	02.03 Transfer function, Concept and Calculation for two port network.		
	02.04 Four Terminal Networks.		
	02.05 Symmetrical and Asymmetrical Networks.		
	02.06 Image and Iterative Impedance.		
	02.07 Design of Simple Symmetrical and Asymmetrical networks.		
	02.08 Propagation Constant.		
	02.09 T and Pai Network.		
	02.10 Conversion of T to л N/W and л to T N/W.		
	02.11 Ladder and Lattice Network.		
Unit -3	Attenuator and Equalizers	[04]	
	03.01 Symmetrical and Asymmetrical Networks.	. ,	
	03.02 Design of T and Pai type attenuators.		
	03.03 Equalizers - Introduction.		
Unit -4	Filters	[08]	
	04.01 Concept of Decibel and Neper.	[oo]	
	04.02 Basic Relations in Filters.		
	04.03 Classification as per use: Low Pass Filters, High Pass Filters, Band Pass Filters and		
	Band Stop Filters.		
	04.04 Attenuation and phase shift characteristics.		
	04.05 Design of simple T and Pai type in derived filters.		
Unit -5	Transmission Lines	[19]	
	05.01 Classification.	[17]	
	05.02 Introduction to open wire, co-axial cable, wave guide, optical fibers with application.		
	05.03 Distributed parameters of lines.		
	05.04 Equivalent Circuit of a finite line.		
	05.05 T and Pai type representation of a section of line.		
	05.06 Voltage and Current distribution in an infinite line.		
	05.07 Characteristics impedance and propagation constant of Transmission line.		
	05.08 Concept of propagation, attenuation constant and phase shift constant of a line.		
	05.09 Expression for impedance at a point on line.		
	05.10 Reflected and standing waves.		
	05.11 Voltage reflection coefficient and VSWR.		
	05.12 Maximum and Minimum impedance.		
	05.13 Input and Output impedance of an open and short-circuited loss-less line.		
	05.14 Input impedance as a function of length of line.		
	05.14 Imput impedance as a runction of Tength of Time. 05.15 Introduction to Smith Chart and Circle Diagrams.		
	Total	50	

BASIC COMMUNICATION TECHNIQUES AND SOUND ENGINEERING

		Theory		No of Period in one session: 50			Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
Subject Code 1621405	L		P/S	ESE	:	70	03
1621405	03		_	TA	:	10	03
				CT	:	20	

Rationale

The basis of communication techniques and a working knowledge of the principles of Acoustics are felt fit to be imparted at this stage.

Objectives

The students are expected to get familiar with the process of Modulation and detection, Sonar and the basic principles of Acoustics. The broad topics to covered are:

Contents : Theory	Hrs/week	Marks
Unit -1 Introduction to Communication System and Noise 01.01 Classification. 01.02 Introduction to Information and Noise and type of noise. 01.03 Introduction of basic elements of communication system, Transmitter channel, receiver.	[04]	
Unit -2 Modulation 02.01 AM, expression for AM wave power in carrier and sideband. 02.02 SSB and Vestigial Side Board Systems. 02.03 Frequency Modulation. 02.04 Phase Modulation, Noise.	[10]	
Unit -3 De-Modulation 03.01 Diode Transistor and FET Demodulation for AM waves. 03.02 Phase discriminators and ratio detectors for FM and PM waves.	[06]	
Unit -4 Pulse Code Modulation 04.01 Introduction. 04.02 Type of Pulse Code Modulation. 04.03 PWM, PPM, PCM, Multiplexing. 04.04 Time-Division Multiplexing and Frequency-Division Multiplexing. 04.05 Introduction to Radio Telemetry.	[13]	
Unit -5 Ultrasonic G/R 05.01 Detection and Application of Remote Control.	[04]	
Unit -6 Acoustics 06.01 Introduction to sound, ear audibility and stereo. 06.02 Recording and Reproduction, disc recording type of recorder 06.03 Reproducers, recording. 06.04 Hi-Fi and Stereophonic Systems. 06.05 Room Acoustics: Requirement of record room, acoustics room shape. Optimum reverberation in room, Absorbent materials, scale model tests designer considerations of open air theaters auditorium, commercial building sound recording.	,	
Total	50	

1	Electronics Communication System		Kemecy
2	Hi-Fi Stereo Hand Book		
3	Radio and TV	,	S.P. Sharma

ELECTRONIC CONSTRUCTION AND REPAIR LAB.

		Practical		No of Period in	one sess	ion :	Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks : 100		100	
1621406	L	T	P/S	ESE	:	100	03
1021400	_	_	06	Internal	:	30	
				External	:	70	

	Contents: Practical	Hrs/week	Marks		
Unit -1	Unit -1 Construction of a Battery Eliminator Box, Stabilizer Box, Radio and TV Cabinets.				
Unit -2	Soldering Practice: connecting circuit components.	[]			
Unit -3	Assembling Battery-Stabilizer, Radio Receiver, Intercoil Circuit.	[]			
Unit -4	Assembling Inverter.	[]			
Unit -5	 Location of faults and repair of: Battery Eliminator Voltage Stabilizer Inverter Radio Receiver 	[]			
Unit -6	Location of faults in different types of Electronics Circuits.	[]			
Unit -7	Tracing fault in a CRO and its repair.	[]			
Unit -8	Handling of different types of multimeter: VTVM, Frequency meters, Calculators.	[]			
Unit -9	Fault Location and repair of instruments - Multimeter VTVM, Frequency meters, Calculators.	[]			
Unit -10	Repair of faulty study panels of your laboratory.	[]			
	Total				

Note:- Three assignments for practical under SL 1 and 2. Two assignments for practical listed under SL 3 and 4, and at least one assignment for each of the practical under SL No. 5 to 10. Altogether eleven assignments to be done by the students in the workshop or laboratory.

1	-	
2	1	
3	-	

ELECTRONICS CIRCUIT LAB.

		Term Work		No of Period in one session :			Credits
Subject Code	No.	of Periods Per V	Full Marks	: 100			
1621407	L	T	P/S	Internal	:	30	02
	_	_	05	External	:	70	

	Contents: Term Work	Hrs/week	Marks
Unit -1	[]		
Unit -2	Measurement of h-parameter of transistor.	[]	
Unit -3	Frequency response of a CE amplifier.	[]	
Unit -4	Frequency response of direct-coupled amplifier.	[]	
Unit -5	Frequency response of RC-coupled amplifiers.	[]	
Unit -6	Characteristics of a transformer-coupled amplifier.	[]	
Unit -7	Calculation of gain, input impedance and output impedance in case of cascaded amplifiers.	[]	
Unit -8	Operation of Push-Pull amplifier.	[]	
Unit -9	Operation of Class C amplifier, operation of puss-pull class-B amplifier.	[]	
Unit -10	Characteristics Curves of FETs.	[]	
Unit -11	Operation of Wein Bridge and RC Phase shift oscillator.	[]	
Unit -12	Verification of basic operation of OP-AMP curves.	[]	
Unit -13	Use of OP-AMP as Adder and Subtractor.	[]	
Unit -14	Use of OP-AMP as integrator and differentiator.	[]	
	Total		

1	-	
2	1	
3	-	

DIGITAL ELECTRONICS LAB.

		Term Work		No of Period in o	ne sess	ion :	Credits
Subject Code	No.	of Periods Per V	Week	Full Marks	:	50	
1621408	L	T	P/S	Internal	:	15	02
	_	_	05	External	:	35	

	Contents : Term Work	Hrs/week	Marks
Unit -1	Construction and verification of diode OR gate.	[]	
Unit -2	Construction and verification of diode AND gate.	[]	
Unit -3	Verification of truth table of Basic Gates.	[]	
Unit -4	Verification of truth table of Universal Gates from ICs.	[]	
Unit -5	Construction of Basic gates from Universal Gates.	[]	
Unit -6	Construction of Ex-OR gate from Universal Gates.	[]	
Unit -7	Construction of Half Adder and Full adder circuit from Gates and Verification of its function.	[]	
Unit -8	Construction of Half and Full subtractor circuit from Universal Gates and Verification of its function.	[]	
Unit -9	Verification of truth table of R-S and J-K Flip Flop.	[]	
Unit -10	Operation of Transistor Multimeter circuits.	[]	
Unit -11	Operation of multivibrator functions from 555 IC.	[]	
Unit -12	Construction and verification of function of Ripple and BCD Counter.	[]	
Unit -13	Construction and verification of Sequence Generator.	[]	
	Total		

1	-	
2	1	
3	-	

STATE BOARD OF TECHNICAL EDUCATION, BIHAR Scheme of Teaching and Examinations for IV SEMESTER DIPLOMA IN INSTRUMENTATION AND CONTROL ENGINEERING.

(Effective from Session 2016-17 Batch)

THEORY

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME			EXAMI	INATION - SCI	HEME			
			Periods per Week	Hours of Exam.	Teacher's Assessment (TA) Marks (A)	Class Test(CT) Marks (B)	End Semester Exam. (ESE) Marks (C)		Pass Marks ESE	Pass Marks in the Subject	Credits
1.	Electrical Machine & Control	1640401	04	03	10	20	70	100	28	40	04
2.	Electronics Comp. & Materials	1621402	03	03	10	20	70	100	28	40	03
3.	Advance Electronic Devices & Circuits.	1621403	04	03	10	20	70	100	28	40	04
4.	Network and Lines	1621404	03	03	10	20	70	100	28	40	03
5.	Industrial & Automation	1640405	03	03	10	20	70	100	28	40	03
		To	tal:- 17				350	500			

PRACTICAL

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME Periods per Week	Hours of Exam.	EXA Practica Internal (A)	MINATION – al (ESE) External (B)	Total Marks (A+B)	Pass Marks in the Subject	Credits
6.	Electronics Construction And repair Lab	1621406	06	03	30	70	100	40	03
	Total:- 06 100								

TERM WORK

Sr. No.	SUBJECTS	SUBJECT CODE	TEACHING SCHEME		EXAMINATION	- SCHEME		
			Periods per week	Marks of Internal Examiner (X)	Marks of External Examiner (Y)	Total Marks (X+Y)	Pass Marks in the Subject	Credits
7.	Electronics Circuit Lab	1621407	05	30	70	100	40	02
8.	Digital Electronics Lab	1621408	05	15	35	50	20	02
		Tota	d:- 10			150		
Tot	tal Periods per week Eacl	n of duratio	n one Hours	= 33	·	Total	Marks = 750	24

ELECTRICAL MACHINE AND CONTROL

		Theory		No of Period in one	sessio	n: 50	Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
	L	T	P/S	ESE	:	70	04
1640401	04	_	_	TA	:	10	04
				CT	:	20	

RATIONALE:-

The students are well conversant with the electric and magnetic field and circuit, electro-magnetic induction, D.C. and A.C. circuits, based on related electric and magnetic theories. They also know about electrical components and materials, Now the Electrical Machine is being introduced for IVth Semester Diploma in Electrical & Electronics Engineering to impart the knowledge of D.C. & A.C. machines, which play vital roles even in this era of electronics in different industries throughout the world.

The topics of requisites and construction of D.C. machines, generators, D.C. & A.C. motors, converters, special motors and electro plating have been included in the content. Which will give full insight of electrical equipments in their practical life.

Topics have been divided into sub-topics in order to facilitate the students to understand the subject matters properly. Tentative no. of lectures have been allotted for each topic and sub-topic, so that the whole syllabus may be covered easily in the academic year.

OBJECTIVES:-

The thorough study of these topics will enable the students know fully about D.C. machines, their operation, maintenance and proper connection and hence will enable him to work as a good supervisor and also to efficiently monitor the works of operators under him. The topics of special motors used and that of electroplating will provide full insight of practical use of electrical equipments.

		Contents : Theory	Hrs/week	Marks
Unit-1	Requi	sites and Construction of D.C. Machines	[15]	
	01.01	Armature winding: Pole-pitch, Conductor coil and winding		
		elements, coil span, coil-pitch, pitch of winding, back pitch, front		
		pitch, resultant pitch, commutator pitch.		
	01.02	Single layer winding, lap and wave winding, use of lap and wave		
		windings.		
Unit-2	D.C. (<u>Generator</u>	[15]	
	02.01	Types of generator, E.M.F. equation of generator.		
	02.02	Losses and efficiency of a generator, condition for maximum		
		efficiency.		
	02.03	Generator characteristics: No load curve of self-excited generator,		
		How to find critical resistance, How to draw O.C.C. at different		
		speeds, critical speed, voltage build-up of a shunt generator,		
		condition for voltage build-up.		

Unit-3	D.C. Motor	[15]	
	03.01 Significance of back E.M.F. voltage equation of a motor, armature		
	torque, shaft torque.		
	03.02 Characteristics of series shunt and compound motors.		
	03.03 Losses and efficiencies of a motor.		
	03.04 Speed control of a D.C. motor: Speed control of a series motor,		
	speed control of a shunt motor, merits and demerits of a rheostatic control		
	method, series. Parallel control.		
	Simple problems.		
	03.05 Testing of D.C. Motors: NO-load test (Swin Burne's test) of D.C.		
	shunt motor, back to back test (Hopkinson's test), retardation test of a series motor.		
	03.06 Necessity of a starter : Shunt motor starter: 3-point starter, 4-point starter.		
Unit-4	A.C Motor	[05]	
	04.01 Speed control of induction motors : control from startor side, control		
	from rotor side.		
	04.02 Direct On-Line starter, star-Delta starter and Autotransformer starter.		
	Total	50	

Sl.No	Title/Publisher	Author
1.	Theory of Direct Current Machinery, TMN editions	Alexander S.Langsdorf
2.	A text-Book of Electrical Technology, Vol-II	B.L. Theraja
3.	Electrical Machinery, Khanna Publications	P.S. Pimbhra

ELECTRONIC COMPONENTS AND MATERIALS

		Theory		No of Period in one	e sessio	n: 50	Credits
Subject Code	No.	of Periods Per V	Veek	Full Marks	:	100	
•	L	T	P/S	ESE	:	70	03
1621402	03	_	_	TA	:	10	03
				CT	:	20	

This subject is being introduced in the Electronics/Electronics and Tele-communication diploma technical programme to prepare a strong base for the students to understand the subjects of electronics that they will have to come across in their higher stage of learning.

The topics and sub-topics are being included which will help the students to:

- Know the characteristics of different electronic components and materials.
- Understand their principles, characteristics, functions and use.
- Develop skill to apply the knowledge in proper selection and use of the electronic components and materials.
- Able to distinguish different types of resistors, capacitors etc. through their color codes.
- Understand the principle of soldering.

	01.01 Characteristics 01.02 Classification: Fixed resistors, Metal film, Carbon film, Wire wound, Variable resistors, Rheostat, Chip resistors, Thermistors, and Varistors. 01.03 Color coding (with simple problem) Capacitors 02.01 General description and characteristics. 02.02 Classification: Fixed capacitors, Mica capacitors, Paper capacitors, Plastic film capacitors, Ceramic glass capacitors, and Electrolytic capacitors. 02.03 Color Coding. Transformer and Chokes 03.01 Applications and general principles of operation. 03.02 Types of magnetic circuits. 03.03 Materials for cores and manufacturing of stacked cores. Induction Coils 04.01 Classification and characteristics. Types of Core. 04.02 G of a Coil. 04.03 Methods used to decrease Skin Effect. 04.04 Eddy Current Loss. 04.05 Dielectric loss and distributed capacitances in coils. Induction Coils 05.01 Base Materials. 05.02 Metal Foil. 05.03 Types of Boards. 05.04 Methods of Fabrications. Taping materials Electronic packaging parts		Hrs/week	Marks
Unit-1	Resistor		[11]	
	01.01	Characteristics		
	01.02	Classification: Fixed resistors, Metal film, Carbon film, Wire wound,		
		Variable resistors, Rheostat, Chip resistors, Thermistors, and Varistors.		
	01.03	Color coding (with simple problem)		
Unit-2	Capacito	<u>ors</u>	[09]	
	02.01	General description and characteristics.		
	02.02	Classification: Fixed capacitors, Mica capacitors, Paper capacitors,		
		Plastic film capacitors, Ceramic glass capacitors, and Electrolytic		
	02.03	Color Coding.		
Unit-3	Transfo	rmer and Chokes	[05]	
	03.01	Applications and general principles of operation.		
	03.02	Types of magnetic circuits.		
	03.03	Materials for cores and manufacturing of stacked cores.		
Unit-4	Inductio	on Coils	[06]	
	04.01	Classification and characteristics. Types of Core.		
	04.02	G of a Coil.		
	04.03	Methods used to decrease Skin Effect.		
	04.04	Eddy Current Loss.		
	04.05	Dielectric loss and distributed capacitances in coils.		
Unit-5	P.C.B. C	Construction Materials	[13]	
	05.01	Base Materials.		
	05.02	Metal Foil.		
	05.03	Types of Boards.		
	05.04	Methods of Fabrications. Taping materials		
Unit-6	Electron	nic packaging parts	[06]	
		Total-	50	

Recommended Books:-

Sl.No. Title/Publisher

- 1. Radio Circuit Construction, Mir Publication.
- 2. Hand Book for Electronic Engineering Technician Seedman
- 3. Electronic Assembly and Fabrication

Author

- A.T. Belesvtsev
- Milt Mafman and Arthur H.
- Goshan J. Wheeler

ADVANCE ELECTRONIC DEVICES AND CIRCUITS

	Theory			No of Period in one session: 60			Credits
Subject Code	No. of Periods Per Week			Full Marks	:	100	
	L	T	P/S	ESE	:	70	0.4
1621403	04	_	_	TA	:	10	04
				CT	:	20	

RATIONALE:-

This paper is meant to make the students familiar with widely used IC chips and the solid state devices such as FETS.

The utility of Electronic Devices depends on circuits. Students are to study amplifier and oscillator circuits of different type meant for various applications and specific uses.

The topics to be covered are:

	Contents : Theory	Hrs/week	Marks
Unit-1	Transistor Biasing	[07]	
	01.01 Output Characteristics of CE Amplifier.		
	01.02 Operating Point.		
	01.03 Bias Stability.		
	01.04 Types of Biasing.		
	01.05 Bias Compensation.		
	01.06 Thermal Sunway.		
Unit-2	Transistor as Amplifier	[07]	
	02.01 Hybrid Circuits.		
	02.02 Z, Y & H Parameters of Two Port Networks.		
	02.03 Equivalent Circuit of Transistor at low and medium frequencies.		
	02.04 Analysis of voltage gains, current gain, power gain, input		
	impedance and output		
Unit-3	Coupled Amplifiers	[09]	
	03.01 Cascading of Amplifier Types.		
	03.02 Principles of R-C, D-D and Transformer Coupling.		
	03.03 Gain Bandwidth consideration.		
	03.04 Effects of coupling on amplifier performance.		
	03.05 Changes in frequency response and due to effects on coupling.		
	03.06 High frequency considerations.		
	03.07 Compensation of amplifier for high and low frequency tuned		
	circuit.		
Unit-4	Feed-Back Amplifiers	[10]	
	04.01 Classification concept.		
	04.02 Gain with feedback, input resistance, type of resistance.		
	04.03 Current Series and Current Shunt Feedback Circuits.		
	04.04 Voltage Series and Voltage Shunt Feedback Circuits.		
	04.05 Voltage Shunt Feedback Circuits with Frequency Response.		
Unit-5	<u>Oscillators</u>	[10]	
	05.01 Principle of Oscillators.		
	05.02 Effect of feedback on Amplifier Bandwidth.		
	05.03 Gain and Phase Margin.		
	05.04 Wein Bridge Oscillator (Basic idea).		
	05.05 Crystal Oscillator.		
	05.06 Frequency Stability.		

Unit-6	FET Amplifiers	[09]	
	06.01 Biasing of FETs.		
	06.02 CS, CD, CG amplifiers with equivalent circuits analysis and		
	frequency response.		
	06.03 Biasing of UJT.		
Unit-7	Operational Amplifiers	[08]	
	07.01 Basic Operational Amplifier (OP-AMP).		
	07.02 Differential Amplifier.		
	07.03 Operational Amplifier Parameters.		
	07.04 Parameters Measurement.		
	07.05 Basic Circuits: Subtractor, Adder, Integrator, Differentiator		
	circuits using Operational Amplifier (OP-AMP).		
	Total	60	

Reference Books:-

Sl.No.	Title/Publisher		Author
1.	Integrated Electronics	-	Millman and Halkias
2.	Electronics Devices and Circuits	-	John D. Ryder
3.	Electronics Devices and Circuits	-	Millman and Halkias
4.	Linear Integrated Circuits	-	Byan
5.	Principle of electronics	-	V.K Mehta
6.	Basic electronics	-	B.L. Thereja

NETWORK AND LINES

	Theory			No of Period in one session: 50			Credits
Subject Code	No. of Periods Per Week			Full Marks	:	100	
	L	T	P/S	ESE	:	70	03
1621404	03	_	_	TA	:	10	03
				CT	:	20]

RATIONALE:-Objectives

The topics to be covered are:-

	Contents : Theory	Hrs/week	Marks
Unit-1	Network Parameters	[07]	
	01.01 Active and Passive Elements.		
	01.02 Linear and non-linear elements.		
	01.03 Unilateral and Bilateral Elements.		
	01.04 Lumped and Distributed Elements.		
	01.05 Ideal and Practical Voltage and Current Sources.		
	01.06 Concept of Nodes, Mesh, Branch, Loop etc.		
Unit-2	Two Port Network	[12]	
	02.01 Introduction to Z, Y and ABCD parameters.		
	02.02 Equivalent Circuits in Z, Y, ABCD, h parameters.		
	02.03 Transfer function, Concept and Calculation for two port		
	network.		
	02.04 Four Terminal Networks.		
	02.05 Symmetrical and Asymmetrical Networks.		
	02.06 Image and Iterative Impedance.		
	02.07 Design of Simple Symmetrical and Asymmetrical networks.		
	02.08 Propagation Constant.		
	02.09 T and Pai Network.		
	02.10 T to Pai to T network transformation.		
	02.11 Ladder and Lattice Network.		
Unit-3	Attenuator and Equalizers	[04]	
	03.01 Symmetrical and Asymmetrical Networks.		
	03.02 Design of T and Pai type attenuators.		
	03.03 Equalizers - Introduction.		
Unit-4	<u>Filters</u>	[08]	
	04.01 Concept of Decibel and Neper.		
	04.02 Basic Relations in Filters.		
	04.03 Classification as per use: Low Pass Filters, High Pass Filters,		
	Band Pass Filters and Band Stop Filters.		
	04.04 Attenuation and phase shift characteristics.		
	04.05 Design of simple T and Pai type in derived filters.		
Unit-5	Transmission Lines	[19]	
	05.01 Classification.		
	05.02 Introduction to open wire, co-axial cable, wave guide, optical		
	fibers with application.		
	05.03 Distributed parameters of lines.		
	05.04 Equivalent Circuit of a finite line.		
	05.05 T and Pai type representation of a section of line.		
	05.06 Voltage and Current distribution in an infinite line.		

05.07	Characteristics impedance a TX line.		
05.08	Concept of propagation, attenuation constant and phase shift		
	constant of a line.		
05.09	Expression for impedance at a point on line.		
05.10	Reflected and standing waves.		
05.11	Voltage reflection coefficient and VSWR.		
05.12	Maximum and Minimum impedance.		
05.13	Input and Output impedance of an open and short-circuited loss-		
	less line.		
05.14	Input impedance as a function of length of line.		
05.15	Introduction to Smith Chart and Circle Diagrams.		
	Total	50	

Recommended Books:-

Sl.No. Title/Publisher

Author

INDUSTRIAL AUTOMATION

Subject Code	Theory					Credits	
1640405	No. of Periods Per Week			Full Marks	:	100	
1040403	L	T	P/S	ESE	:	70	03
	03	_	_	TA	:	10	03
	_	_	_	CT	:	20	

	CONTENTS: THEORY	Hours	Marks	
	Automation			
Unit-1	1.1 Need of automation			
ome 1	1.2 Advantages of automation	02		
	1.3 Requirements of automation			
	Control System			
	2.1 Concept of control system			
	2.2 Basic block diagram of control system			
	2.3 Transfer function			
Unit-2	2.4 Different terms in control system	04	04	
	2.5 Types of control system			
	2.6 Applications of control system			
	2.7 Development of block diagram for simple applications like level,			
	temperature, flow control			
	Control System Components			
	3.1 Contacts-types, current capacity & load utilization categories			
	3.2 Solenoids-dc, ac			
	3.3 I/P devices- switches-push buttons, foot switch, selector switch, pilot			
	switch, proximity, photoelectric, temperature actuated, level control,			
Unit-3	pressure sensing, overload sensing	08	12	
	3.4 Relays- electromechanical, reed		12	
	3.5 O/P devices- contactors, valves, pilot lamps			
	3.6 Symbols in power & control circuits			
	3.7 Developing control circuit-basic & thumb rule			
	3.8 Power & control circuit for different applications like hoist, crane,			
	conveyer belt, induction motors			
	Electrical Actuators			
	4.1 Potentiometers-working & use as error detector			
	4.2 Servomotors-ac & dc –working principle			
Unit-4	4.3 Synchros - transmitter, control transformer, use of as error detector	08	10	
	4.4 Stepper motor-PM & variable reluctance- working principle			
	4.5 Tacho - generator			
	4.6 Applications of above components as AC/DC control system.			
	Controllers			
	5.1 Hydraulic-advantages & disadvantages, hydraulic servomotor, types of			
	pumps used, control valves, components like accumulator, filter, seals			
	5.2 Pneumatic-resistance & capacitance of pressure system,			
** 1. =	pneumatic flapper-nozzle system, pneumatic relays,	0.0	4.0	
Unit-5	actuating valves, cylinders, comparison between pneumatic & hydraulic	08	10	
	systems			
	5.3 Electrical & electronic controller-brief overview of op-amps, inverting,			
	non-inverting, lead-lag networks			
	5.4 Digital controllers-brief overview of microprocessor & micro- controller			
	to be worked as controller			
	Control actions			
Unit-6	6.1 On-Off, P, I, P+I, P+D,P+I+D, actions	06	10	
-	6.2 P+I+D action using hydraulic, pneumatic electronic controller			
	6.3 Tuning of P+I+D controller			

Unit-7	Programmable Logic Controller 7.1 Introduction 7.2 Advantages & disadvantages 7.3 PLC Vs PC 7.4 Block diagram of PLC 7.5 Basic blocks like CPU, I/O modules, bus system, power supplies & remote I/Os	08	10
Unit-8	7.6 Different PLC's available in market Programming of PLC 8.1 development of Ladder logic 8.2 some simple programs such as I/O connections, starting of IM, stepper	02	10
onit-o	motor control (treatment to topic no.8.2 should be given at the time of practical / pp hours.)	02	10
Unit-9	Introduction to special control systems 9.1 Distributed Control System(DCS)-brief introduction to hardware & software used 9.2 SCADA- brief introduction to hardware & software used	02	04
	Total	48	70

Name of Authors	Titles of the Book	Name of the Publisher		
Nagrath Gopal	Control System Engg.	Wiley Eastern		
K.Ogata	Modern Control Engg.	Prentice Hall		
Jacob	Industrial Control Engg	Prentice Hall		
Andrew Parr	Hydraulics & Pneumatics	Jaico Publication		
Webb & Reis	Programmable Logic Controller: Principle applications	Wiley Eastern		
S.K. Bhattachrya Brijinder Singh	Control of Electrical Machines	New Age International		
Jon stenerson	Industrial automation and process control	Prentice Hall		
Richad Shell	Handbook of Industrial automation	Taylor and Francis		

ELECTRONIC CONSTRUCTION AND REPAIR LAB.

Subject Code 1621406		Practical			No of Period in one session :		
	No.	No. of Periods Per Week			:	100	
	L	T	P/S	ESE	:	100	03
	_	_	06	Internal	:	30	03
				External	:	70	

	Contents : Practical	Hrs/week	Marks
Unit-1	Construction of a Battery Eliminator Box, Stabilizer Box, Radio and TV Cabinets.	[]	
Unit-2	Soldering Practice: connecting circuit components.	[]	
Unit-3	Assembling Battery-Stabilizer, Radio Receiver, Intercoil Circuit.	[]	
Unit-4	Assembling Inverter.	[]	
Unit-5	 Location of faults and repair of:- Battery Eliminator Voltage Stabilizer Inverter Radio Receiver 	[]	
Unit-6.	Location of faults in different types of Electronics Circuits.	[]	
Unit-7	Tracing fault in a C.H.O. and its repair.	[]	
Unit-8	Handling of different types of multimeter: VTVM, Frequency meters, Calculators.	[]	
Unit-9	Fault Location and repair of instruments - Multimeter VTVM, Frequency meters, Calculators.	[]	
Unit-10	Repair of faulty study panels of your laboratory.	[]	
	Total		

Note: Three assignments for practical under SL 1 and 2. Two assignments for practical listed under SL 3 and 4, and at least one assignment for each of the practical under SL No. 5 to 10. Altogether eleven assignments to be done by the students in the workshop or laboratory.

ELECTRONICS CIRCUIT LAB.

		Term Work			No of Period in one session :		
Subject Code	No.	of Periods Per V	Week	Full Marks	Full Marks : 100		
1621407	L	T	P/S	Internal	:	30	02
	_	_	05	External	:	70	

	Contents : Term Work	Hrs/week	Marks
Unit-1	Introduction to various meters and instruments to be used.	[]	
	 Study of CRO; Phase and Frequency measurement. 		
Unit-2	Measurement of h-parameter of transistor.	[]	
Unit-3	Frequency response of a CE amplifier.	[]	
Unit-4	Frequency response of direct-coupled amplifier.	[]	
Unit-5	Frequency response of RC-coupled amplifiers.	[]	
Unit-6	Characteristics of a transformer-coupled amplifier.	[]	
Unit-7	Calculation of gain, input impedance and output impedance in	[]	
	case of cascaded amplifiers.		
Unit-8	Operation of Push-Pull amplifier.	[]	
Unit-9	Operation of Class C amplifier.	[]	
Unit-10	Characteristics Curves of FETs.	[]	
Unit-11	Operation of Wein Bridge and RC Phase shift oscillator.	[]	
Unit-12	Verification of basic operation of OP-AMP curves.	[]	
Unit-13	Use of OP-AMP as Adder and Subtractor.	[]	
Unit-14	Use of OP-AMP as integrator and differentiator.	[]	
	Total		

DIGITAL ELECTRONICS LAB.

	Term Work			No of Period in one session :			Credits
Subject Code	No.	of Periods Per V	Full Marks	:	50		
· ·	L	T	P/S				02
1621408	_	_	05	Internal	:	15	02
				External	:	35	

	Contents : Term Work	Hrs/week	Marks
Unit-1	Construction and verification of diode OR gate.	[]	
Unit-2	Construction and verification of diode AND gate.	[]	
Unit-3	Verification of truth table of Basic Gates.	[]	
Unit-4	Verification of truth table of Universal Gates from ICs.	[]	
Unit-5	Construction of Basic gates from Universal Gates.	[]	
Unit-6	Construction of Ex-OR gate from Universal Gates.	[]	
Unit-7	Construction of Half Adder and Full adder circuit from Gates	[]	
	and Verification of its function.		
Unit-8	Construction of Half and Full subtractor circuit from Universal	[]	
	Gates and Verification of its function.		
Unit-9	Verification of truth table of R-S and J-K Flip Flop.	[]	
Unit-10	Operation of Transistor Multimeter circuits.	[]	
Unit-11	Operation of multivibrator functions from 555 IC.	[]	
Unit-12	Construction and verification of function of Ripple and BCD	[]	
	Counter.		
Unit-13	Construction and verification of Sequence Generator.	[]	
	Total		