Course Title:	Computer Organization and Architecture	
Course Number:	COE 608	
Semester/Year (e.g.F2016)		

Instructor:	Nagi Mekhiel
-------------	--------------

Assignment/Lab Number:	6
Assignment/Lab Title:	Complete CPU

Submission Date:	10/04/2019
Due Date:	10/04/2019

Student LAST Name	Student FIRST Name	Student Number	Section	Signature*
Dhillon	Rajveer	500777569	5	

## Reset Form

<sup>\*</sup>By signing above you attest that you have contributed to this written lab report and confirm that all work you have contributed to this lab report is your own work. Any suspicion of copying or plagiarism in this work will result in an investigation of Academic Misconduct and may result in a "0" on the work, an "F" in the course, or possibly more severe penalties, as well as a Disciplinary Notice on your academic record under the Student Code of Academic Conduct, which can be found online at: <a href="http://www.ryerson.ca/senate/current/pol60.pdf">http://www.ryerson.ca/senate/current/pol60.pdf</a>