

Rapport TP1

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Chapter 1

Ibsim

Après avoir recopié le fichier de configuration topologique et lancé le programme avec ce dernier, on peut observer que la sortie dans la console correspond exactement avec la figure 2 du sujet.

```
administrateur@vm-ubuntu-base:~/Bureau/ibsim/ibsim$ ibsim example.topo
parsing: example.topo
example.topo: parsed 10 lines
#####
Network simulator ready.
MaxNetNodes      = 2048
MaxNetSwitches   = 256
MaxNetPorts      = 13312
MaxLinearCap     = 30720
MaxMcastCap      = 1024
sim (inactive)> dump
# Net status - Wed Feb 14 14:45:28 2018

Ca 1 "workstation1"      nodeguid 100000 sysimgguid 100000
100001 [1] "router1"[1] lid 0 lmc 0 smlid 0 4x 2.5G Init/LinkUp

Ca 1 "workstation2"      nodeguid 100002 sysimgguid 100002
100003 [1] "router2"[2] lid 0 lmc 0 smlid 0 4x 2.5G Init/LinkUp

Switch 2 "router1"      nodeguid 200000 sysimgguid 200000
# linearcap 30720 FDBtop 0 portchange 1
200000 [0] "Sma Port"[0] lid 0 lmc 0 smlid 0 4x 2.5G Active/LinkUp
200000 [1] "workstation1"[1] 4x 2.5G Init/LinkUp
200000 [2] "router2"[1] 4x 2.5G Init/LinkUp

Switch 2 "router2"      nodeguid 200001 sysimgguid 200001
# linearcap 30720 FDBtop 0 portchange 1
200001 [0] "Sma Port"[0] lid 0 lmc 0 smlid 0 4x 2.5G Active/LinkUp
200001 [1] "router1"[2] 4x 2.5G Init/LinkUp
200001 [2] "workstation2"[1] 4x 2.5G Init/LinkUp
# dumped 4 nodes
sim (inactive)> █
```

Figure 1.1 – Sortie du programme avec dump

On peut par la suite rentrer de nouvelles commandes. La liste de ces dernières est disponible en tapant `help`.

- `!`: Lance des commandes depuis un fichier
- `dump`: Affiche les informations du réseau
- `Route`: Affiche la route entre deux noeuds
- `Link`: permet de faire une connexion réseau.
- `Relink`: Réétabli une connexion réseau.

- Unlink: Supprime un lien réseau.
- Clear: Supprime les liens réseau & réinitialise les ports.
- Guid: Définit le GUID d'un noeud.
- Error: Définit le statut d'erreur d'un port/noeud.
- PerformanceSet: Définit les conteurs de performance.
- Baselid: Change le lid d'un port.
- Verbose: Définit le niveau de sortie dans la console.
- Wait: Suspend la simulation.
- Attached: Liste tous les clients attachés.
- X: Déconnecte un client.
- Quit: Ferme le programme.

Nous avons tenté route mais on nous dit que les lid ne sont pas définis alors que nous les avons définis pour chaque nœud.

Chapter 2

Générateur de topologie

Tout d'abord le dossier de sources contient un Makefile. Il est donc possible de compiler facilement le programme grâce à la commande `make`.

Plusieurs classes sont présentes:

- Node: représente un nœud du réseau. Ce dernier est composé d'un nom, type, ID, et d'une liste de ports avec leurs connections.
- Machine: représente une machine du réseau. Cette classe étend Node avec comme type "Hca" et 1 port.
- Commutator: représente un switch du réseau. Cette classe étend Node avec comme type "Switch".

Le fichier main réalise la création des éléments ainsi que les liens entre ces derniers:

- Création de toutes les machines
 - Aggregation
 - * Création de tout les switch dans la zone d'aggregation
 - * Création des liens entre le switch et les machines qui le concernent
 - Edge
 - * Création de tout les switch dans la zone edge
 - * Création des liens entre le switch et les switchs de la zone d'aggregation
 - Core
 - * Création de tout les switch dans la zone core
 - * Création des liens entre le switch et les switchs de la zone edge
- Ajustements des ports pour suivre la norme imposée par le PDF

Voici un exemple de fichier de sortie pour $k = 4$:

Listing 2.1 – topo.topo

```

1 #Fat tree topology file
2 #Value of k = 4
3 #Total number of hosts = 16
4 #Number of hosts under each switch = 2
5 #Number of commutators = 20
6 #####
7
8 #Machines:
9 Hca 1 "Node(0)" lid 0
10 [1]"Edge(0 0 1)"[2]
11
12 Hca 1 "Node(1)" lid 1
13 [1]"Edge(0 0 1)"[4]
14
15 Hca 1 "Node(2)" lid 2
16 [1]"Edge(0 1 1)"[2]
17

```

```

18 Hca 1 "Node(3)" lid 3
19 [1]"Edge(0 1 1)"[4]
20
21 Hca 1 "Node(4)" lid 4
22 [1]"Edge(1 0 1)"[2]
23
24 Hca 1 "Node(5)" lid 5
25 [1]"Edge(1 0 1)"[4]
26
27 Hca 1 "Node(6)" lid 6
28 [1]"Edge(1 1 1)"[2]
29
30 Hca 1 "Node(7)" lid 7
31 [1]"Edge(1 1 1)"[4]
32
33 Hca 1 "Node(8)" lid 8
34 [1]"Edge(2 0 1)"[2]
35
36 Hca 1 "Node(9)" lid 9
37 [1]"Edge(2 0 1)"[4]
38
39 Hca 1 "Node(10)" lid 10
40 [1]"Edge(2 1 1)"[2]
41
42 Hca 1 "Node(11)" lid 11
43 [1]"Edge(2 1 1)"[4]
44
45 Hca 1 "Node(12)" lid 12
46 [1]"Edge(3 0 1)"[2]
47
48 Hca 1 "Node(13)" lid 13
49 [1]"Edge(3 0 1)"[4]
50
51 Hca 1 "Node(14)" lid 14
52 [1]"Edge(3 1 1)"[2]
53
54 Hca 1 "Node(15)" lid 15
55 [1]"Edge(3 1 1)"[4]
56
57 #Edge:
58 Switch 4 "Edge(0 0 1)" lid 16
59 [1]"Aggr(0 2 1)"[2]
60 [2]"Node(0)"[1]
61 [3]"Aggr(0 3 1)"[2]
62 [4]"Node(1)"[1]
63
64 Switch 4 "Edge(0 1 1)" lid 17
65 [1]"Aggr(0 2 1)"[4]
66 [2]"Node(2)"[1]
67 [3]"Aggr(0 3 1)"[4]
68 [4]"Node(3)"[1]
69
70 Switch 4 "Edge(1 0 1)" lid 18
71 [1]"Aggr(1 2 1)"[2]
72 [2]"Node(4)"[1]
73 [3]"Aggr(1 3 1)"[2]
74 [4]"Node(5)"[1]
75
76 Switch 4 "Edge(1 1 1)" lid 19
77 [1]"Aggr(1 2 1)"[4]
78 [2]"Node(6)"[1]
79 [3]"Aggr(1 3 1)"[4]
80 [4]"Node(7)"[1]
81
82 Switch 4 "Edge(2 0 1)" lid 20
83 [1]"Aggr(2 2 1)"[2]
84 [2]"Node(8)"[1]
85 [3]"Aggr(2 3 1)"[2]
86 [4]"Node(9)"[1]

```

```

87
88 Switch 4 "Edge(2 1 1)" lid 21
89 [1]"Aggr(2 2 1)"[4]
90 [2]"Node(10)"[1]
91 [3]"Aggr(2 3 1)"[4]
92 [4]"Node(11)"[1]
93
94 Switch 4 "Edge(3 0 1)" lid 22
95 [1]"Aggr(3 2 1)"[2]
96 [2]"Node(12)"[1]
97 [3]"Aggr(3 3 1)"[2]
98 [4]"Node(13)"[1]
99
100 Switch 4 "Edge(3 1 1)" lid 23
101 [1]"Aggr(3 2 1)"[4]
102 [2]"Node(14)"[1]
103 [3]"Aggr(3 3 1)"[4]
104 [4]"Node(15)"[1]
105
106 #Aggregation:
107 Switch 4 "Aggr(0 2 1)" lid 24
108 [1]"Core(4 1 1)"[1]
109 [2]"Edge(0 0 1)"[1]
110 [3]"Core(4 1 2)"[1]
111 [4]"Edge(0 1 1)"[1]
112
113 Switch 4 "Aggr(0 3 1)" lid 25
114 [1]"Core(4 2 1)"[1]
115 [2]"Edge(0 0 1)"[3]
116 [3]"Core(4 2 2)"[1]
117 [4]"Edge(0 1 1)"[3]
118
119 Switch 4 "Aggr(1 2 1)" lid 26
120 [1]"Core(4 1 1)"[2]
121 [2]"Edge(1 0 1)"[1]
122 [3]"Core(4 1 2)"[2]
123 [4]"Edge(1 1 1)"[1]
124
125 Switch 4 "Aggr(1 3 1)" lid 27
126 [1]"Core(4 2 1)"[2]
127 [2]"Edge(1 0 1)"[3]
128 [3]"Core(4 2 2)"[2]
129 [4]"Edge(1 1 1)"[3]
130
131 Switch 4 "Aggr(2 2 1)" lid 28
132 [1]"Core(4 1 1)"[3]
133 [2]"Edge(2 0 1)"[1]
134 [3]"Core(4 1 2)"[3]
135 [4]"Edge(2 1 1)"[1]
136
137 Switch 4 "Aggr(2 3 1)" lid 29
138 [1]"Core(4 2 1)"[3]
139 [2]"Edge(2 0 1)"[3]
140 [3]"Core(4 2 2)"[3]
141 [4]"Edge(2 1 1)"[3]
142
143 Switch 4 "Aggr(3 2 1)" lid 30
144 [1]"Core(4 1 1)"[4]
145 [2]"Edge(3 0 1)"[1]
146 [3]"Core(4 1 2)"[4]
147 [4]"Edge(3 1 1)"[1]
148
149 Switch 4 "Aggr(3 3 1)" lid 31
150 [1]"Core(4 2 1)"[4]
151 [2]"Edge(3 0 1)"[3]
152 [3]"Core(4 2 2)"[4]
153 [4]"Edge(3 1 1)"[3]
154
155 #Core:

```

```
156 Switch 4 "Core(4 1 1)" lid 32
157 [1]"Aggr(0 2 1)"[1]
158 [2]"Aggr(1 2 1)"[1]
159 [3]"Aggr(2 2 1)"[1]
160 [4]"Aggr(3 2 1)"[1]
161
162 Switch 4 "Core(4 1 2)" lid 33
163 [1]"Aggr(0 2 1)"[3]
164 [2]"Aggr(1 2 1)"[3]
165 [3]"Aggr(2 2 1)"[3]
166 [4]"Aggr(3 2 1)"[3]
167
168 Switch 4 "Core(4 2 1)" lid 34
169 [1]"Aggr(0 3 1)"[1]
170 [2]"Aggr(1 3 1)"[1]
171 [3]"Aggr(2 3 1)"[1]
172 [4]"Aggr(3 3 1)"[1]
173
174 Switch 4 "Core(4 2 2)" lid 35
175 [1]"Aggr(0 3 1)"[3]
176 [2]"Aggr(1 3 1)"[3]
177 [3]"Aggr(2 3 1)"[3]
178 [4]"Aggr(3 3 1)"[3]
```


Appendix A

Sources du générateur

A.1 Node

Listing A.1 – Node.h

```
1 //
2 // Created by Thomas Couchoud on 08/02/2018.
3 //
4
5 #ifndef GENERATOR_NODE_H
6 #define GENERATOR_NODE_H
7
8 #include <string>
9 #include <vector>
10
11 /**
12  * A node of the network.
13  */
14 class Node
15 {
16 private:
17     ///! Its unique ID.
18     int ID;
19     ///! Next unique ID for the generation.
20     static int NEXT_ID;
21     ///! The type of the node.
22     std::string type;
23     ///! The name of the node.
24     std::string name;
25 protected:
26     ///! The ports of the node.
27     std::vector<Node *> connectedTo;
28 public:
29     /**
30      * Constructor.
31      * @param type The type of the node.
32      * @param name The name of the node.
33      * @param connections The number of ports of the node.
34      * @param i The x coordinate.
35      * @param j The y coordinate.
36      * @param k The z coordinate.
37      */
38     Node(const std::string &type, const std::string &name, int connections, int i, int j, int k);
39
40     /**
41      * Get the type.
42      * @return The type.
43      */
44     std::string &getType();
45
46     /**
47      * Get the ID.
```

```

48     * @return The ID.
49     */
50     int getID();
51
52     /**
53     * Get the name.
54     * @return The name.
55     */
56     std::string &getName();
57
58     /**
59     * Get the number of ports.
60     * @return The number of ports.
61     */
62     int getPortCount();
63
64     /**
65     * Set where a port is connected.
66     * @param index The port index.
67     * @param node Where this port is connected.
68     */
69     void setLink(int index, Node * node);
70
71     /**
72     * Get where a port is connected.
73     * @param index The port concerned.
74     * @return The Node connected to, or null if disconnected.
75     */
76     Node * getLink(int index);
77
78     /**
79     * Get the port where a Node is connected.
80     * @param node The Node to find.
81     * @return The index of the port, -1 if not found.
82     */
83     int getPortWhereIs(Node * node);
84
85     bool operator==(const Node &rhs) const;
86
87     bool operator!=(const Node &rhs) const;
88 };
89
90 #endif //GENERATOR_NODE_H

```

Listing A.2 – Node.cpp

```

1  //
2  // Created by Thomas Couchoud on 08/02/2018.
3  //
4
5  #include <algorithm>
6  #include <sstream>
7  #include "Node.h"
8
9  using namespace std;
10
11  int Node::NEXT_ID = 0;
12
13  Node::Node(const std::string &type, const std::string &name, int connections, int i, int j, int k) :
14      type(type)
15  {
16      this->ID = Node::NEXT_ID++;
17      connectedTo = vector<Node *>();
18      connectedTo.resize(connections, nullptr);
19
20      stringstream s;
21      s << name << "(" << i;
22      if(j != -1 && k != -1)

```

```

22     s << " " << j << " " << k;
23     s << " ";
24     this->name = s.str();
25 }
26
27 string &Node::getType()
28 {
29     return type;
30 }
31
32 int Node::getID()
33 {
34     return ID;
35 }
36
37 string &Node::getName()
38 {
39     return name;
40 }
41
42 void Node::setLink(int index, Node * node)
43 {
44     if(index >= connectedTo.size())
45     {
46         printf("Trying to set a link on a non existing port (%d/%d) on %s", index, getPortCount(),
47             getName().c_str());
48         exit(EXIT_FAILURE);
49     }
50     if(connectedTo[index] != nullptr)
51         printf("WARN: Overriding existing link of %s (%d)\n", getName().c_str(), index);
52     connectedTo[index] = node;
53 }
54
55 int Node::getPortCount()
56 {
57     return static_cast<int>(connectedTo.size());
58 }
59
60 Node * Node::getLink(int index)
61 {
62     if(index >= connectedTo.size())
63     {
64         perror("Trying to get a link of a non existing port");
65         exit(EXIT_FAILURE);
66     }
67     return connectedTo[index];
68 }
69
70 int Node::getPortWhereIs(Node * node)
71 {
72     auto it = std::find(connectedTo.begin(), connectedTo.end(), node);
73     return static_cast<int>(it == connectedTo.end() ? -1 : distance(connectedTo.begin(), it));
74 }
75
76 bool Node::operator==(const Node &rhs) const
77 {
78     return ID == rhs.ID;
79 }
80
81 bool Node::operator!=(const Node &rhs) const
82 {
83     return !(rhs == *this);
84 }

```

A.2 Machine

Listing A.3 – Machine.h

```

1 //
2 // Created by Thomas Couchoud on 08/02/2018.
3 //
4
5 #ifndef GENERATOR_MACHINE_H
6 #define GENERATOR_MACHINE_H
7
8 #include "Node.h"
9
10 /**
11  * A machine.
12  */
13 class Machine : public Node
14 {
15 public:
16     /**
17      * Constructor.
18      *
19      * @param i The x coordinate.
20      * @param j The y coordinate.
21      * @param k The z coordinate.
22      */
23     Machine(int i, int j, int k);
24
25     /**
26      * Set the node where the machine is connected to.
27      * @param node The node to connect.
28      */
29     void setConnectedTo(Node * node);
30 };
31
32 #endif //GENERATOR_MACHINE_H

```

Listing A.4 – Machine.cpp

```

1 //
2 // Created by Thomas Couchoud on 08/02/2018.
3 //
4
5 #include "Machine.h"
6
7 Machine::Machine(int i, int j, int k) : Node("Hca", "Node", 1, i, j, k)
8 {
9 }
10
11 void Machine::setConnectedTo(Node * node)
12 {
13     setLink(0, node);
14 }

```

A.3 Commutator

Listing A.5 – Commutator.h

```

1 //
2 // Created by Thomas Couchoud on 08/02/2018.
3 //
4
5 #ifndef GENERATOR_COMMUTATOR_H
6 #define GENERATOR_COMMUTATOR_H
7
8 #include "Node.h"
9 #include "Machine.h"
10

```

```

11  /**
12   * A Switch.
13   */
14  class Commutator : public Node
15  {
16  public:
17      /**
18       * Constructor.
19       * @param name The name of the switch.
20       * @param connections The number of ports of teh switch.
21       * @param i The x coordinate.
22       * @param j The y coordinate.
23       * @param k The z coordinate.
24       */
25      Commutator(const std::string &name, int connections, int i, int j, int k);
26
27      /**
28       * Normalize ports to follow those in the TP subject.
29       */
30      void normalizePorts();
31  };
32
33  #endif //GENERATOR_COMMUTATOR_H

```

Listing A.6 – Commutator.cpp

```

1  //
2  // Created by Thomas Couchoud on 08/02/2018.
3  //
4
5  #include <vector>
6  #include <cmath>
7  #include "Commutator.h"
8  #include "Machine.h"
9
10 using namespace std;
11
12 Commutator::Commutator(const std::string &name, int connections, int i, int j, int k) : Node("Switch",
13     name, connections, i, j, k)
14 {
15 }
16
17 void Commutator::normalizePorts()
18 {
19     unsigned long s = connectedTo.size();
20     vector<Node *> normalized = vector<Node *>(s, nullptr);
21     for(int i = 0; i < s; i++)
22         normalized[(i * 2) % s + (i * 2 < s ? 1 : 0)] = connectedTo[i];
23     swap(connectedTo, normalized);
24 }

```

A.4 Main

Listing A.7 – main.cpp

```

1  #include <iostream>
2  #include <cmath>
3  #include <vector>
4  #include <fstream>
5  #include "Commutator.h"
6
7  using namespace std;
8
9  /**
10   * Write a node into a stream.
11   *

```

```

12  * @param s The stream to write into.
13  * @param node The node to write.
14  */
15  void writeNode(ostream &s, Node * node)
16  {
17      //Write node
18      s << node->getType() << '\t' << node->getPortCount() << '\t' << '"' << node->getName() << '"' << " lid
        " << node->getID() << endl;
19      for(int i = 0; i < node->getPortCount(); i++)
20      {
21          //Write connection
22          Node * connectedTo = nullptr;
23          if((connectedTo = node->getLink(i)) != nullptr)
24              s << '[' << (i + 1) << ']' << '"' << connectedTo->getName() << '"' << '[' <<
                (connectedTo->getPortWhereIs(node) + 1) << ']' << endl;
25      }
26      s << endl;
27  }
28
29  /**
30   * Write the topology file.
31   * @param s The stream to write into.
32   * @param k The k used.
33   * @param machines The machines.
34   * @param edge The edge switches.
35   * @param aggregation The aggregation switches.
36   * @param core The core switches.
37   */
38  void writeAll(ostream &s, int k, vector<Machine *> machines, vector<Commutator *> edge, vector<Commutator
    *> aggregation, vector<Commutator *> core)
39  {
40      s << "#Fat tree topology file" << endl;
41      s << "#Value of k = " << k << endl;
42      s << "#Total number of hosts = " << machines.size() << endl;
43      s << "#Number of hosts under each switch = " << (k / 2) << endl;
44      s << "#Number of commutators = " << (edge.size() + aggregation.size() + core.size()) << endl;
45      s << "#####" << endl << endl;
46
47      s << "#Machines:" << endl;
48      for(auto m : machines)
49          writeNode(s, m);
50
51      s << "#Edge:" << endl;
52      for(auto c : edge)
53          writeNode(s, c);
54
55      s << "#Aggregation:" << endl;
56      for(auto c : aggregation)
57          writeNode(s, c);
58
59      s << "#Core:" << endl;
60      for(auto c : core)
61          writeNode(s, c);
62
63      s.flush();
64  }
65
66  int main(int argc, char ** argv)
67  {
68      int k;
69      if(argc == 2)
70      {
71          k = atoi(argv[1]);
72      }
73      else
74      {
75          cout << "Enter k: ";
76          cin >> k;
77      }

```

```

78
79 cout << "Selected k: " << k << endl;
80 if(k%2 != 0)
81 {
82     cout << "K must be a multiple of 2" << endl;
83     exit(EXIT_FAILURE);
84 }
85
86 //Init constants (count of elements par category)
87 auto machineCountPerPod = static_cast<int>(pow(k / 2, 2));
88 auto machineCount = machineCountPerPod * k;
89 auto edgeCount = static_cast<int>(pow(k, 2) / 2);
90 auto aggregationCount = static_cast<int>(pow(k, 2) / 2);
91 auto coreCount = static_cast<int>(pow(k / 2, 2));
92
93 vector<Machine *> machines = vector<Machine *>();
94 vector<Commutator *> edge = vector<Commutator *>();
95 vector<Commutator *> aggregation = vector<Commutator *>();
96 vector<Commutator *> core = vector<Commutator *>();
97
98 //Create machines
99 for(int i = 0; i < machineCount; i++)
100 {
101     cout << "Creating machine " << i << endl;
102     machines.push_back(new Machine(i, -1, -1));
103 }
104
105 //Create edge
106 for(int i = 0; i < edgeCount; i++)
107 {
108     //Create switch
109     cout << "Creating edge " << i << endl;
110     Commutator * commutator = new Commutator("Edge", k, i / (k / 2), i % (k / 2), 1);
111     edge.push_back(commutator);
112
113     //Link switch to the machines
114     for(int j = 0; j < k / 2; j++)
115     {
116         Machine * m = machines[i * (k / 2) + j];
117         m->setConnectedTo(commutator);
118         commutator->setLink(j, m);
119     }
120 }
121
122 //Create aggregation
123 for(int i = 0; i < aggregationCount; i++)
124 {
125     //Create switch
126     cout << "Creating aggregation " << i << endl;
127     Commutator * commutator = new Commutator("Aggr", k, i / (k / 2), i % (k / 2) + (k / 2), 1);
128     aggregation.push_back(commutator);
129
130     //Link to edge switches
131     int group = i / (k / 2);
132     int offsetPort = i % (k / 2);
133     for(int j = 0; j < k / 2; j++)
134     {
135         Commutator * c = edge[group * k / 2 + j];
136         c->setLink(k / 2 + offsetPort, commutator);
137         commutator->setLink(j, c);
138     }
139 }
140
141 //Create core
142 for(int i = 0; i < coreCount; i++)
143 {
144     //Create switch
145     cout << "Creating core " << i << endl;
146     Commutator * commutator = new Commutator("Core", k, k, 1 + i / (k / 2), 1 + i % (k / 2));

```

```
147     core.push_back(commutator);
148
149     //Link to aggregation
150     int offsetAggregation = 2 * i / k;
151     int offsetPortAggregation = i % (k / 2);
152     for(int j = 0; j < k; j++)
153     {
154         Commutator * c = aggregation[offsetAggregation + j * (k / 2)];
155         c->setLink(k / 2 + offsetPortAggregation, commutator);
156         commutator->setLink(j, c);
157     }
158 }
159
160 //Normalize ports to follow the naming in the TP subject
161 for(auto c : edge)
162     c->normalizePorts();
163 for(auto c : aggregation)
164     c->normalizePorts();
165
166 //Write file
167 ofstream outFile;
168 outFile.open("topo.topo");
169 writeAll(outFile, k, machines, edge, aggregation, core);
170 outFile.close();
171
172 //Bye bye
173 cout << flush;
174 return 0;
175 }
```