TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62786AP, TD62786F, TD62786AF TD62787AP, TD62787F, TD62787AF

### 8CH HIGH-VOLTAGE SOURCE DRIVER

The TD62786AP/F/AF series are eight channel huyx non-inverting source current transistor array. All units feature integral clamp diodes for switching inductive loads. Applications include relay, hammer and lamp drivers.

### **FEATURES**

• High output voltage type-AP, AF: V<sub>CE</sub> (SUS) = 50V (Min.)

type-F : VCE (SUS) = 35V (Min.)

Output current (single output) : I<sub>OUT</sub> = -500mA / ch

(Max.)

Output clamp diodes

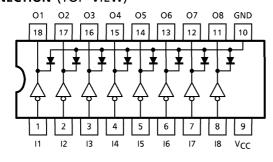
Single supply voltage

Input compatible with TTL, 5V CMOS

Low level active input

Package type-AP : DIP-18 pinPackage type-F, AF : SOP-18 pin

### PIN CONNECTION (TOP VIEW)

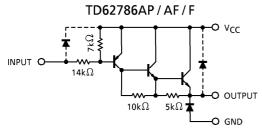


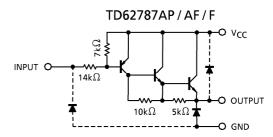
# DIP18-P-300-2.54D TD62786AF TD62786AF TD62786AF TD62787AF TD62787AF SOP18-P-375-1.27

Weight

DIP18-P-300-2.54D : 1.47g (Typ.) SOP18-P-375-1.27 : 0.41g (Typ.)

### **SCHEMATICS** (EACH DRIVER)





(Note) The input and output parasitic diodes cannot be used as clamp diodes.

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TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

### **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERIST	С	SYMBOL	RATING	UNIT	
Summly Valtage	AP/AF	V V	50	< -	
Supply Voltage	F	V <sub>CC</sub> - V <sub>GND</sub>	35		
Output Sustaining	AP/AF	V	- 50	V	
Voltage	F	Vout	<b>–</b> 35		
Output Current		lout	- 500	mA / ch	
Input Voltage		V <sub>IN</sub> (Note 1)	-30~0.5	V	
Input Voltage		V <sub>IN</sub> (Note 2)	V <sub>GND</sub> ∼7	V	
Clamp Diode	AP/AF	\/-	50	V	
Forward Current	F	V <sub>R</sub>	35		
Clamp Diode Forward	Current	IF	500	mA	
Darrey Dissipation	AP	D- (Nets 2)	1.47	w	
Power Dissipation	F/AF	P <sub>D</sub> (Note 3)	0.96		
Operating Temperatur	е	T <sub>opr</sub>	- 40~85	°C	
Storage Temperature		T <sub>stg</sub>	<b>- 55∼150</b>	°C	

(Note 1) Only TD62786AP/F/AF

(Note 2) Only TD62787AP/F/AF

(Note 3) Delated above 25°C in the proportion of 11.7mW/°C (AP Type), 7.7mW/°C (F, AF Type).

### **RECOMMENDED OPERATING CONDITIONS** (Ta = $-40 \sim 85$ °C, $V_{CC} = 0V$ )

CHARACTERISTIC		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Cumply Voltage	AP/AF	Vac - Vaun	_	<u> </u>	_	50	V	
Supply Voltage	F	V <sub>CC</sub> – V <sub>GND</sub>	_	_	_	35		
Output Voltage	AP/AF	VOUT	_	_	1	- 50	V	
	F		_	-	_	- 35		
Output Current		IOUT	_	_	_	- 350	mA / ch	
Input Voltage	TD62786	VIN	_	- 30	_	0	V	
	TD62787	] VIN	_	V <sub>GND</sub>	1	7		
Clamp Diode	AP/AF	V <sub>R</sub>	_	_	_	50	V	
Reverse Voltage	F	VR	_	_	_	35	7 <b>'</b>	
Clamp Diode Forward Current		lF	<u>_</u>			350	mA	
			_					
Power Dissipation	on AP	- P <sub>D</sub>	_	_	_	0.52	w	
Fower Dissipation		טי	_	_	_	0.35	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	

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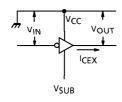
# **ELECTRICAL CHARACTERISTICS** (Ta = 25°C, V<sub>CC</sub> = 0V)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Output Leakage Current			ICEX	1	$V_{OUT} = V_{GND} = -50V$ Ta = 85°C		1	- 100	$\mu$ A
Output Catamatica Walterna			V <sub>CE</sub> (sat)	2	V <sub>IN</sub> = V <sub>IL</sub> MAX. I <sub>OUT</sub> = -100mA		-	- 1.8	· v
Output Saturation Voltage		V <sub>IN</sub> = V <sub>IL</sub> MAX. I <sub>OUT</sub> = -350mA					- 2.0		
DC Current transfer Ratio		h <sub>FE</sub>	2	$V_{CC} = 0V$ , $V_{CE} = 3V$ $I_{OUT} = -350$ mA	1000	ı	_	l	
Input Voltage	"H" Level	TD62786	· V <sub>IN</sub>	4	_	- 1.2		0	V
		TD62787				- 1.6	_	5.5	
	"L" Level	TD62786				- 30	_	- 2.8	
		TD62787				$V_{GND}$	_	- 3.7	
Input Cu	Input Current		Iμ	_	$V_{CC} = 5.5V, V_{IN} = 0.4V$	_	_	-0.4	mA
Clamp Diode Reverse		I <sub>R</sub>	_	$V_R = V_R MAX.$ , $Ta = 85$ °C	_	_	100	μΑ	
Current						_	100		
Clamp Diode Forward Voltage		V <sub>F</sub>	_	_	_	_	2.0	V	
Turn-On Delay		t <sub>ON</sub>	5	$V_{OUT} = -50V$ , $R_L = 163\Omega$	_	0.2	T —	$\mu$ s	
Turn Off Delay				$C_L = 15pF$ (Note)	_	1.0	_		

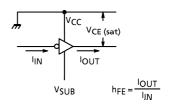
(Note) V $_{OUT}$  = - 35V,  $R_L$  = 116  $\Omega$  for Type-F

### **TEST CIRCUIT**

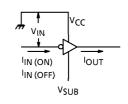
### 1. ICEX



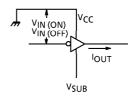
# 2. VCE (sat), hFE



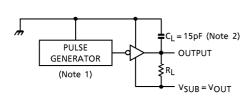
# 3. In (ON), In (OFF)

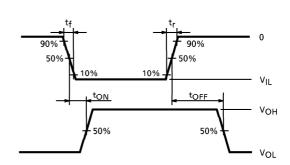


# 4. VIN (ON), VIN (OFF)



## 5. ton, toff

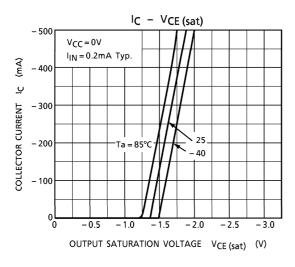


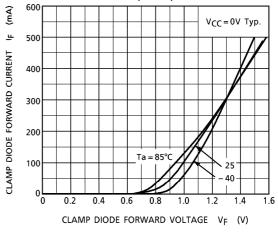


- (Note 1) Pulse Width 50  $\mu s$  , Duty Cycle 10% Output Impedance 50  $\Omega$  ,  $t_f {\leq}$  10ns,  $t_f {\leq}$  5ns
- (Note 2) C<sub>L</sub> includes probe and jig capacitance.

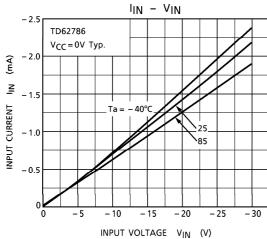
### PRECAUTIONS for USING

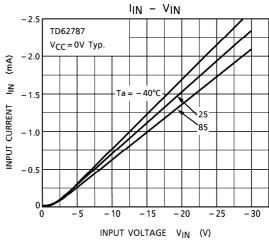
Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

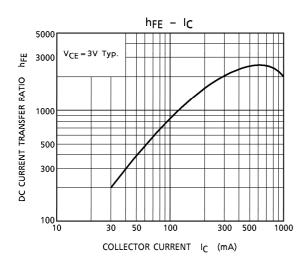


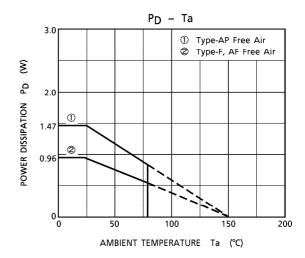


IF - VF



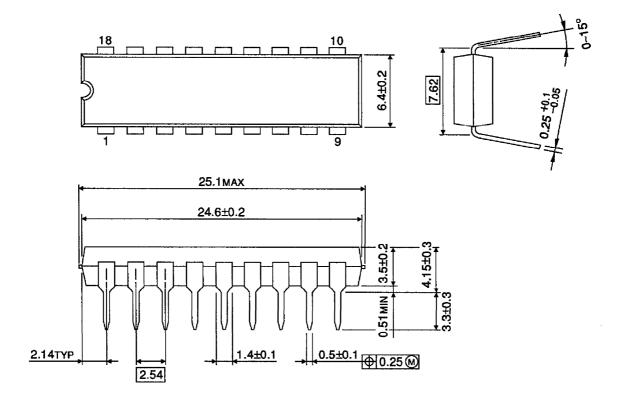






### OUTLINE DRAWING DIP18-P-300-2.54D

Unit: mm



Weight: 1.47g (Typ.)

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