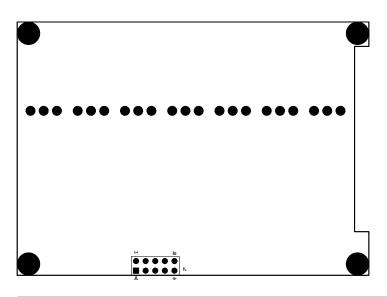


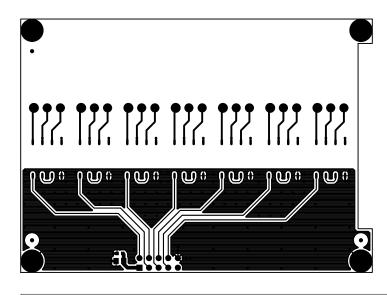
		_				,	REVISION	HISTORY *
	_	REV	DATE	Designer				
	<b>← ART LINE</b>							Jeong Sook, Kim
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	R			
Rev	1.0	Layer	SSblid6crblenskTolpp	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Mon Mar 02 10:05:00 2020 - (Untitled)



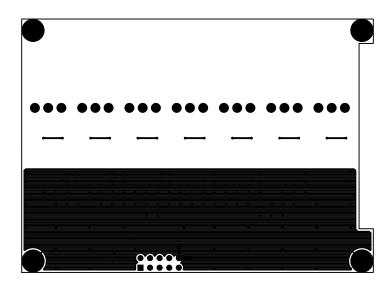
		_					REVISION	HISTORY *
		REV	DATE	Designer				
		R 1.0	20. 03. 02	Jeong Sook, Kim				
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	K			
Rev	1.0	Layer	Söllde8ck/keenk EBrotttom	Design Unit	mm			
Data	2020. 03. 02	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : art01.pho



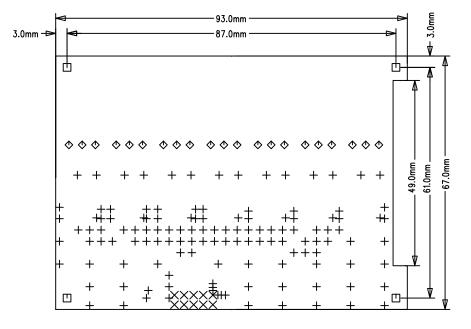
		_					REVISION	HISTORY *
	_	Λι	RT LINE	:		REV	DATE	Designer
			VI FIIAF	•		R 1.0	20. 03. 03	Jeong Sook, Kim
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	P			
0,0	KELAT_DU	D/U SIZE	93 X 07 mm	2 LAIL				
Rev	1.0	Layer	Component Side	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : art02.pho



		Α.		•			REVISION	HISTORY *
			REV R 1.0	DATE 20. 03. 03	Jeong Sook, Kim			
	•			K 1.0	20. 03. 03	Jeong Sook, Killi		
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	R			
Rev	1.0	Layer	Solder Side	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

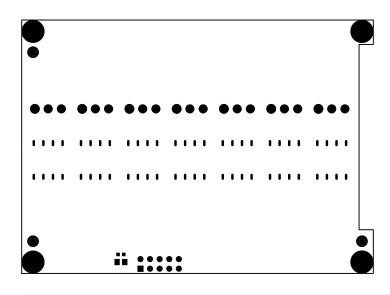
CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : dd0124.pho



				_			REVISION	HISTORY *
	_	$\Lambda$	RT LINE			REV	DATE	Designer
				R 1.0	20. 03. 03	Jeong Sook, Kim		
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	R			
5,5	KELAT_DD	D/ D 3126	93 X 07 IIIII	Z CATE				
Rev	1.0	Layer	Drill Drawing	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

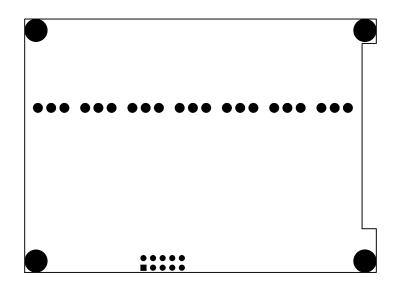
SIZE	QTY	SYM	PLATED	TOL
11.81	128	+	YES	+/-0.0
39.37	10	×	YES	+/-0.0
125.98	4		YES	+/-0.0
51.18	21	<b>\Delta</b>	YES	+/-0.0

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : sm0121.pho



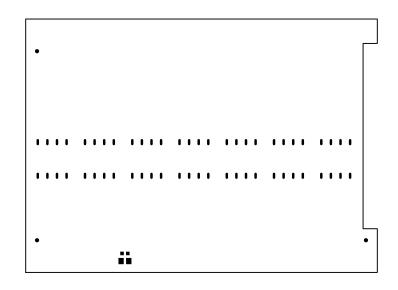
		_					REVISION	HISTORY *
	_	Λι	RT LINE	:		REV	DATE	Designer
		<b>—</b> ''	VI FIIAF			R 1.0	20. 03. 03	Jeong Sook, Kim
D /D		- t I		0.1475				
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	К			
Rev	1.0	Layer	Solder Mask Top	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : sm0228.pho



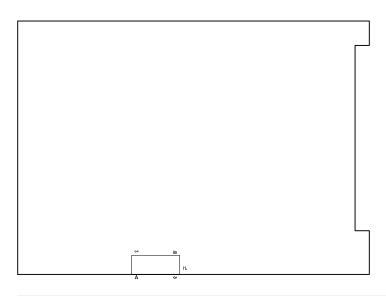
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	_	Λ	RT LINE			REV	DATE	Designer
			VI FIIAF	•		R 1.0	20. 03. 03	Jeong Sook, Kim
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	R			
<u> </u>	KEEKT_DD	.,						
Rev	1.0	Layer	Solder Mask Bottom	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : smd001023.pho



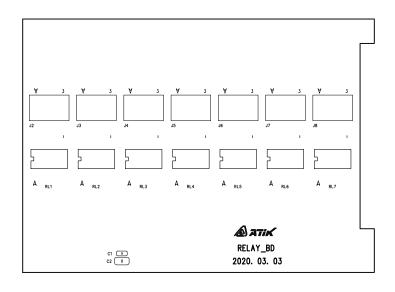
				_				HISTORY *
			RT LINE	•		REV	DATE	Designer
		<b>—</b> ''	VI FIIAF	-		R 1.0	20. 03. 03	Jeong Sook, Kim
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	R			
Rev	1.0	Layer	Paste Mask Top	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : ssb0229.pho



						•	REVISION	HISTORY *
	_	Λ	RT LINE	:		REV	DATE	Designer
			VI FIIAF			R 1.0	20. 03. 03	Jeong Sook, Kim
D /D				0.1475	_			
B/D	RELAY_BD	B/D Size	93 X 67 mm	2 LAYE	K			
Rev	1.0	Layer	Silk Screen Bottom	Design Unit	mm			
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T			

CAM350/DFMSTREAM V 12.2 : Tue Mar 03 16:09:21 2020 - (Untitled) : sst0126.pho



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		Λι	DT I INE			F	REV	ı	DATE		Des	signer	
	/D RELAY_BD B/D Size 93 X 67 mm 2 LAYER							20.	. 03.	03	Jeong	Sook,	Kim
D /D	DE: 11/ DD	n /n o:		O LAVE									
ט/ם	KETA L TRD	B/D Size	93 X 6/ mm	Z LATE									
Rev	1.0	Layer	Silk Screen Top	Design Unit	mm								
Data	2020. 03. 03	Designer	Jeong Sook, Kim	Pcb Thickness	T								