# **L-Titrator MAIN BOARD**

Version 0.2

#### **SCHEMATICS PAGE DESCRIPTIONS**

P01: COVER

P02: BLOCK DIAGRAM P03: REVISION HISTORY

P04: DAC

P05: POWER/PARALLEL\_IN\_OUT

P06: TEMP/24VOUT/BLDC

P07: UART P08: ADC

P09: INTERFACE

P10: RELAY INPUT

#### **INPUT POWER SUPPLY RATING**

+24VDC: INPUT +12VDC: 1A (Max) +7VDC: not use +A5V: 100mA (Max) +D5V: 800mA (Max) +3.3VDC: 800mA (Max)

#### **INDICATION LED for SYSTEM POWER**

LED5: RED(D3.3V)

#### **BOARD TYPE & REVISION**

P09: TYPE, REVISION

#### **TOP SILK**

ATiK Logo ATiK Co., Ltd

ASS'Y: L-Titrator Main Rev0.2

Date : 2022.04.01

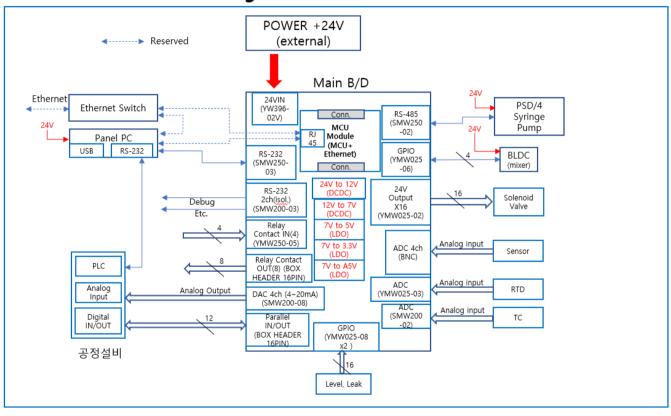
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Bottom Silk - L-Titrator Main, Rev0.2

Tile	L-Titrator Mai	n Board	d			
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## 2022.04.26

# L-Titrator Rev0.2 Block Diagram



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Size A3	Document Number Block Diagram	II BUAI				Rev 0.2
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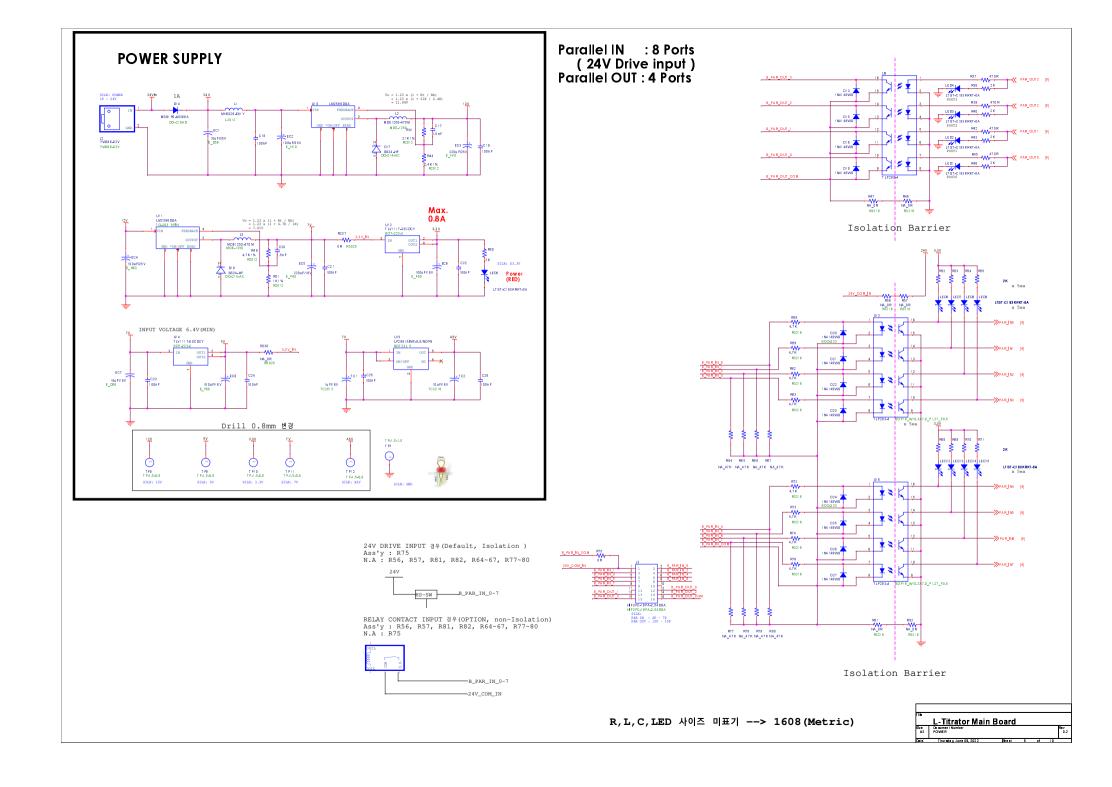
**Revision History** 

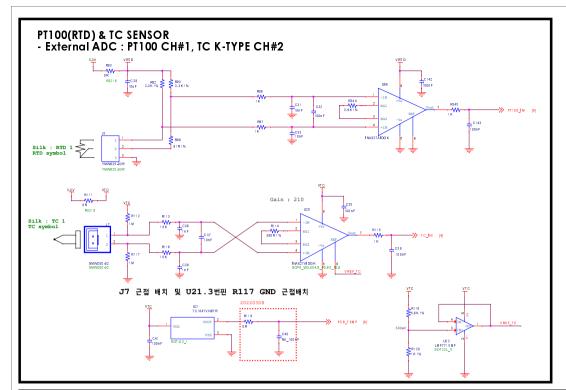
IXC VISIOII I		
REVISION	DATE	DESCRIPTION
Rev 0.1	2021.11.17	new design
	2021.11.19	1차 DR 진행 후 업데이트
	2021.11.29	2차 DR 진행 후 업데이트
Rev 0.2	2022.01.19	DAC part 저항 변경, 7V adj 저항값(오기입) 변경
	2022.03.08	7V,A5V TP 추가 ,TC 파트 R119 -> 0R , C40 -> NA
	2022.03.08	BLDC파트 level shifter 부품 변경
	2022.04.26	DAC , 4~20mA 파트 제너다이오드 추가 및 시정수 수정
	2022.04.26	ADC 파트 레퍼런스 전압 및 Gain 수정
	2022.05.17	BLDC in/out ESD protection

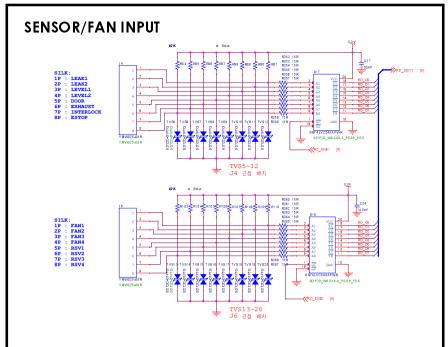
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L-Titrator Main Board							
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A3	Revision History					0.2	
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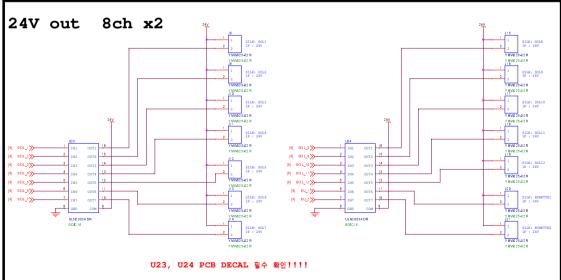
R,L,C,LED 사이즈 미표기 --> 1608 (Metric)

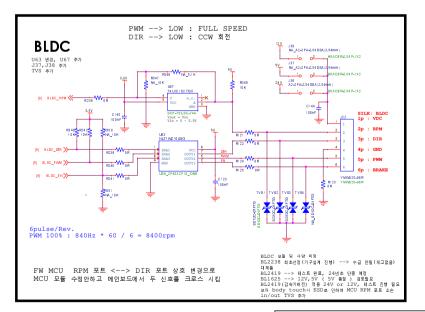
L-Titrator Main Board



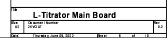








R,L,C 사이즈 미표기 --> 1608 (Metric)



### RS-232/RS-485

