

# ECE213: Digital Electronics



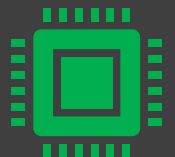
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# The Course Contents

## Unit II

Combinational Logic System : Truth table, Basic logic operation, Boolean Algebra, Basic postulates, Standard representation of logic functions -SOP forms, Simplification of switching functions - K-map, Synthesis of combinational logic circuits, Logic gates, Fundamental theorems of Boolean algebra, Standard representation of logic functions POS forms

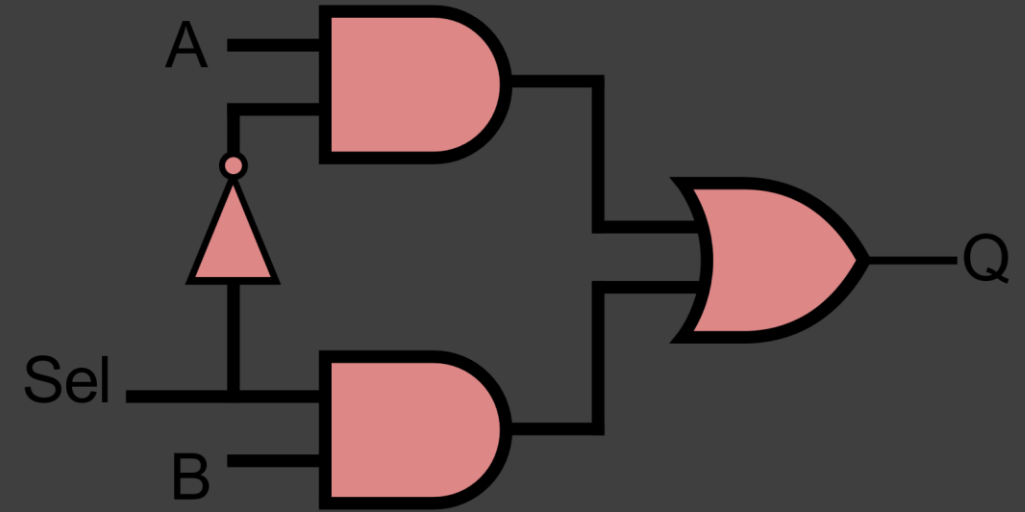
AB		00	01	11	10
CD	00	0	0	1	1
	01	0	0	1	1
	11	0	0	0	1
	10	0	1	1	1



# The Course Contents

## Unit III

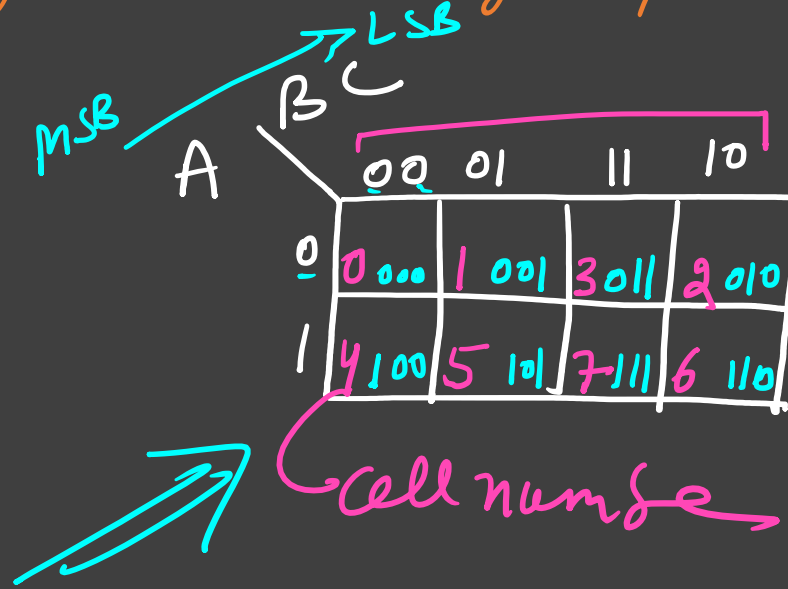
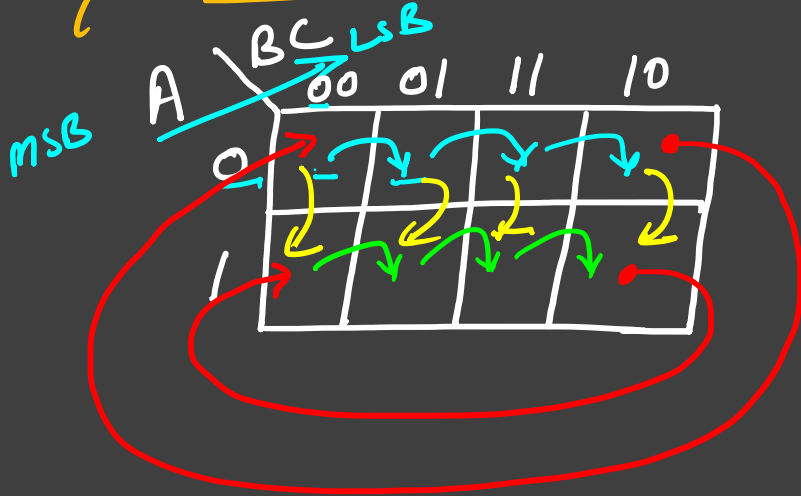
Introduction to Combinational Logic Circuits : Adders,  
Subtractors, Comparators, Multiplexers and  
Demultiplexers, Decoders, Encoders, Parity circuits  
Introduction to Logic Families : Introduction to  
different logic families, Structure and operations of  
TTL, MOS and CMOS logic families



# Combinational Logic System

Simplification of switching functions - Karnaugh-map

★ 3-Var K-map

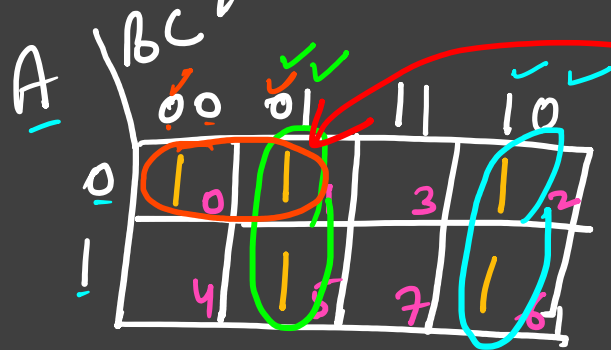


# Combinational Logic System

Simplification of switching functions - K-map

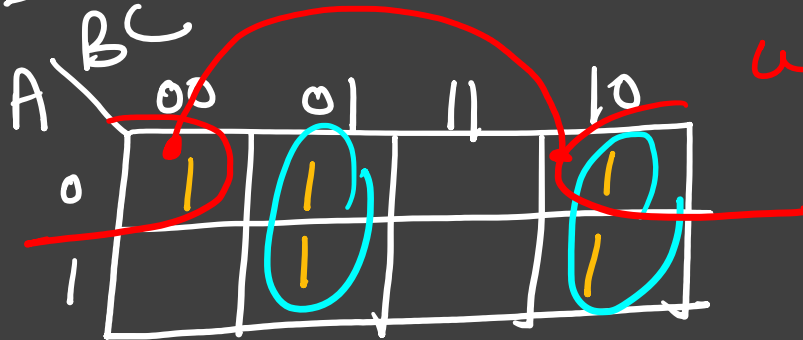
Ex Reduce the following Boolean fun. using K-map

$$Y = \sum m(\underline{0}, \underline{1}, \underline{2}, \underline{5}, \underline{6})$$



$$Y = \underline{B\bar{C}} + \underline{\bar{B}C} + \bar{A}\bar{B}$$

$$Y = (B \oplus C) + \bar{A}\bar{B}$$



$$Y = B\bar{C} + \bar{B}C + \bar{A}\bar{B}$$

$$Y = (B \oplus C) + \bar{A}\bar{B}$$

2-Var 0-3

3-Var 0-7

4-Var 0-15

5-Var 0-31

6-Var 0-63

# Combinational Logic System

## Simplification of switching functions - K-map

$y_1$

A \ BC	00	01	11	10
0	1	1	0	0
1	1	1	0	0

$$y_1 = \underline{\underline{\overline{B}}}$$

$y_3$

A \ BC	00	01	11	10
0	0	0	1	1
1	0	0	1	1

$$y_3 = \underline{\underline{B}}$$

$y_5$

A \ BC	00	01	11	10
0	1	0	0	1
1	1	0	0	1

$$y_5 = \underline{\underline{\overline{C}}}$$

$y_2$

A \ BC	00	01	11	10
0	0	1	1	0
1	0	1	1	0

$$y_2 = \underline{\underline{C}}$$

$y_4$

A \ BC	00	01	11	10
0	1	1	1	1
1	0	0	0	0

$$y_4 = \underline{\underline{\overline{A} + \overline{B}\overline{C}}}$$

Q: How many variable can be eliminate using  $2^n$  group size is K-map

A:

$n=0$	$2^0 = 1$	$n \text{ max} =$
$n=1$	$2^1 = 2$	
$n=2$	$2^2 = 4$	
$n=3$	$2^3 = 8$	

# Combinational Logic System

## Simplification of switching functions - K-map

$y_6$

A \ BC	00	01	11	10
0	0	1	1	1
1	0	1	1	1

$$y_6 = C + B$$

$y_8$

A \ BC	00	01	11	10
0	0	1	0	1
1	0	1	1	1

$$y_8 = \bar{B}C + B\bar{C} + AC$$

$y_{10}$

A \ BC	00	01	11	10
0		1	1	
1			1	1

$$y_{10} = \bar{A}C + AB$$

$y_7$

A \ BC	00	01	11	10
0	1	0	0	1
1	1	1	1	1

$$y_7 = A + \bar{C}$$

$y_9$

A \ BC	00	01	11	10
0	1	1	1	0
1	0	1	1	1

$$y_9 = C + \bar{A}\bar{B} + AB$$

$y_{11}$

A \ BC	00	01	11	10
0	1	0	1	1
1	0	0	1	1

$$y_{11} = B + \bar{A}\bar{C}$$

# Combinational Logic System

Simplification of switching functions - K-map

EX

A \ BC	00	01	11	10
0	0	1	0	1
1	1	0	1	0

$$\begin{aligned}
 Y &= \bar{A}\bar{B}C + \bar{A}B\bar{C} + A\bar{B}\bar{C} + ABC \\
 &= \bar{A}(\bar{B}C + B\bar{C}) + A(\bar{B}\bar{C} + BC) \\
 &= \bar{A}(B \oplus C) + A(\overline{B \oplus C}) \\
 &= A \oplus B \oplus C
 \end{aligned}$$

X \ A \ B	0	1
0	0	1
1	1	0

$$X = A \oplus B$$

Z \ A \ B	0	1
0	0	1
1	1	0

$$Z = \overline{A \oplus B}$$

Y \ A \ BC	00	01	11	10
0	0	1	0	1
1	1	0	1	0

$$\begin{aligned}
 Y &= \bar{B}(A \oplus C) + B(\overline{A \oplus C}) \\
 &= A \oplus B \oplus C
 \end{aligned}$$



## Combinational Logic System

Simplification of switching functions - K-map

Σx

A	B C			
	00	01	11	10
0	1	0	1	0
1	0	1	0	1

$$\begin{aligned} Y &= \bar{A}\bar{B}\bar{C} + A\bar{B}C + \bar{A}BC + AB\bar{C} \\ &= \bar{B}(\bar{A}\bar{C} + AC) + B(\bar{A}C + A\bar{C}) \\ &= \bar{B}(\overline{A \oplus C}) + B(A \oplus C) \\ &= A \oplus B \oplus C \end{aligned}$$

# Combinational Logic System

Unit 3

## Simplification of switching functions - Karnaugh-map

Ex Design and implement the full adder circuit

Ans Logic: It can add two 1-bit binary numbers, considering previous carry

⇒

	A	B	Cin	S	Cout
0	0	0	0	0	0
1	0	0	1	1	0
2	0	1	0	1	0
3	0	1	1	0	1
4	1	0	0	1	0
5	1	0	1	0	1
6	1	1	0	0	1
7	1	1	1	1	1

for Sum

A \ B Cin	00	01	11	10
0	0	1	0	1
1	1	0	1	0

$$S = A \oplus B \oplus C_{in}$$

for Carry

A \ B Cin	00	01	11	10
0	0	0	1	0
1	0	1	1	1

$$Y = A C_{in} + B C_{in} + A B$$

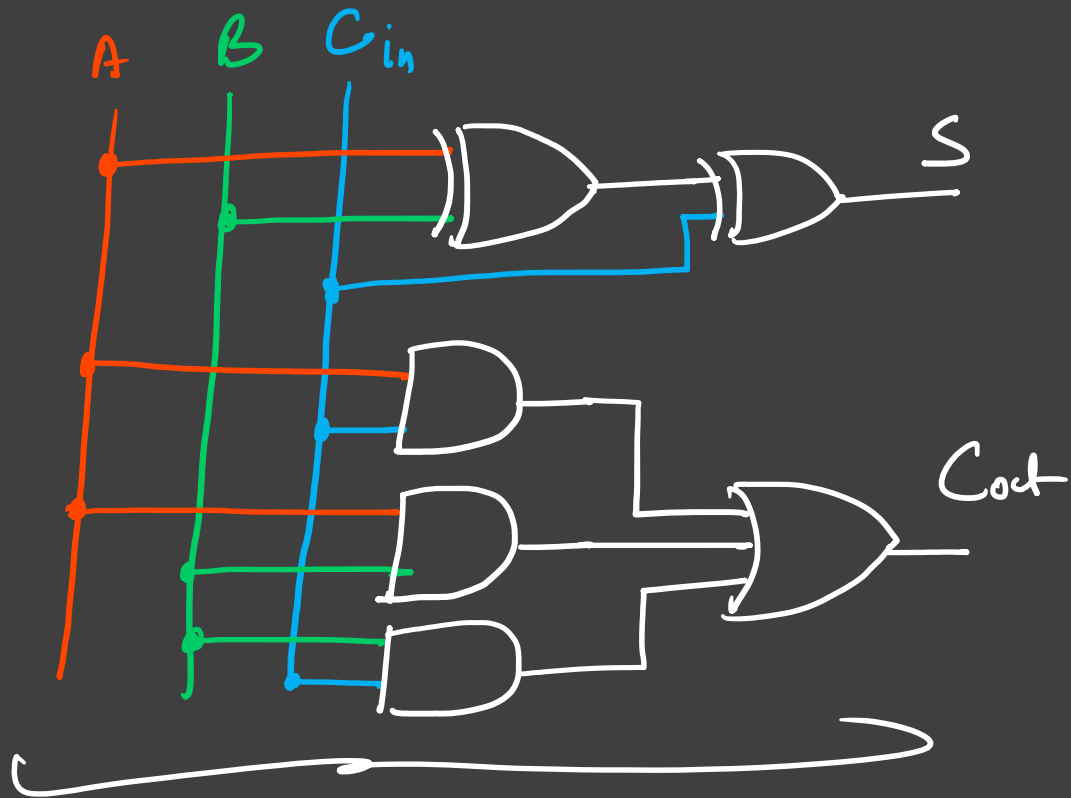
or for carry

A \ B Cin	00	01	11	10
0	0	0	1	0
1	0	1	1	1

$$Y = (A \oplus B) C_{in} + A B$$

# Combinational Logic System

Simplification of switching functions - K-map



full adder.

Unit 3

# Combinational Logic System

Simplification of switching functions - K-map

Ex Design and implement the full subtractor

Ans Logic: It can subtract two 1-bit binary number considering previous borrow.

A	B	B <sub>in</sub>	D	B <sub>out</sub>
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

# for D

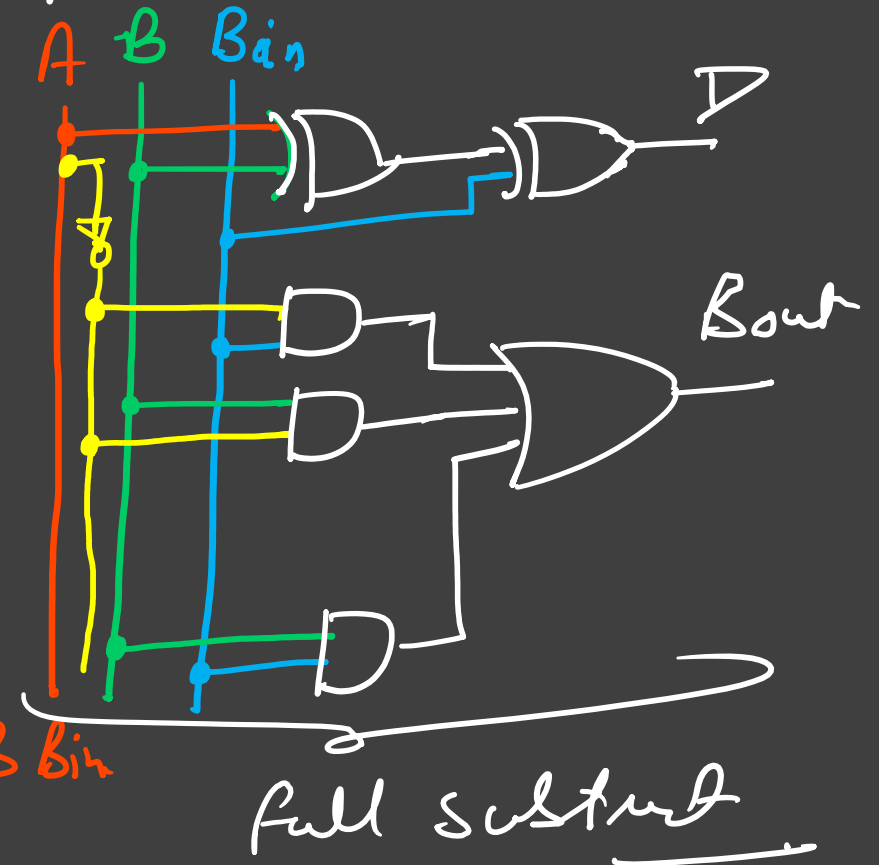
A	B	B <sub>in</sub>	D
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

$$D = A \oplus B \oplus B_{in}$$

# B<sub>out</sub>

A	B	B <sub>in</sub>	B <sub>out</sub>
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

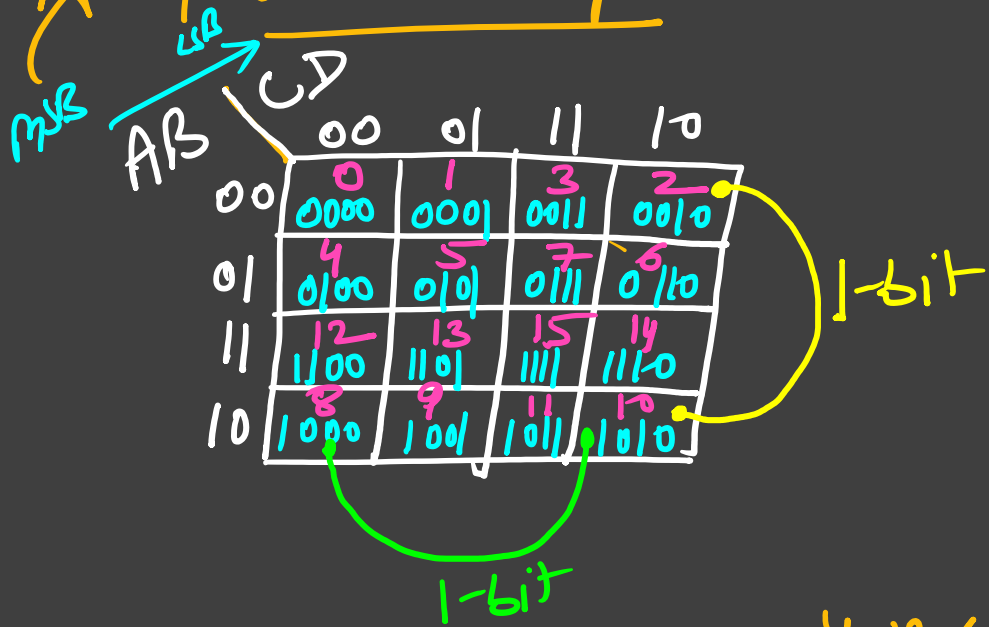
$$B_{out} = \bar{A} B_{in} + \bar{A} B + B B_{in}$$



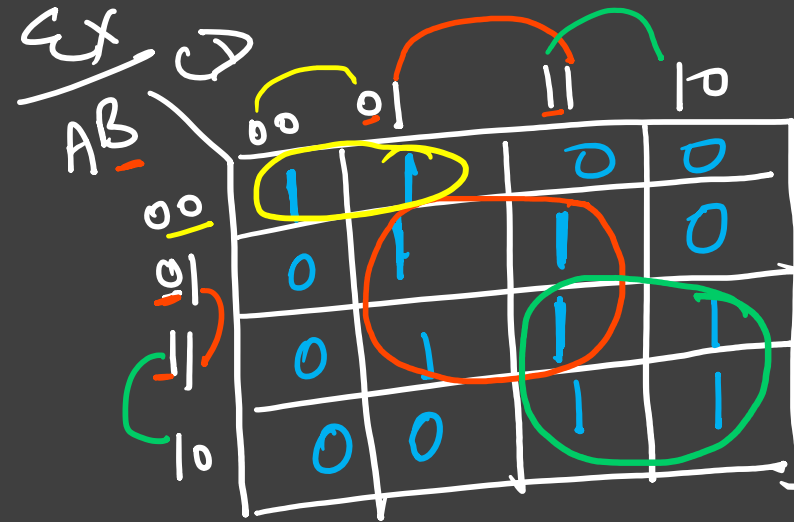
# Combinational Logic System

Simplification of switching functions - K-map

★ 4-Var K-map



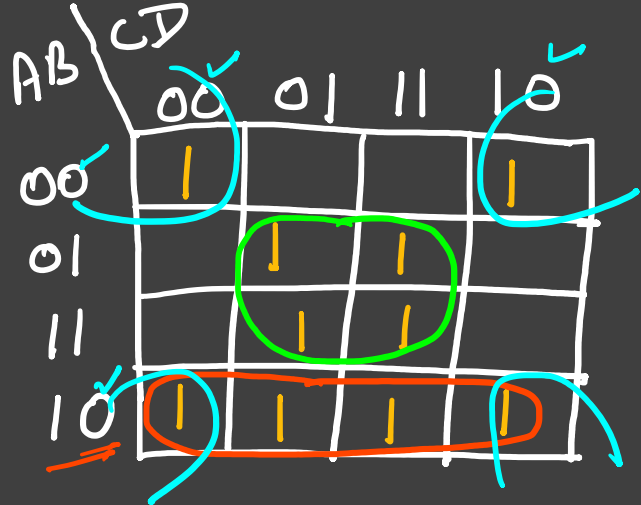
- Possible group size in 4-Var K-map
- 1 cell, 2 cell, 4 cell, 8 cell, 16 cell.



$$Y = BD + AC + \bar{A}\bar{B}\bar{C}$$

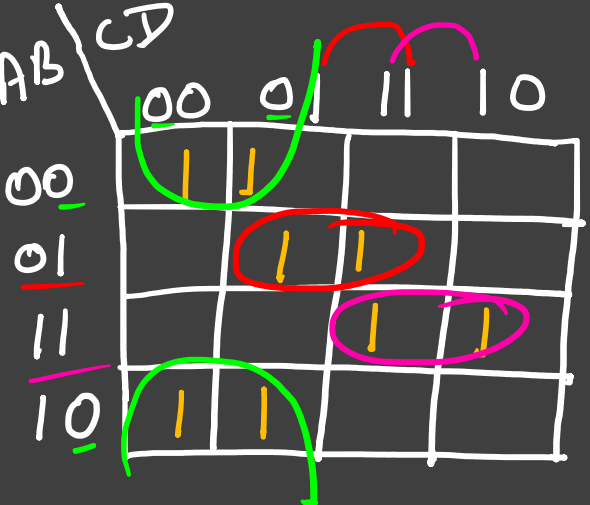
# Combinational Logic System

Simplification of switching functions - K-map

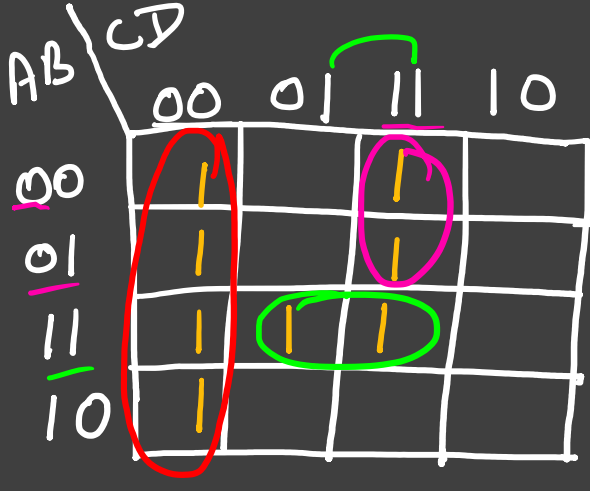

  

$$Y_1 = BD + AB + \bar{B}\bar{D}$$

$$= \overline{B \oplus D} + A\bar{B}$$

$$Y_2 = \bar{B}\bar{C} + \bar{A}BD + ABC$$

$$Y_3 = \bar{C}\bar{D} + ABD + \bar{A}CD$$

# Combinational Logic System

Simplification of switching functions - K-map

AB \ CD	00	01	11	10
00	1	1		
01	1	1		
11			1	1
10			1	1

AB \ CD	00	01	11	10
00			1	1
01	1	1		
11			1	1
10	1	1		

AB \ CD	00	01	11	10
00				
01	1	1	1	1
11	1	1	1	1
10				



# Combinational Logic System

Simplification of switching functions - K-map

AB \ CD	00	01	11	10
00	1	1	1	1
01				
11				
10	1	1	1	1

AB \ CD	00	01	11	10
00	1	1	1	1
01	1			1
11	1			1
10	1	1	1	1

AB \ CD	00	01	11	10
00	1	1		
01		1	1	
11			1	1
10				1



# Combinational Logic System

Simplification of switching functions - K-map

AB \ CD				
	00	01	11	10
00		1		
01	1	1	1	1
11	1		1	1
10		1		

AB \ CD				
	00	01	11	10
00	1			1
01		1	1	1
11	1			
10	1		1	1

AB \ CD				
	00	01	11	10
00		1		1
01		1		1
11		1		1
10		1		1