

# *ECE216: Digital Electronics Laboratory*

*Exp 3: Understanding the combinational logic by  
implementing the Boolean function using  
multiplexer*

## Table of Content

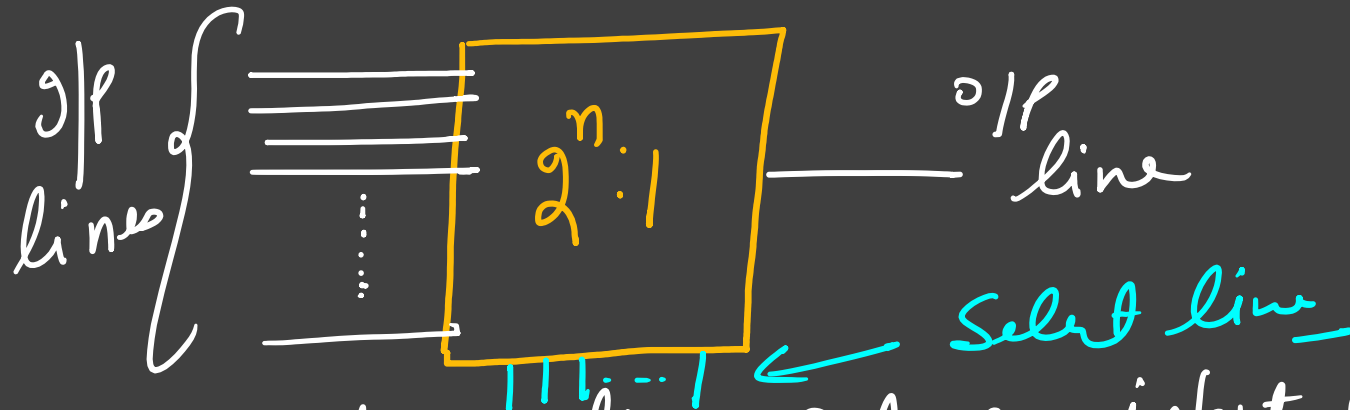
Task	Title	Page No.
Experiment 1	Understanding the combinational logic by implementing the boolean function using basic logic gates	1
Experiment 2	To design and analyze the circuit for Full adder and Full subtractor using Logic Gates.	6
<b>Practical work evaluation 1</b>	Practical work evaluation based on Experiment 1 and Experiment 2.	9
Experiment 3	Understanding the combinational logic by implementing the boolean function using multiplexer	12
Experiment 4	Understanding the combinational logic by implementing the boolean function using decoder	16
<b>Practical work evaluation 2</b>	Practical work evaluation based on Experiment 3 and Experiment 4.	20
<b>Project evaluation 1</b>	Design and Implementation of application-based projects-1	23

Experiment 5	Understanding the sequential logic by implementing the flip flop with the help of logic gates	26
Experiment 6	Understanding the sequential logic by implementing the counter with flip flop.	28
<b>Practical work evaluation 3</b>	Practical work evaluation based on Experiment 5 and Experiment 6.	31
Experiment 7	To visualize the output of decade counter on seven segment display	34
Experiment 8	To implement and simulate combinational and sequential circuit using DSCH/Proteus.	37
<b>Practical work evaluation 4</b>	Practical work evaluation based on Experiment 7 and Experiment 8.	41
<b>Project evaluation 2</b>	Design and Implementation of application-based projects-2	44

# ★ Multiplexer

which can connect many i/p to one o/p.

No. of select line  $\rightarrow 2^n$   
No. of inputs  $\rightarrow 2^n$   
No. of o/p.



Note: At one time only one input will be connected to the o/p

Q: How to identify which input get connected with the o/p?

Ans: We need select line—

No. of select line for  $2^n:1$  mux are:  $n$

Ex       $2^n : 1$

$g \mid n=1$        $2:1$

$g \mid n=2$        $4:1$

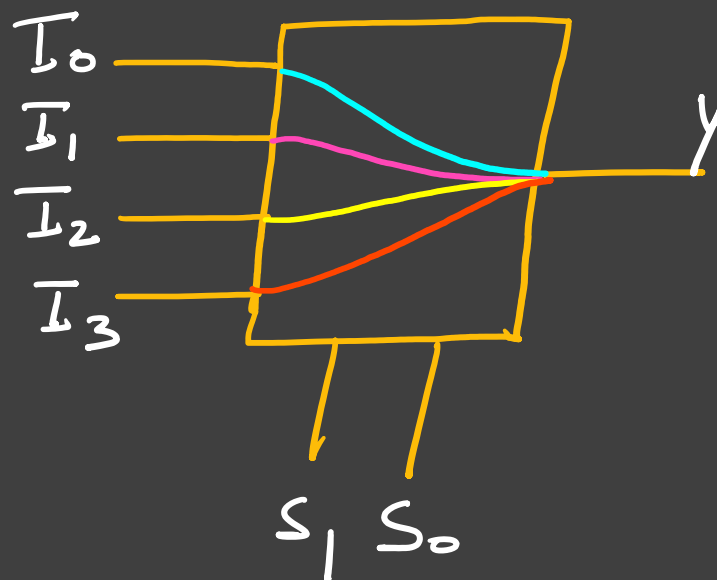
$g \mid n=3$        $8:1$

$g \mid n=4$        $16:1$

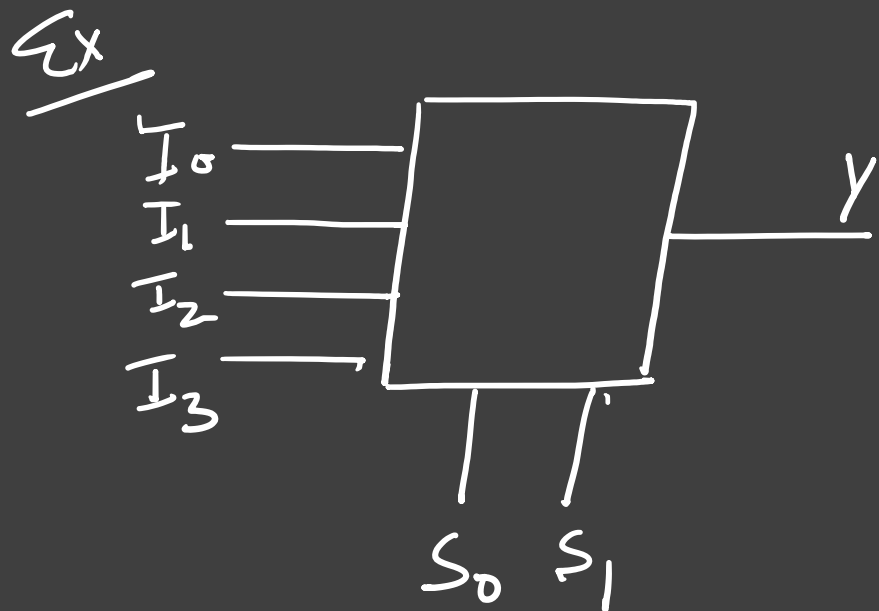
$g \mid n=5$        $32:1$

$\vdots$

$\vdots$



	$S_1$	$S_0$	$y$
0	0	0	$I_0$
1	0	1	$I_1$
2	1	0	$I_2$
3	1	1	$I_3$



$S_0$	$S_1$	$I_0$	$I_1$	$I_2$	$I_3$	$Y$
0	1	1	0	1	1	
1	0	0	1	0	1	

Anisha

LSB  
MSB

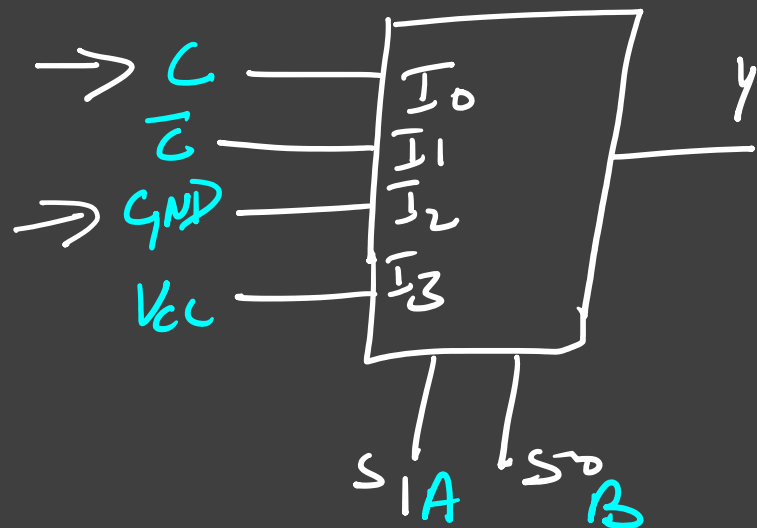
$$\left\{ \begin{array}{l} S_0 = 0 \\ S_1 = 1 \end{array} \right\}, \quad \begin{array}{l} I_1 = 0 \\ Y = 0 \end{array}$$

$$\frac{10}{2} \quad \begin{array}{l} I_2 = 1 \\ Y = 1 \end{array}$$

LSB  
MSB

$$\begin{array}{l} S_0 = 1 \\ S_1 = 0 \end{array} \quad \frac{01}{1} \quad \begin{array}{l} I_1 = 1 \\ \boxed{Y = 1} \end{array}$$

Ex



00 0  
 $I_0$

10 2  
 $I_2$

A	B	C	Y
0	0	1	1
1	0	1	0

## Experiment-3

### 1. Aim: To design a circuit to implement Boolean functions using Multiplexers.

Apparatus required: Multiplexer ICs (dual 4:1 mux 74153), 7404, Chords.

### 2. Learning objectives:

- a) How to realize functionality of Dual 4 Line to 1 Line Multiplexer using 74153 IC.
- b) How Dual 4 Line to 1 Line Multiplexer select the particular input to be sent to the output.

### 3. Theory:

It quite often happens, in the design of large-scale digital systems, that a single line is required to carry two or more different digital signals. Of course, only one signal at a time can be placed on the one line. What is required is a device that will allow us to select, at different instants, the signal we wish to place on this common line. Such a circuit is referred to as a Multiplexer. A multiplexer performs the function of selecting the input on any one of 'n' input lines and feeding this input to one output line.

Multiplexers are used as one method of reducing the number of integrated circuit packages





# ★ Understanding the combinational logic by implementing the Boolean function using multiplexer

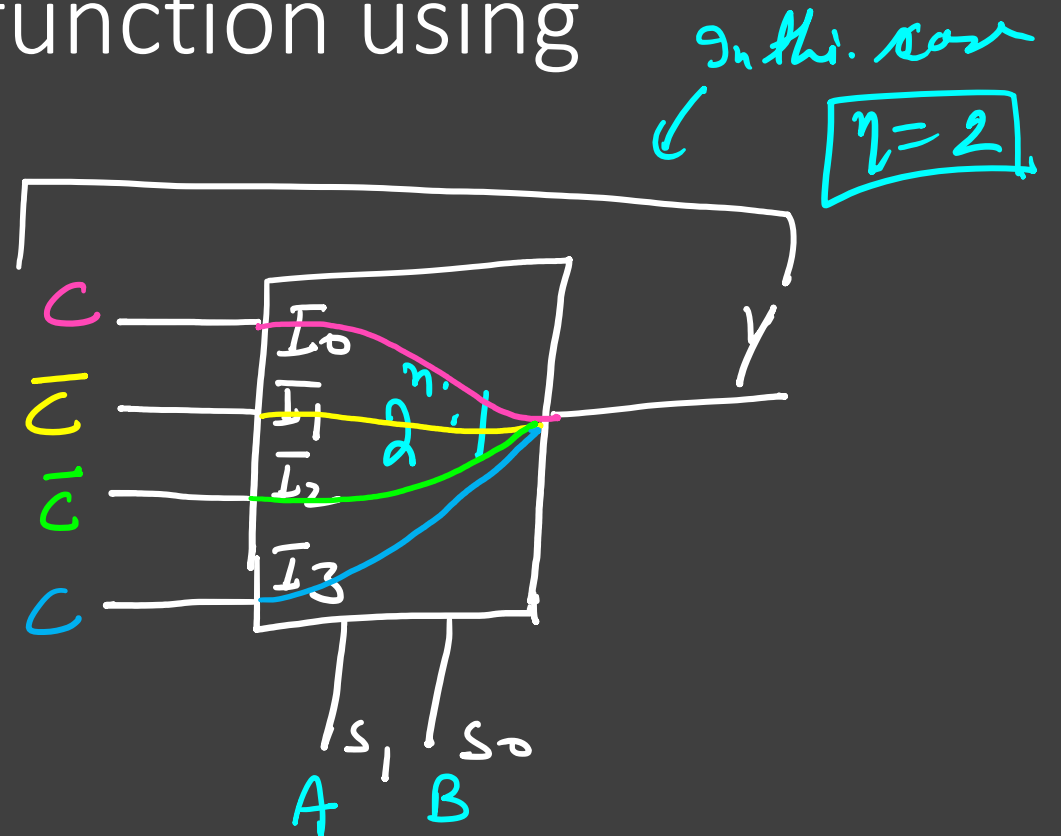
Ex: Sum of full adder

$$S = A \oplus B \oplus C$$

MSB      LSB

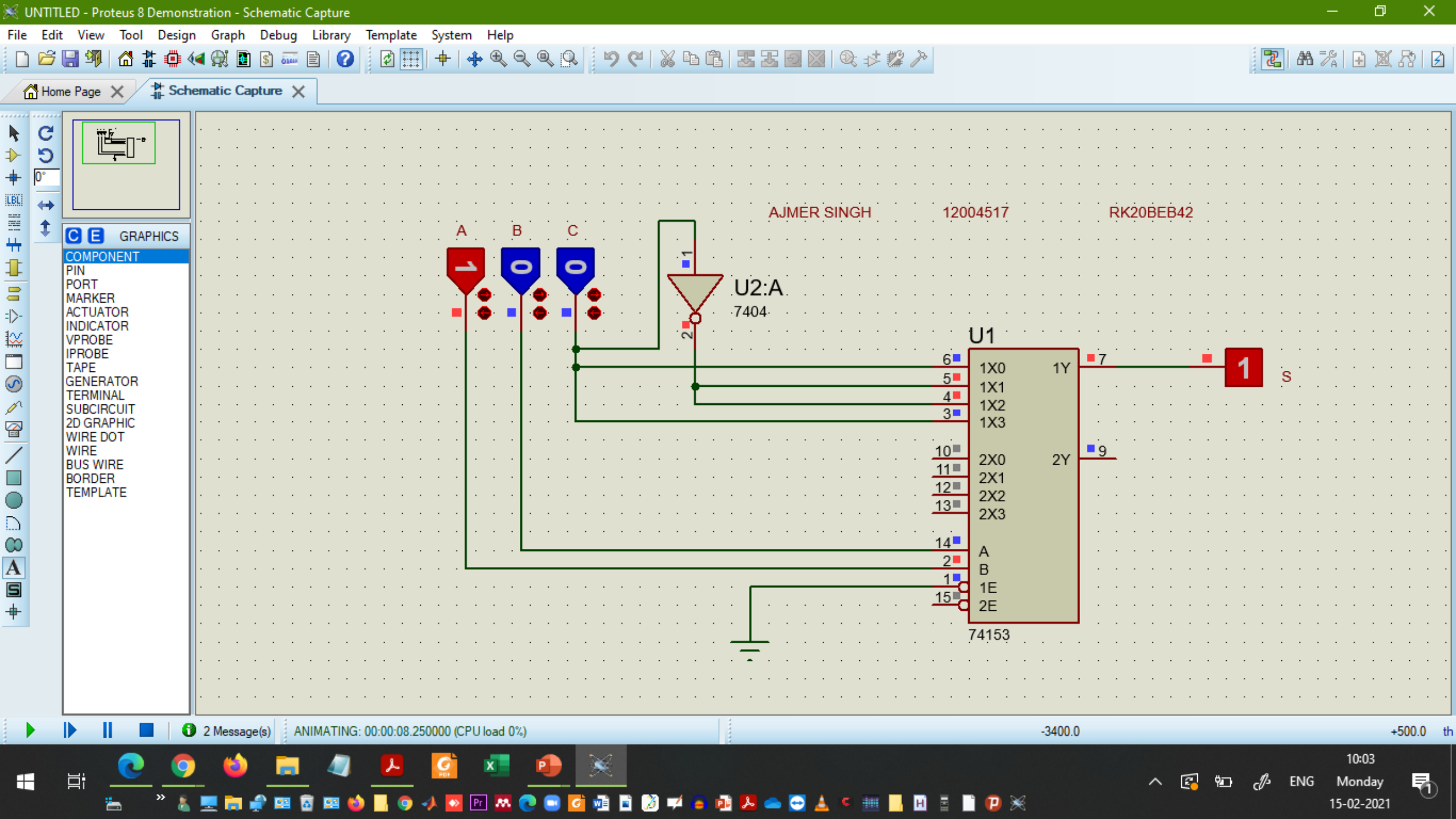
A	B	C	S
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

★ Allow consider the MSB Bits for select lines.



Q: How many Variable function can be implement using one  $2^n:1$  Mux

A:  $n+1$  Variables.



Ex Carry of full adder

$$C_{out} = (A \oplus B)C + AB$$

$s_1, s_0$

A	B	C	Carry
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

GND  
C  
C  
Vcc

0  $I_0$   
 1  $I_1$   
 2  $I_2$   
 3  $I_3$   
 4  $I_4$   
 5  $I_5$   
 6  $I_6$   
 7  $I_7$

Minterm Representation

$$C_{out} = \sum m(3, 5, 6, 7)$$

other the o/p is 0

SOP

Max term Representation

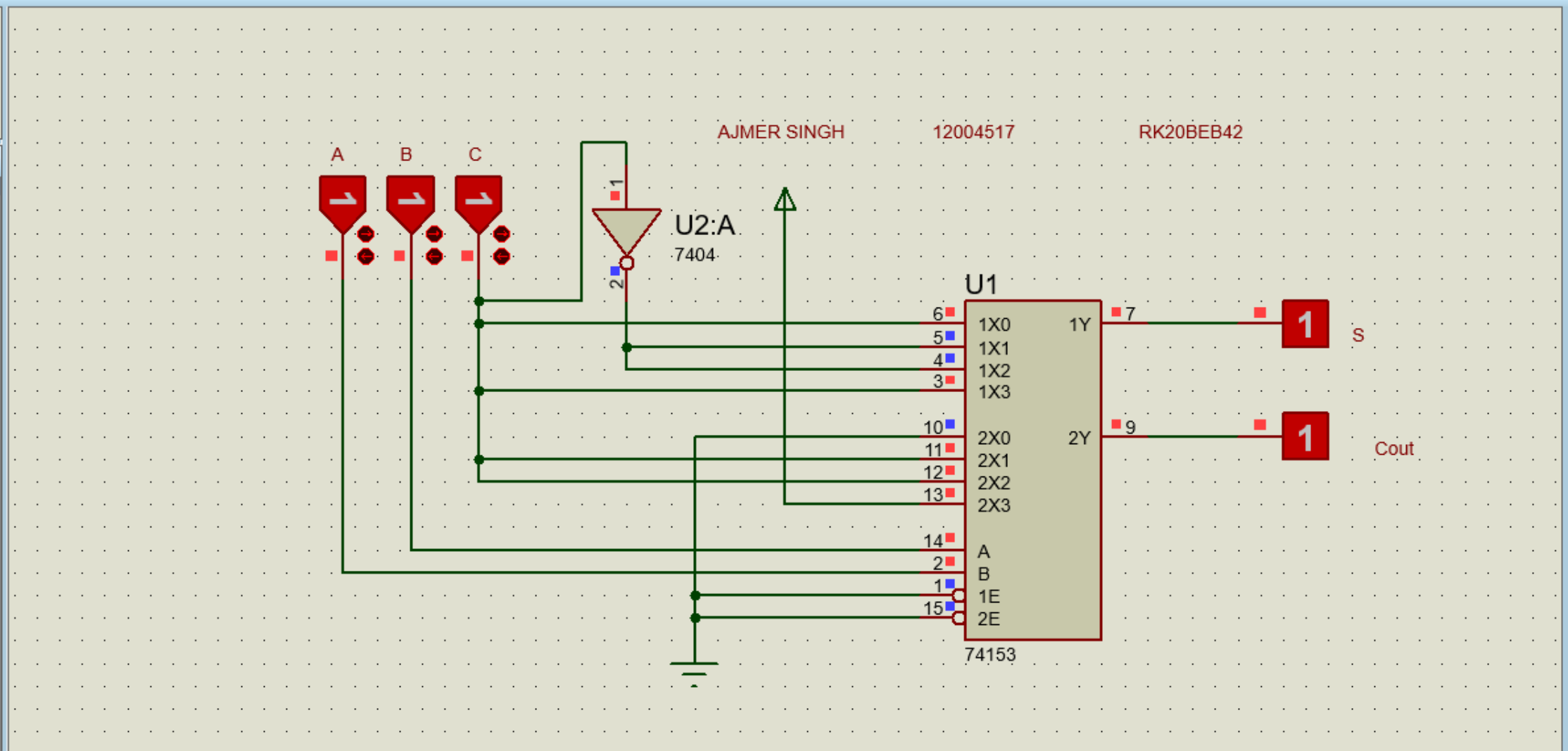
$$C_{out} = \prod M(0, 1, 2, 4)$$

other the o/p is one

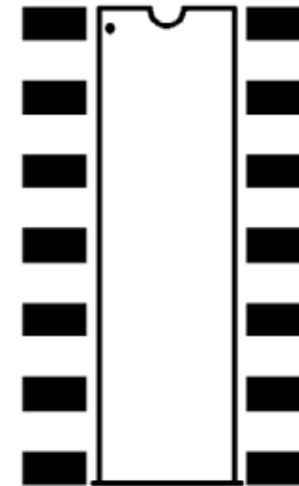
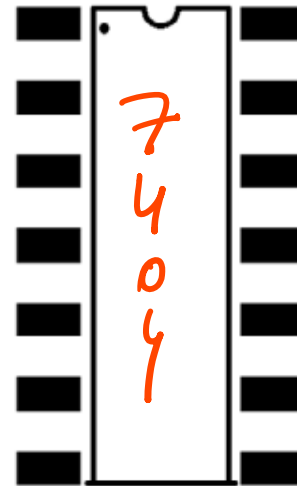
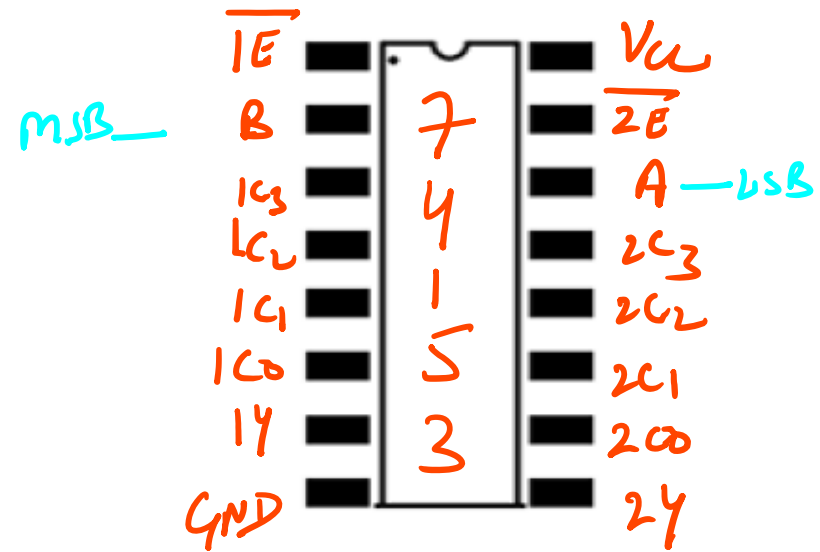
**GRAPHICS**

**COMPONENT**

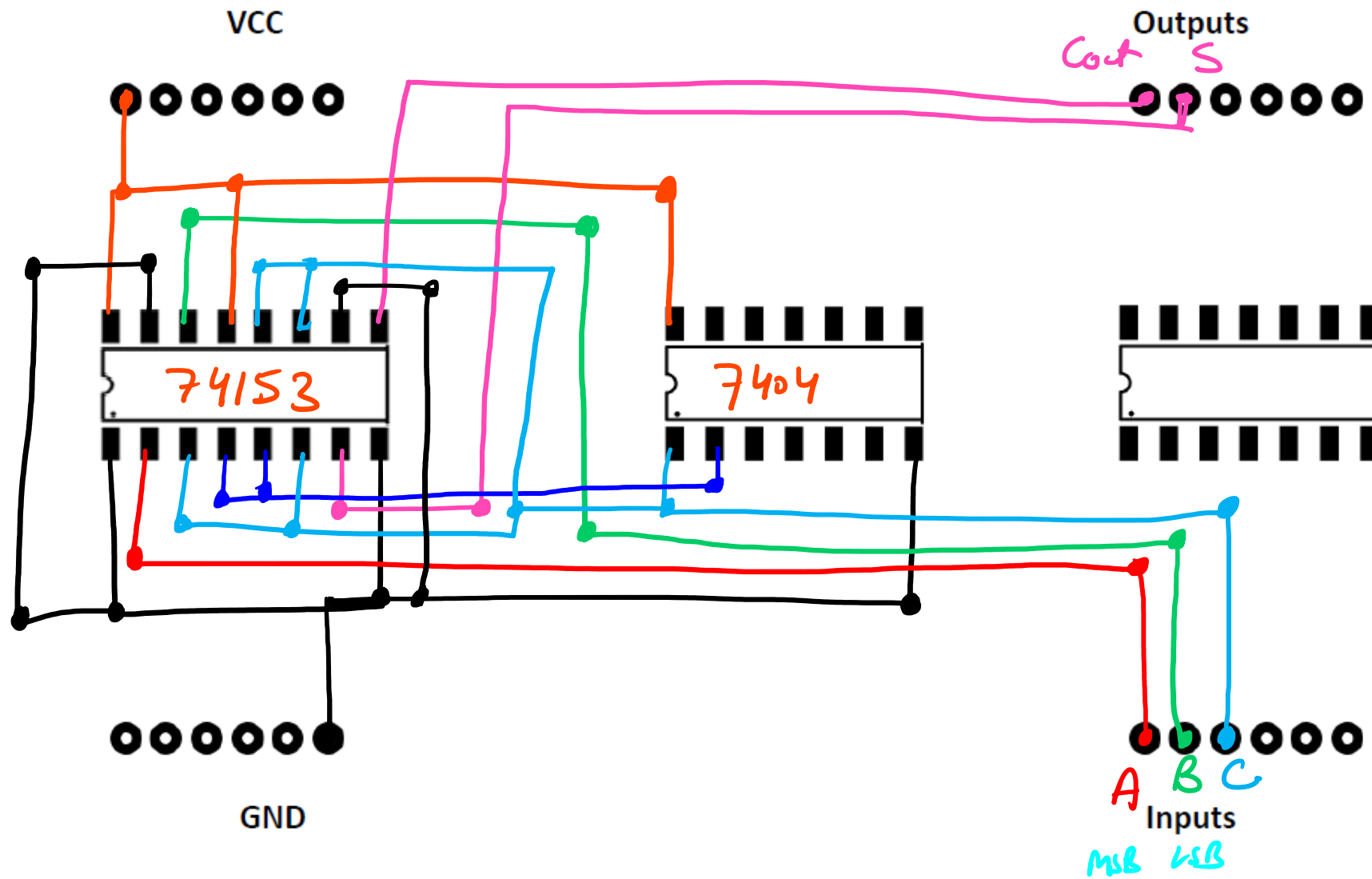
- PIN
- PORT
- MARKER
- ACTUATOR
- INDICATOR
- VPROBE
- IPROBE
- TAPE
- GENERATOR
- TERMINAL
- SUBCIRCUIT
- 2D GRAPHIC
- WIRE DOT
- WIRE
- BUS WIRE
- BORDER
- TEMPLATE



Pin configuration of ICs:



Draw Bread Board Connection diagram: *Full adder using mux*

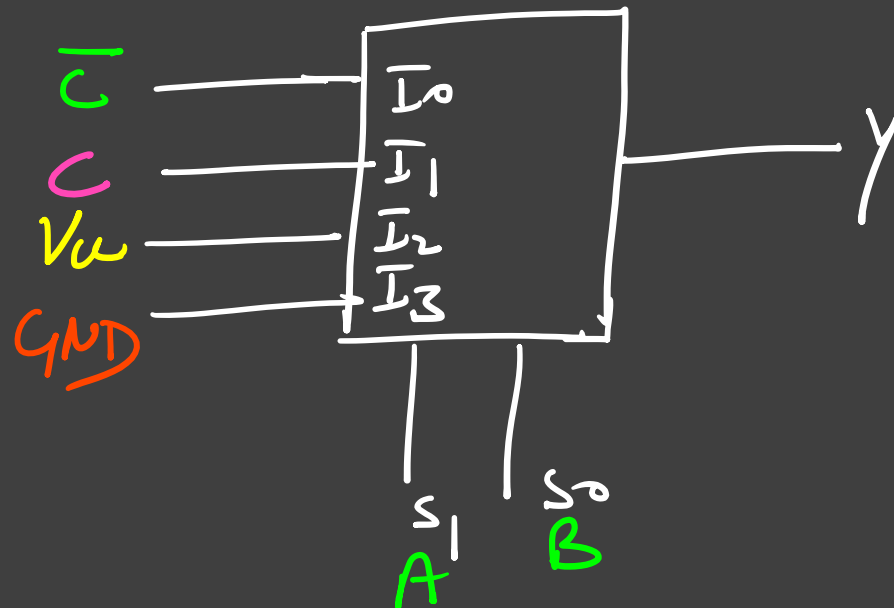


Ex Implement the following Boolean fun. using Mux

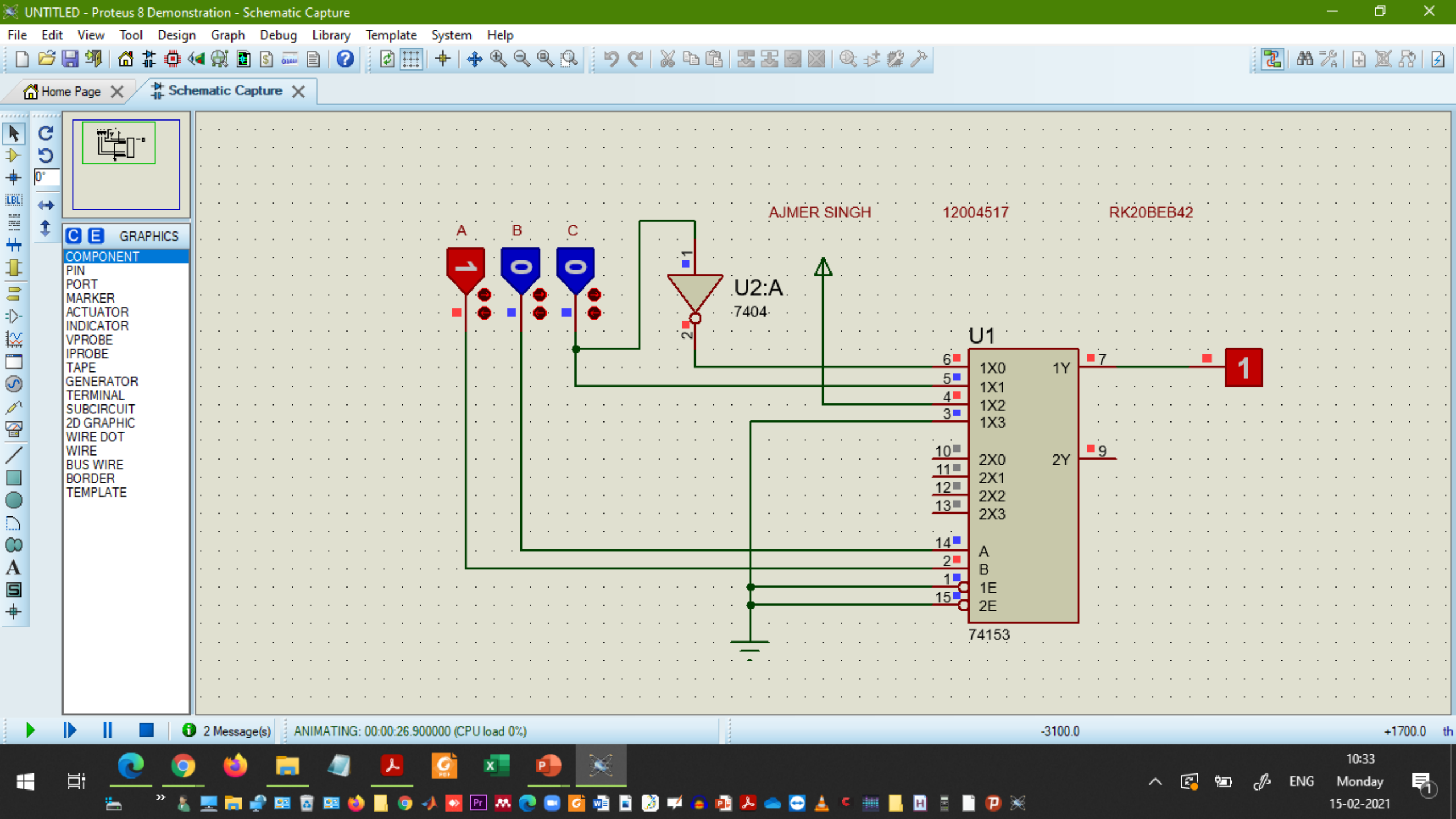
$$Y(A, B, C) = \sum m(\underline{0}, \underline{3}, \underline{4}, \underline{5})$$

msb  $s_1$   $s_0$  lsb

	A	B	C	Y
$I_0$	0	0	0	1
	0	0	1	0
$I_1$	0	1	0	0
	0	1	1	1
$I_2$	1	0	0	1
	1	0	1	1
$I_3$	1	1	0	0
	1	1	1	0







Ex Implement the following Boolean functions

(i)  $Y_1 = \sum m(\underline{0}, \underline{1}, \underline{5}, \underline{6})$

(ii)  $Y_2 = \prod M(\underline{0}, \underline{1}, \underline{3}, \underline{4})$

A	B	C	$Y_1$	$Y_2$
0	0	0	1	0
0	0	1	1	0
0	1	0	0	1
0	1	1	0	0
1	0	0	0	0
1	0	1	1	1
1	1	0	1	1
1	1	1	0	1

$V_{CC}$

GND

GND

$\bar{C}$

C

C

$\bar{C}$

$V_{CC}$