

ECE216: Digital Electronics Laboratory

Exp -8

Table of Content

Task	Title	Page No.
Experiment 1	Understanding the combinational logic by implementing the boolean function using basic logic gates	1
Experiment 2	To design and analyze the circuit for Full adder and Full subtractor using Logic Gates.	6
Practical work evaluation 1	Practical work evaluation based on Experiment 1 and Experiment 2.	9
Experiment 3	Understanding the combinational logic by implementing the boolean function using multiplexer	12
Experiment 4	Understanding the combinational logic by implementing the boolean function using decoder	16
Practical work evaluation 2	Practical work evaluation based on Experiment 3 and Experiment 4.	20
Project evaluation 1	Design and Implementation of application-based projects-1	23

Experiment 5	Understanding the sequential logic by implementing the flip flop with the help of logic gates	26
Experiment 6	Understanding the sequential logic by implementing the counter with flip flop.	28
Practical work evaluation 3	Practical work evaluation based on Experiment 5 and Experiment 6.	31
Experiment 7	To visualize the output of decade counter on seven segment display	34
Experiment 8	To implement and simulate combinational and sequential circuit using DSCH/Proteus.	37
Practical work evaluation 4	Practical work evaluation based on Experiment 7 and Experiment 8.	41
Project evaluation 2	Design and Implementation of application-based projects-2	44

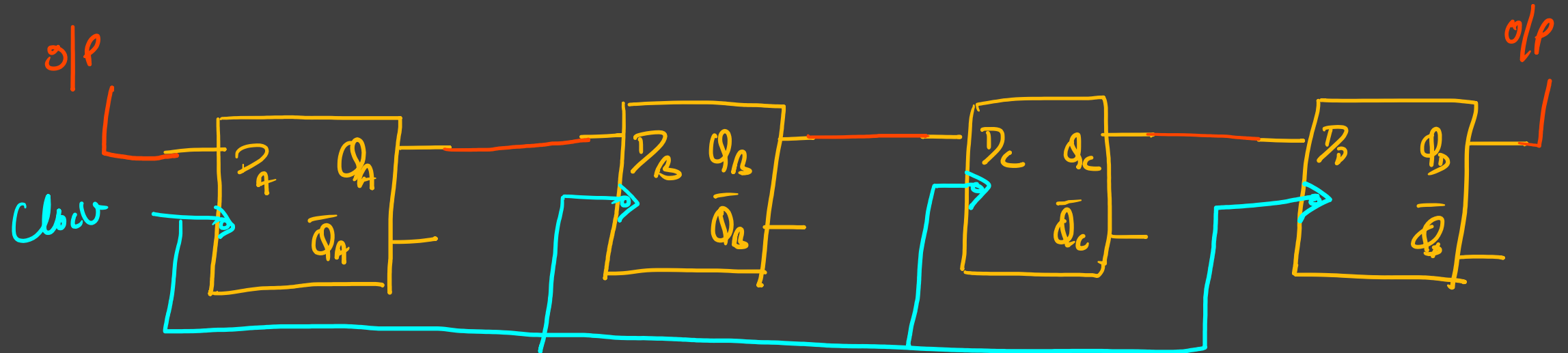
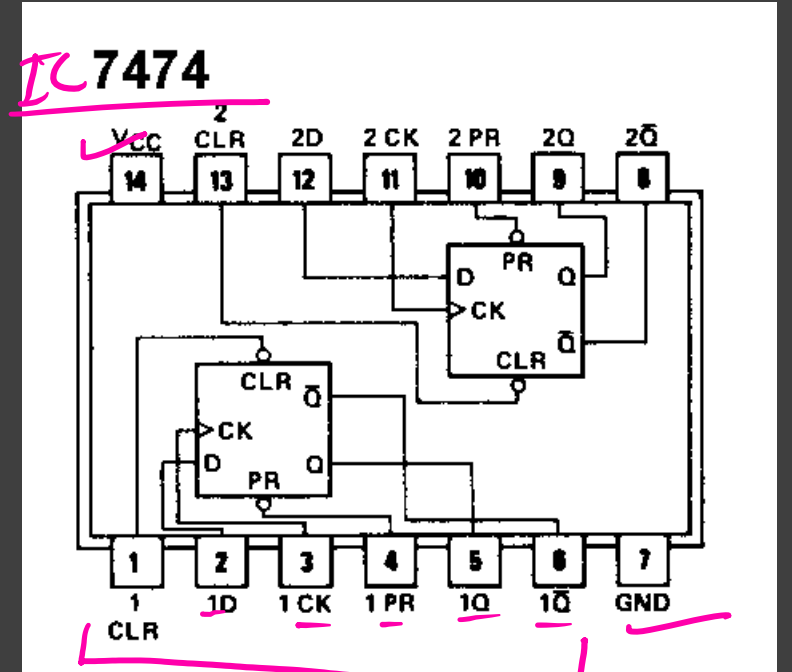
Experiment 8: To implement the shift register (SISO, PISO, SIPO, PIPO) OR

Understanding the sequential logic by implementing the shift register with flip flop.

★ Type of shift registe.

- 1) SISO
 - 2) SIPO
 - 3) PISO
 - 4) PIPO
- } 4-bit

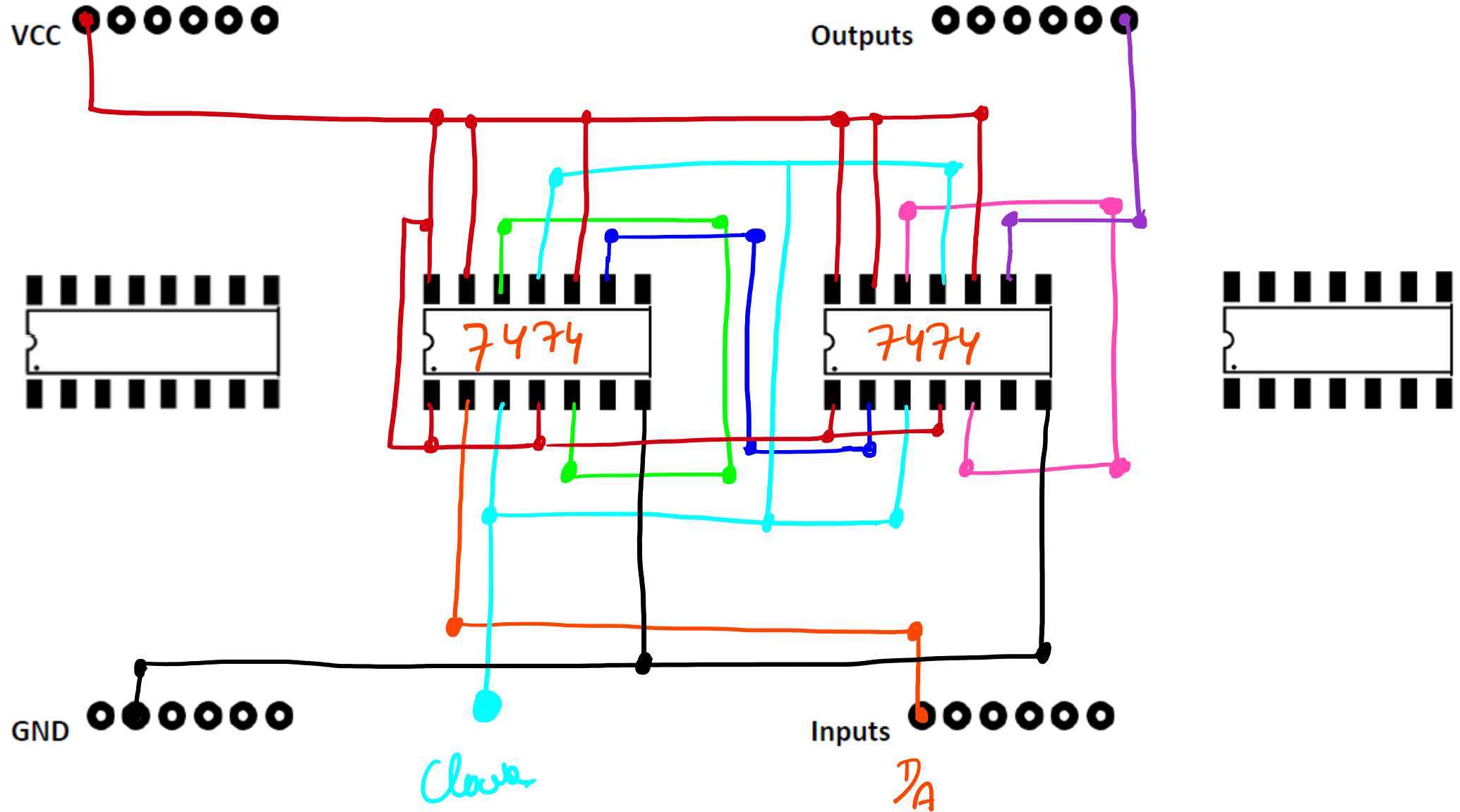
SISO

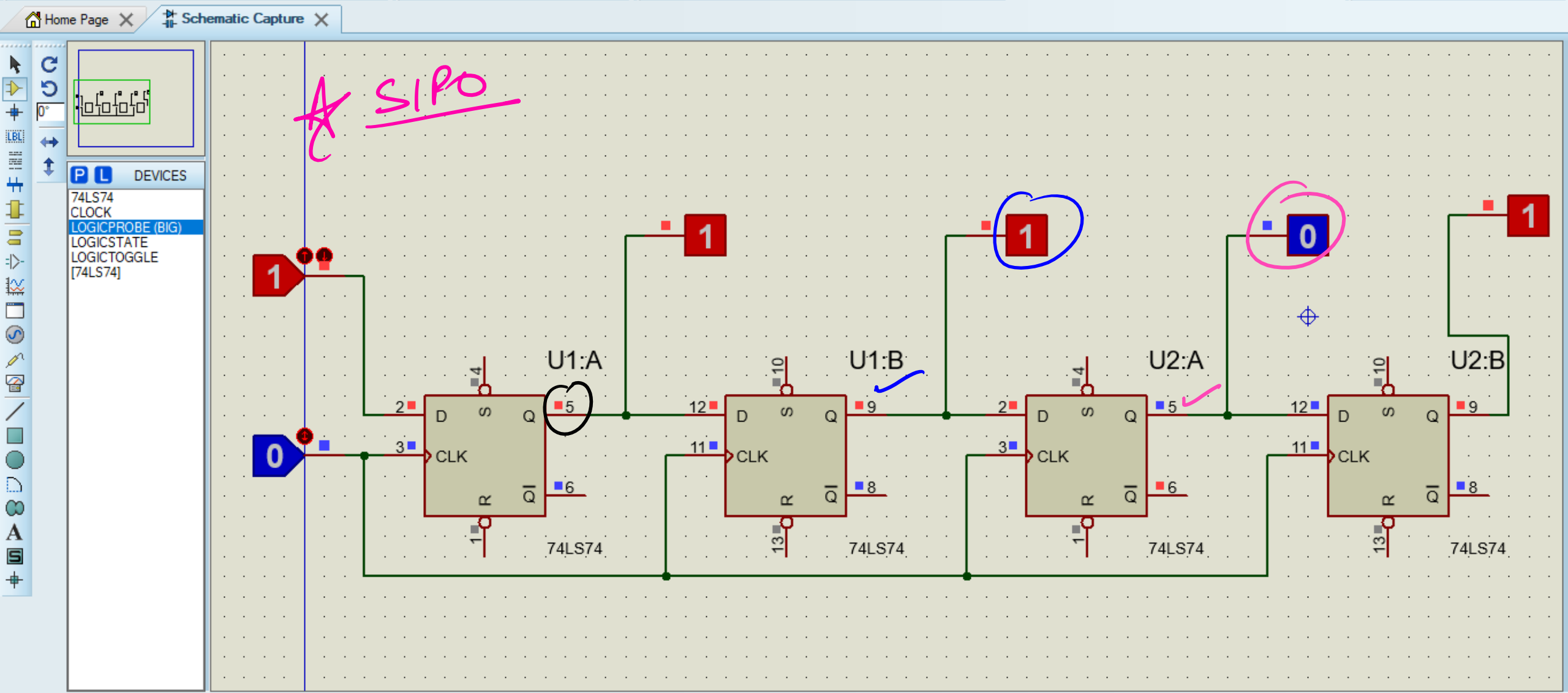




Draw Bread Board Connection diagram:

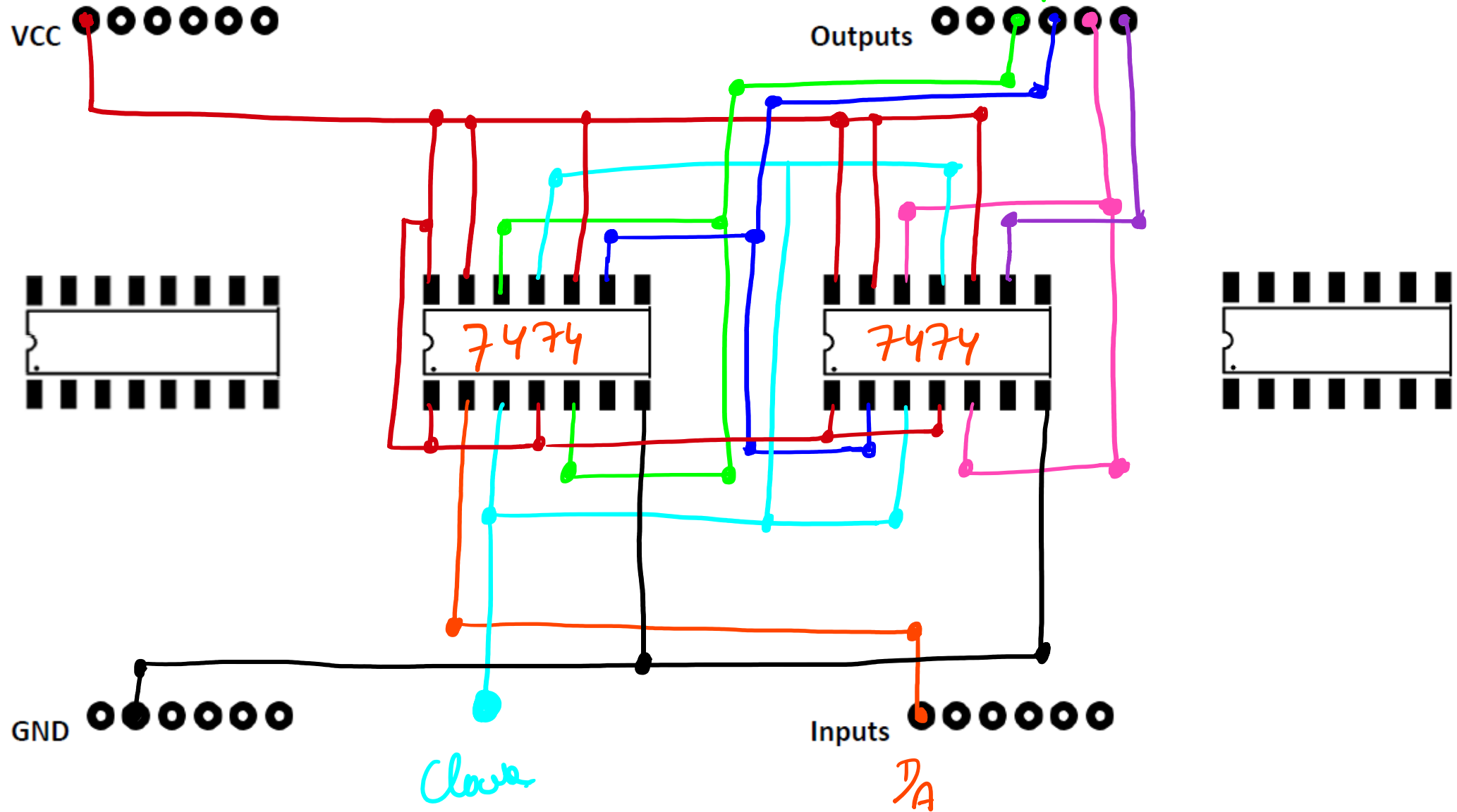
4-bit SI SO shift Reg





Draw Bread Board Connection diagram:

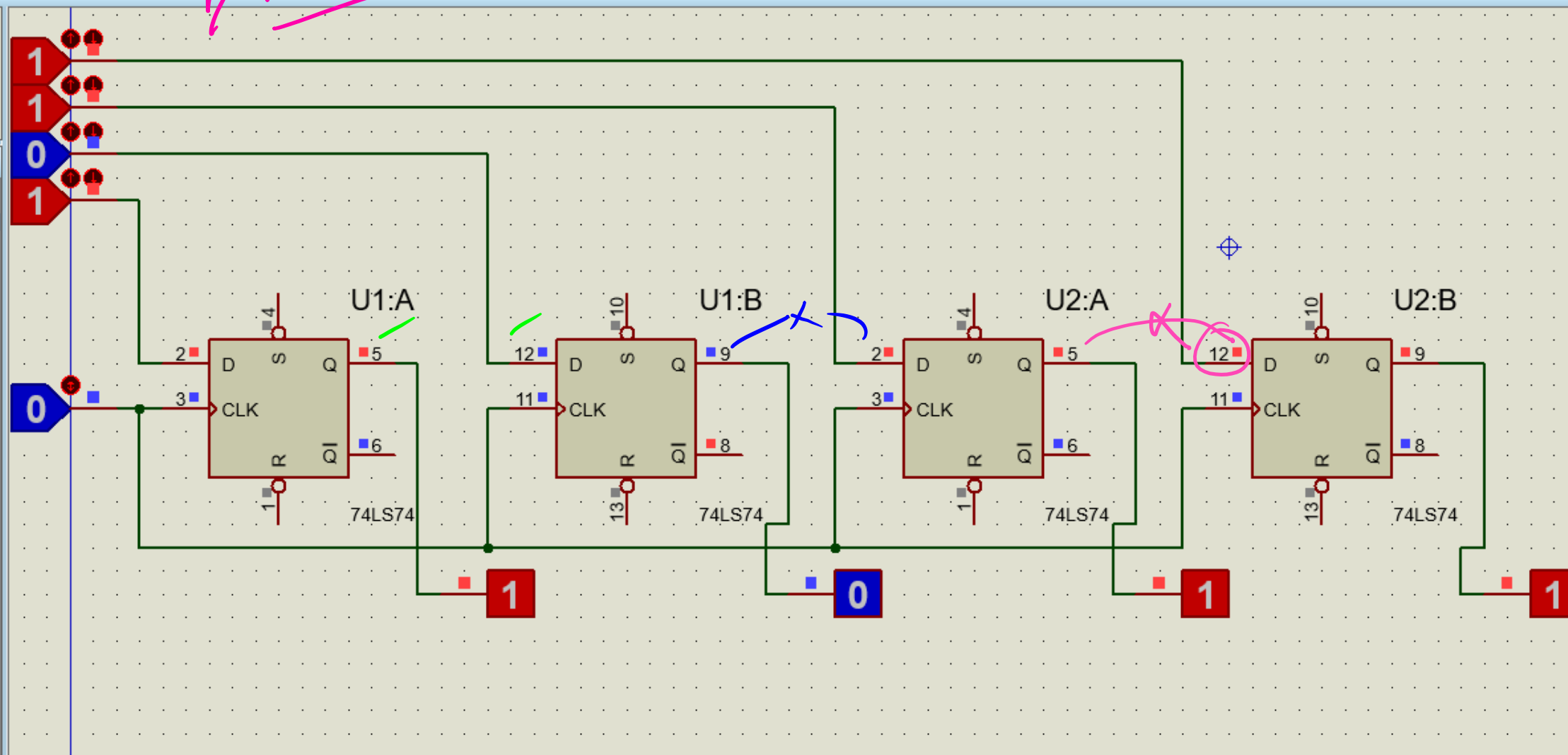
4-bit SISO shift Reg



★ PIP0

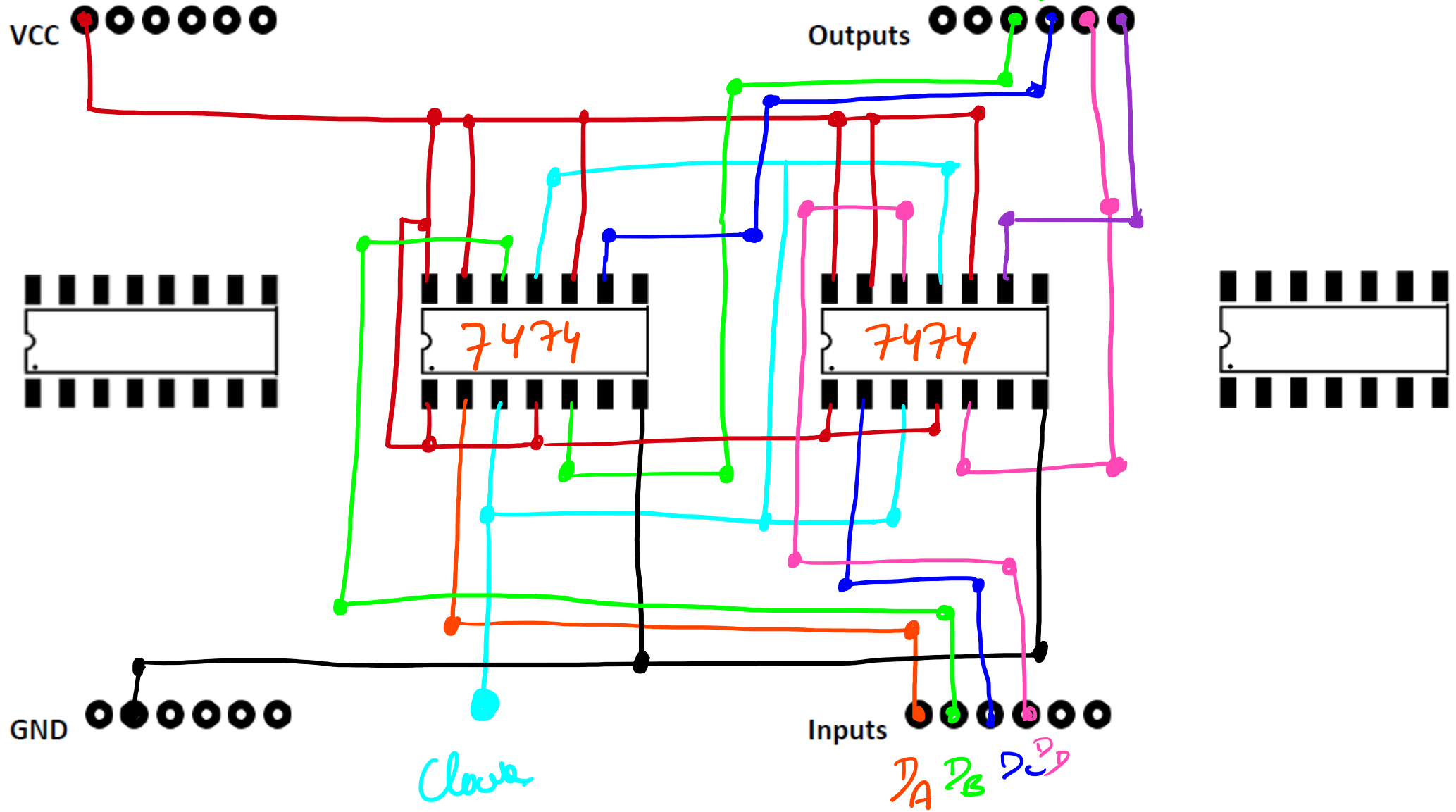
DEVICES

- 74LS74
- CLOCK
- LOGICPROBE (BIG)
- LOGICSTATE
- LOGICTOGGLE [74LS74]



Draw Bread Board Connection diagram:

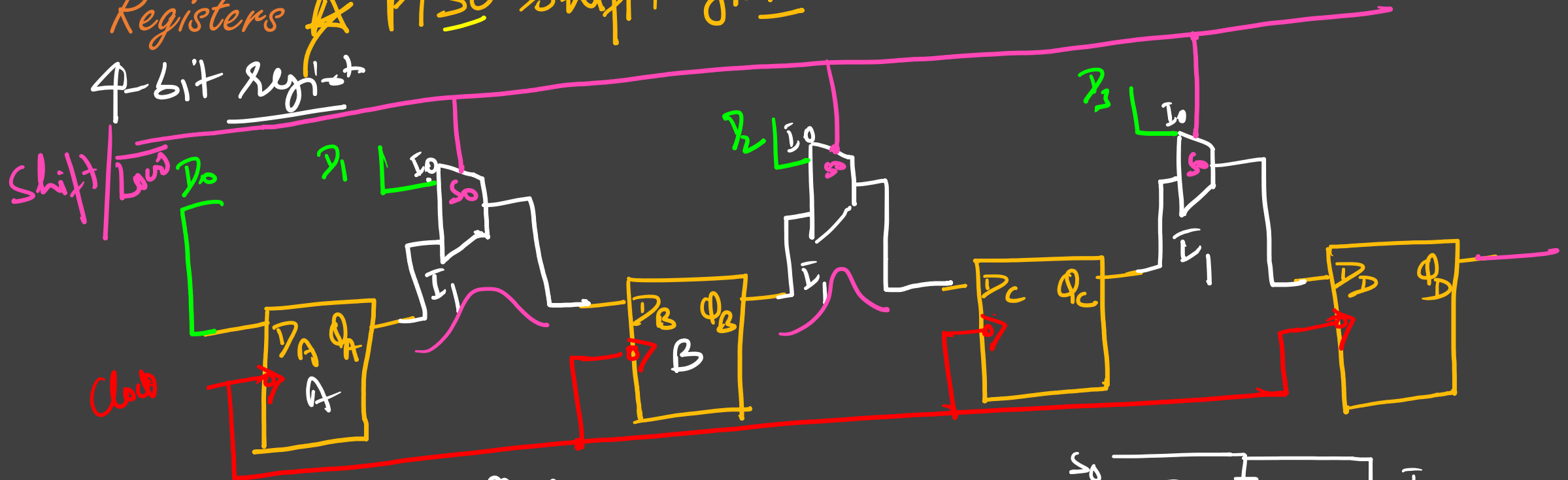
4-bit PI PO shift Reg



Sequential Logic Circuits Applications

Registers & PISO shift register

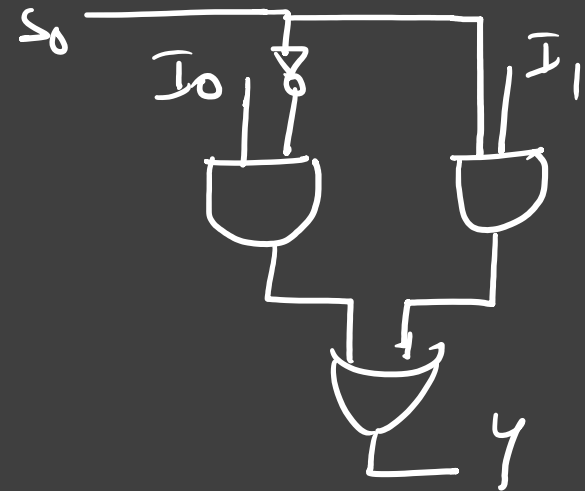
4-bit Register



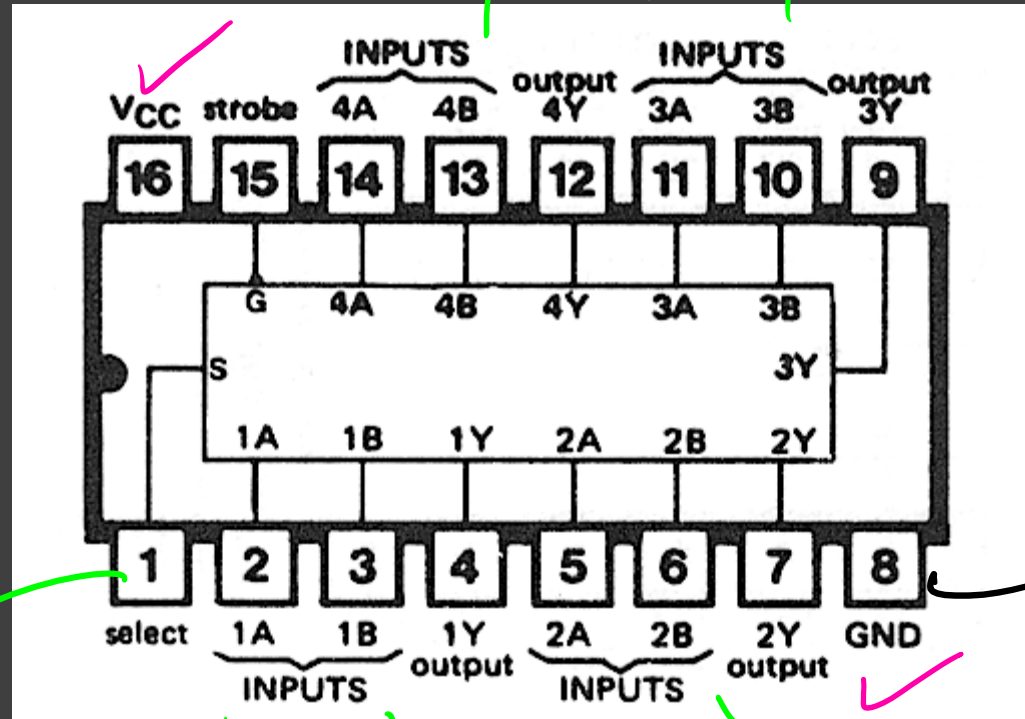
Shift/Load
R/W

2:1 Mux
4:1 Mux

74153

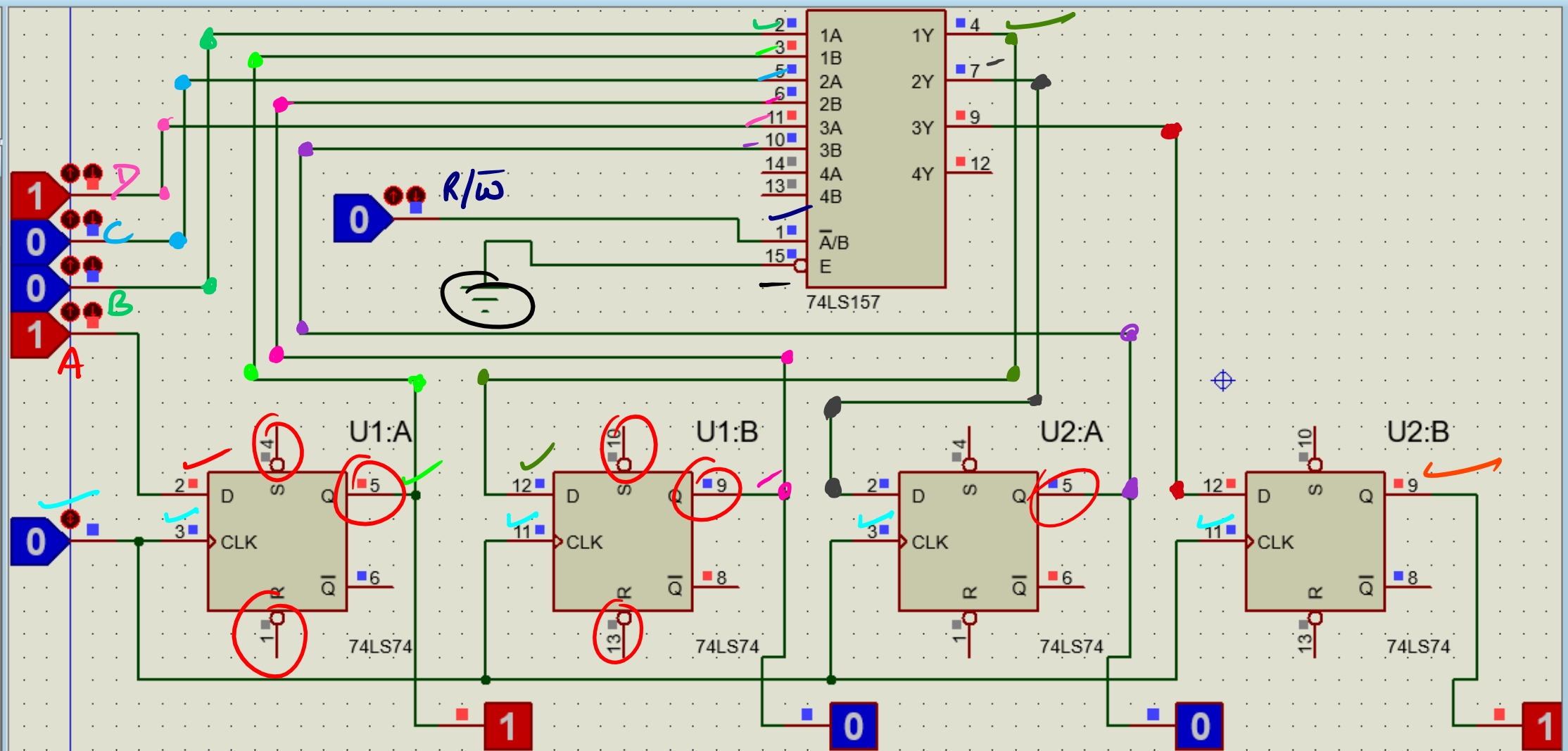


IC 74157

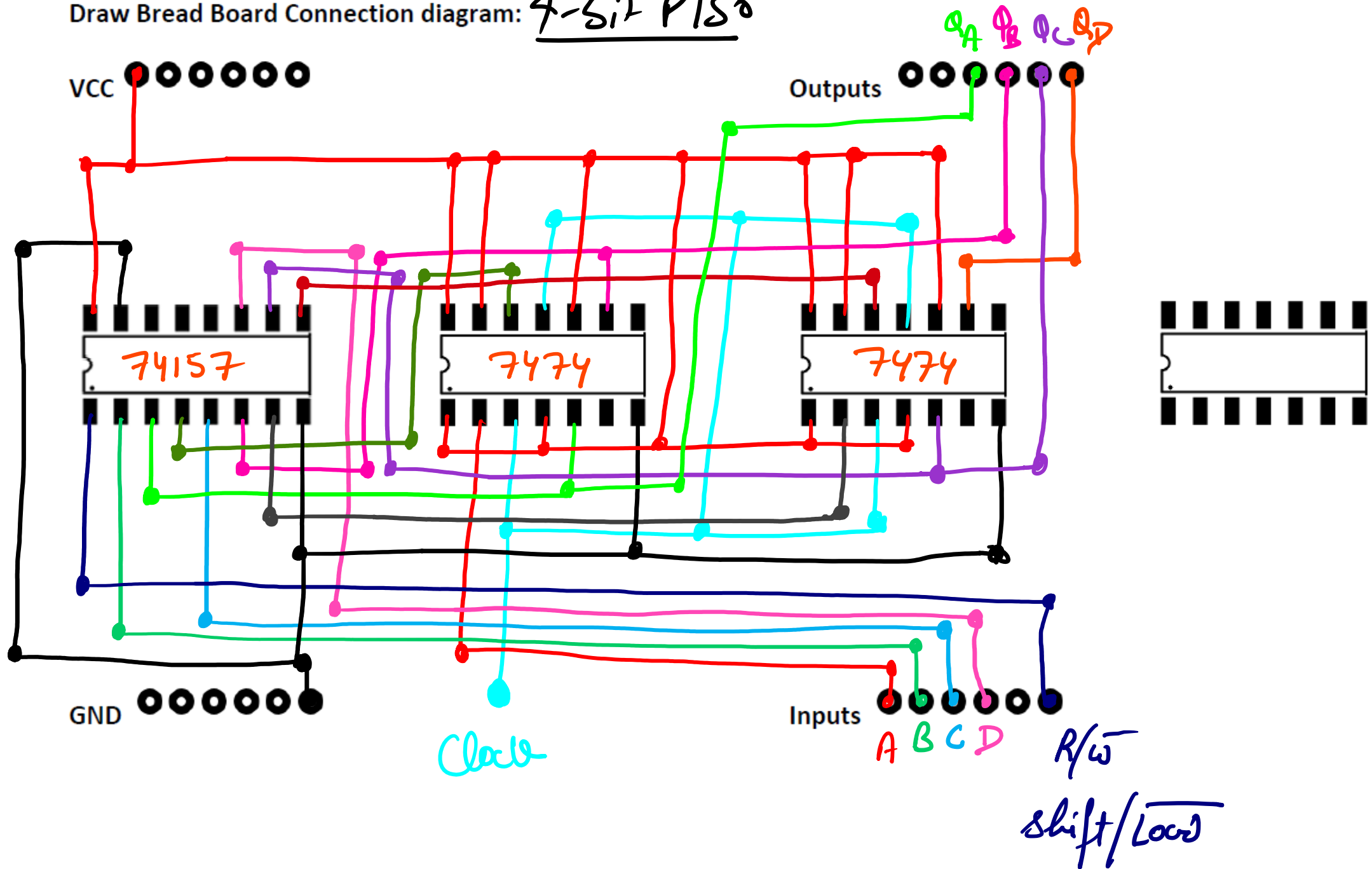


DEVICES

- 74LS74
- 74LS157
- CLOCK
- LOGICPROBE (BIG)
- LOGICSTATE
- LOGICTOGGLE
- [74LS74]



Draw Bread Board Connection diagram: 4-bit PISO



Next CA4 (4 May 2021)
(03-04 PM) Google

Exps Exp 7, Exp 8

- 10 Marks (write up)
 - 10 Marks (Similes)
 - 10 Marks (MCQ/Short Answer)
- } 1 hr

Next to Next

7 May 2021
(05-06 PM) Google

Exps Exp 5, 6, 7, 8

30 MCQ, 1 Mark each
with 0.25 - 0.5 Marks
for wrong answers

1 hr