What is concurrency?

Concurrency

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KTH

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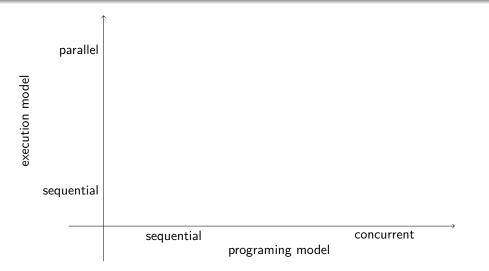
Concurrency: (the illusion of) happening at the same time.

A property of the programming model.

Why would we want to do things concurrently?

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concurrency vs parallelism



concurrency models

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- Shared memory: modify a shared data structure
 - C++/C
 - Java
- Message passing: processes send and receive messages
 - Erlang/Elixir
 - Go
 - Scala
 - Occam
 - Rust
 - Smalltalk

There are more, but these are the two large groups.

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how do we send messages

actor model

- Communicating Sequential Processes (CSP), messages are sent **through channels**, a process can choose to read a message from one or more channels
 - Go, Occam, Rust
- Actor model, messages are sent to a process, a process reads implicitly from its own channel
 - Smalltalk, Erlang/Elixir, Scala

An actor:

- state: keeps a private state that can only be changed by the actor
- receive: has one channel of incoming messages
- execute: given a state and a received message, the actor can
 - send: send a number of messages to other actors
 - spawn: create a number of new actors
 - transform: modify its state an continue, or terminate

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ordering of messages

naming of actors

Is the set of messages to an actor ordered?

In what order should the messages be handled?

The evaluation of a function is deterministic, how about the execution of an actor?

How can an actor direct a message to a specific actor?

Do we have a global naming scheme?

How do we find the identifier of an actor?

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exceptions

process identifier

What should we do if we're sending a message to an actor that has terminated?

What if we're waiting for a message that will never be sent?

Are sent messages guaranteed to arrive?

We introduce one additional data structure:

 $Structures = \{Process \ identifiers\} \cup Atoms \cup \{\{s_1, s_2\} | s_i \in Structures\}$

There is no term, nor pattern, that corresponds to an identifier.

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spawn

send

A new process is spawned by giving it a function to evaluate,

... the result is a process identifier (pid).

or

In the later case, the function must be exported from the module.

Given a process identifier, an arbitrary data structure can be sent to the process.

send(pid, message)

In Erlang this was written using an operator !, often called "bang".

receive example

We extend expressions:

```
<expression> ::= <receive expression> | ...
<receive expression> ::=
    receive do <clauses> end

similar to case expressions
```

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side effects

Who am I?

In a *pure functional* program, the only effect of evaluating an expressions is the returned value - not any more.

One more built-in function:

myPid = self()

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few extensions

order of messages

Few extensions to the functional subset:

- pid: a process identifier as a data structure
- spawn: creating a process, returning a pid
- send: sending of messages to a pid
- receive: selective receive of messages
- self: the process identifier of the current process

All constructs can, apart from the receive statement, almost be given a functional interpretation.

Our operational semantics does not give us any understanding of the execution.

Message passing is: unreliable FIFO.

```
:
send(pid, {:this, :is, :message, 1})
send(pid, {:this, :is, :message, 2})
send(pid, {:this, :is, :message, 3})
:
```

What could be the result at the receiving end?

How many messages are lost in reality?

order of messages

selective receive

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```
def sum(s) do
  receive do
    {:add, x} -> sum(s + x)
    {:sub, x} -> sum(s - x)
    {:mul, x} -> sum(s * x)
  end
end
```

Assume we spawn a process given the expression $fn() \rightarrow sum(10)$ end, and the sequence of messages in the queue is:

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```
{:sub, 4}, {:add, 10}, {:mul, 4}, {:mul, 2}, {:sub, 10}
```

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selective receive

```
implicit deferral
```

Assume we spawn $fn() \rightarrow closed(4)$ end and the sequence of messages is:

```
{:sub, 4}, :open, {:mul, 4}, {:add, 2}, :close {:add, 2}, :done
```

In every receive expression we start from the beginning of the queue.

Selective receive: we specify which messages we are willing to accept.

Implicit deferral: messages that we do not explicitly receive, remain in the message queue.

We could have chosen *fifo receive* i.e. messages must be received in the order they have in the message queue (Actors model).

We could have chosen *explicit deferral*, but then we would have to state which messages that should be handled later.

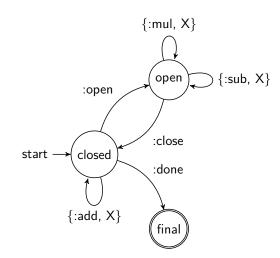
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how to describe a processes

- Finite state machine (FSM) : describing the states where messages determine transitions
- Sequence diagram : how processes interact, protocol definitions
- Flow-based Programming (FBP) : architecture view of processes
- Domain Specific Language : describe the systems in a high level programming language

...

finite state machine - FSM



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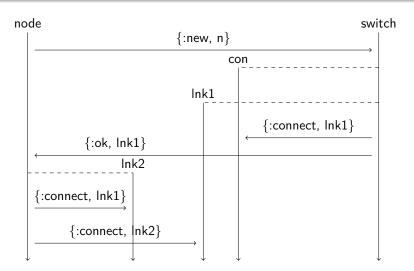
finite state machine - FSM

Elixir receive statements are not a direct realization of a finite state machine.

Messages that arrive too early in a finite state automata would give us an undefined state.

The *implicit deferral* give us a very simple description of a finite state machine where messages are allowed to arrive too early.

sequence diagram



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flow-based programming

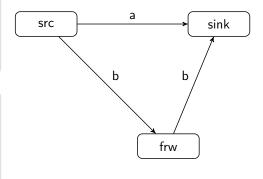
Receive request Process request Router Return response Process response Back-end Ba

Figure: from J Paul Morrison www.jpaulmorrison.com

time to deliver

```
src/2
def src(sink, frw) do
    send(sink, :a)
    send(frw, :b)
end
```

frw/2 def frw(sink) do receive do msg -> send(sink,msg) end end



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example account example account

recieve do {:deposit, money} -> def check(acc) do acc(saldo + money) send(acc, {:request, self()}) {:withdraw, money} -> receive do acc(saldo - money) {:saldo, saldo} -> {:request, from} -> saldo send(from, {:saldo, saldo}) end acc(saldo) end end

def acc(saldo) do

end

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summary

- asynchronous: messages are sent and eventually (hopefully) delivered
- FIFO: message delivery is ordered
- selective receive: the receiver decides the order of handling messages
- implicit deferral: messages remain in the queue until handled
- \bullet diagrams: finite state machines, sequence diagrams, flow-based program