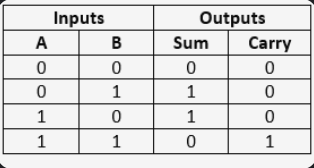
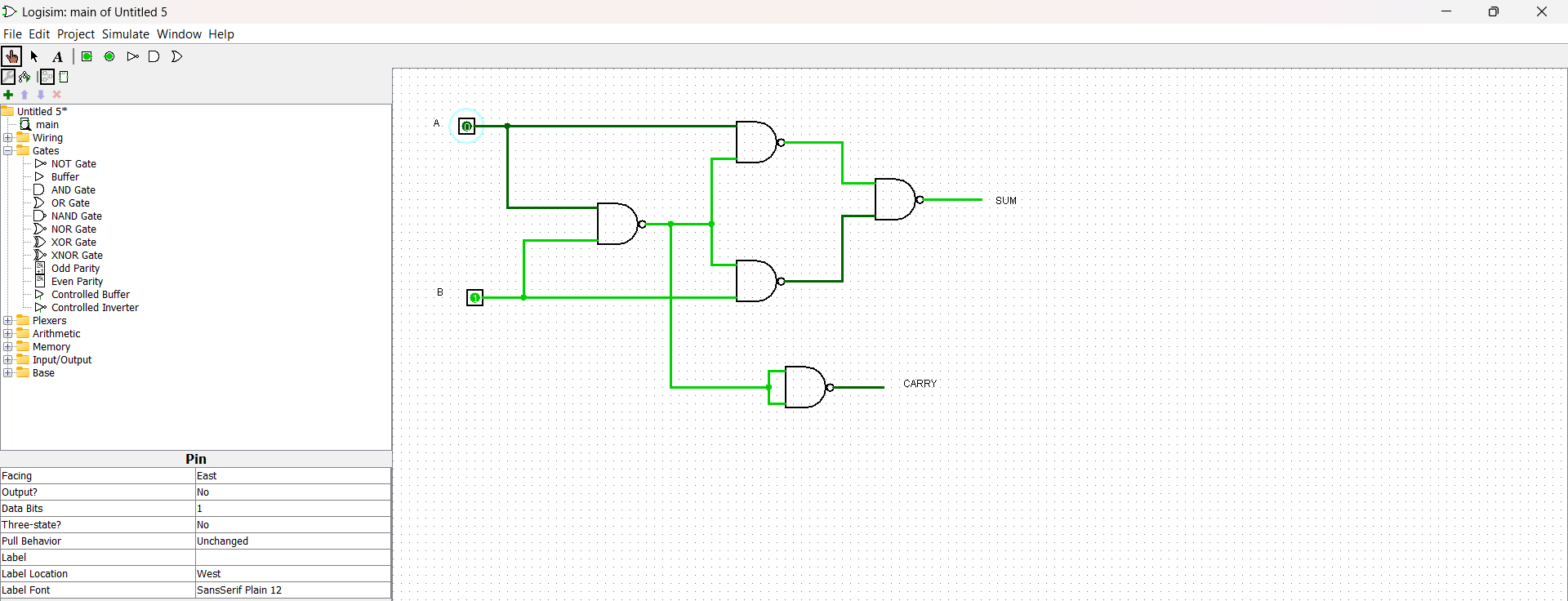
HALF ADDER USING NAND GATES

AIM: To implement the half adder using nand gates through logic sim

INPUT:



OUTPUT:



RESULT: Thus the program was executed successfully using logic sim.