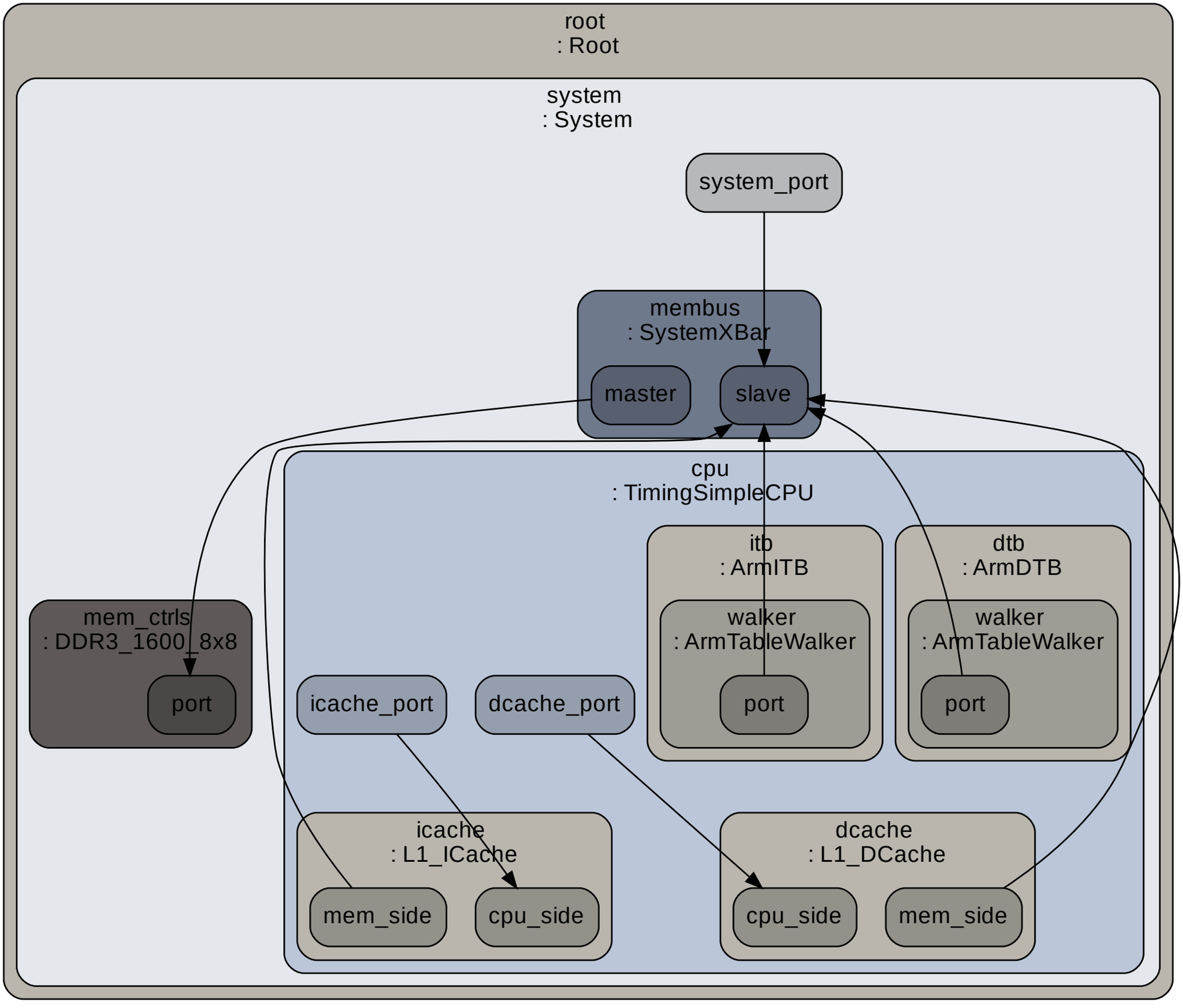
root
: Root
system
: System
system_port
membus
: SystemXBar
master
slave
cpu
: TimingSimpleCPU
itb
: ArmITB
walker
: ArmTableWalker
port
dtb
: ArmDTB
walker
: ArmTableWalker
port
mem_ctrls
: DDR3_1600_8x8
port
icache_port
dcache_port
icache
: L1_ICache
mem_side
cpu_side
dcache
: L1_DCache
cpu_side
mem_side