

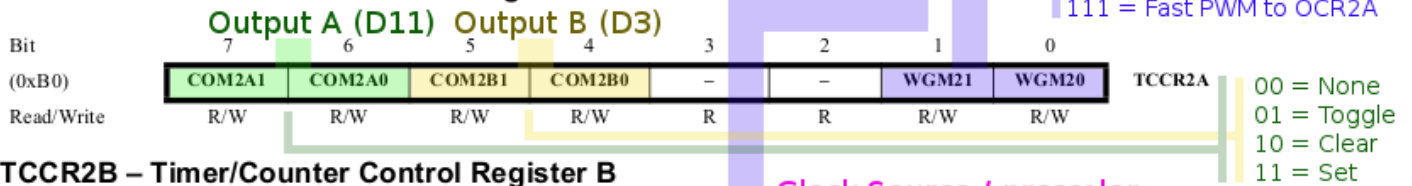
ATmega328P Timer 2

D3 = PD3 (chip pin 5)
D11 = PB3 (chip pin 17)

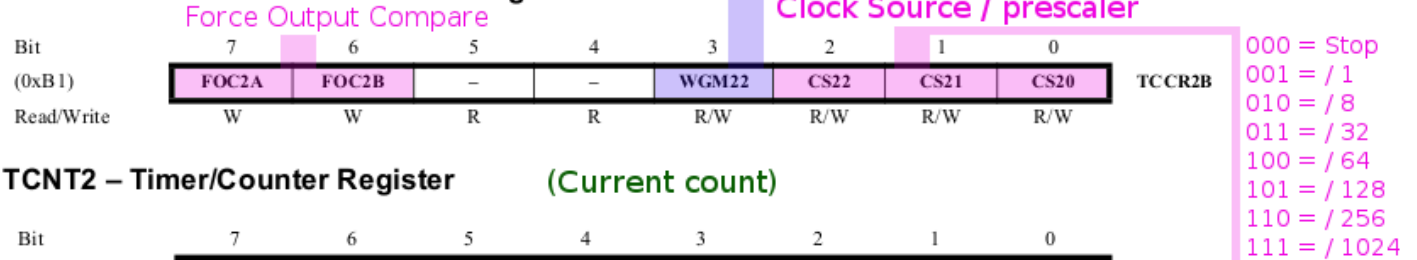
Timer Mode

000 = Normal
001 = PWM to 255
010 = CTC to OCR2A
011 = Fast PWM to 255
100 = n/a
101 = PWM to OCR2A
110 = n/a
111 = Fast PWM to OCR2A

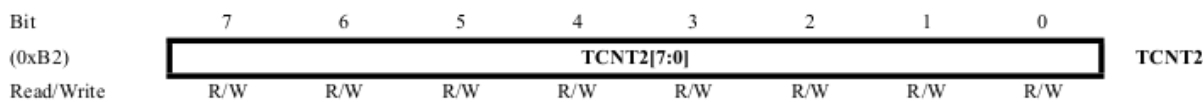
TCCR2A – Timer/Counter Control Register A



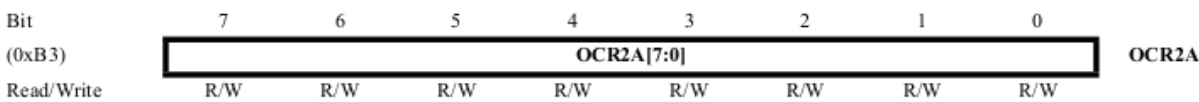
TCCR2B – Timer/Counter Control Register B



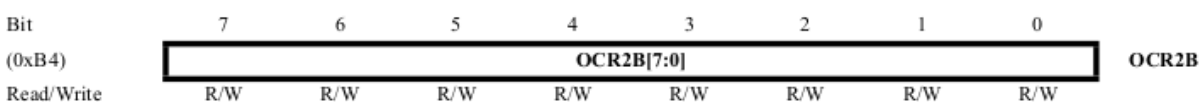
TCNT2 – Timer/Counter Register (Current count)



OCR2A – Output Compare Register A (Compare A limit)



OCR2B – Output Compare Register B (Compare B limit)



TIMSK2 – Timer/Counter2 Interrupt Mask Register

Interrupt Enable

Compare B / Compare A / Overflow



TIFR2 – Timer/Counter2 Interrupt Flag Register

Interrupt Flag

Compare B / Compare A / Overflow

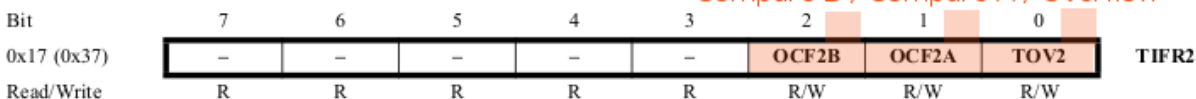


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More information at: http://www.gammon.com.au/timers
Date: 11th August 2015