Test of the **SAMD** boards:

(NOTE 1: For all boards US915 region applied)

(NOTE 2:Catena\_hello.ino sketch is applied for all boards)

**TEST CASE:** board (SAMD) 🡪 4410

**RESULT**: Compiled successfully

**TEST CASE:** board (SAMD) 🡪4420

**RESULT:** Compiled successfully

**TEST CASE:** board (SAMD) 🡪4450

**RESULT:** compiled successfully

**TEST CASE:** board (SAMD) 🡪4460

**RESULT:** Compiled successfully

**TEST CASE:** board (SAMD) 🡪4470

**RESULT: C**ompiled successfully

Test the user-command example & the fsm example on the 4470:

(NOTE 1: For all boards US915 region applied)

(NOTE 2: 4470 board is applied for both sketches)

**TEST CASE:** Catena\_usercommand.ino

**RESULT:** Compiled successfully

**TEST CASE:** Catena\_fsm.ino

**RESULT:** Compiled successfully

Test each of the regions on the 4450:

(NOTE 1: 4450 board and Catena \_hello\_lora.ino is applied for all regions)

**TEST CASE:** Catena\_hello\_lora.ino 🡪 board (4450) 🡪 Region ( US 915 , EU 868 , AU 915 , AS 923 , AS 923 JP , IN866)

**TEST CASE 1:** Region (US 915)

**RESULT:** Compiled successfully

**TEST CASE 2:** Region (EU 868)

**RESULT:** Compiled successfully

**TEST CASE 3:** Region (AU 915)

**RESULT:** Compiled successfully

**TEST CASE 5:** Region (AS 923)

**RESULT:** Compiled successfully

**TEST CASE 6:** Region (AS 923 JP)

**RESULT:** Compiled successfully

**TEST CASE 7:** Region (IN 866)

**RESULT:** Compiled successfully

Test each of the STM32L0 boards:

(NOTE 1: For all boards US915 region applied)

(NOTE 2: Catena \_hello.ino and Catena\_hello\_lora.ino is applied for same boards)

**TEST CASE:**  Board (STM32) 🡪4551

**RESULT:** Compiled successfully

**TEST CASE:**  Board (STM32) 🡪4551

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4610

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4610

**RESULT: C**ompiled successfully

**TEST CASE:** Board (STM32) 🡪4611

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4611

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4612

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4612

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4617

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4617

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4618

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4618

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4630

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4630

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4801

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4801

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4801

**RESULT:** Compiled successfully

**TEST CASE:** Board (STM32) 🡪4802

**RESULT:** Compiled successfully

Test the user-command and fsm examples on the 4610:

(NOTE 1: Region US915 is applied for both the sketches)

(NOTE 2: 4610 board is applied for both the sketches)

**TEST CASE:** Catena\_usercommand.ino

**RESULT:** Compiled successfully

**TEST CASE:** Catena\_fsm.iio

**RESULT:** Compiled successfully

Test each of the regions for 4610 (using it as an example): US915 already tested)

(NOTE 1: Board 4610 is applied for all different kinds of regions)

(NOTE 2: Catena\_hello\_lora.ino is applied for all the cases)

**TEST CASE:** Region (EU 868)

**RESULT:** Compiled successfully

**TEST CASE:** Region (AU 915)

**RESULT:** Compiled successfully

**TEST CASE:** Region (AS 923)

**RESULT:** Compiled successfully

**TEST CASE:** Region (AS 923 JP)

**RESULT:** Compiled successfully

**TEST CASE:** Region (IN 866)

**RESULT:** Compiled successfully