

## PUBLICATIONS

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1. **Shiyamala S.** and Rajamani V. “A modified MAP decoder architecture for low power consumption using folded technique”, International Journal of Electronics, **Taylor and Francis**, Vol 100, issue 7, pp 900 – 912, 2013. (SCI indexed)
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3. Shiyamala S. and Rajamani V. “Performance analysis of a modified MAP decoder architecture for low power dissipation”, Circuits systems and Signal Processing , **Springer** Vol. 34, issue 12 , pp 3949–3964, Dec 2015. (SCI indexed)
4. **Shiyamala S.** and Rajamani V. “A novel area efficient folded modified convolutional interleaving architecture for MAP decoder”, International Journal of Computer Applications, Vol.9, No. 9, pp 18-22, Nov 2010.
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6. Murugesan, S. and **Shiyamala S.** “Soft error tolerant highly reliable multiport memory cell design” , International Journal of Advanced Technology in Engineering and Science, 2015.
7. Ezhuvaraja and **Shiyamala S.** , “Power Dissipation minimization in Datapath design using operand isolation circuits”, Vol 10, No 5, International Journal of Applied Engineering and Research , 2015(**Annexure II**)
8. T.Kavitha, **S.Shiyamala**, A.Farithkhan and J.Gnana Arun Johnson “Designing the Antenna for ISM band Application” International Journal of Applied Engineering Research, Vol.10, No.19, pp:40197-40202, 2015. (SCOPUS Indexed)
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10. T. Kavitha, **S.Shiyamala** and P.Nagarajan, “FPGA implementation arbiters algorithm for network-on-chip”, ARPN Journal of Engineering and Applied Sciences, Vol 11, No 19, Oct 2016.(SCOPUS Indexed)
11. P.Nagarajan, T. Kavitha and **S.Shiyamala**, “Efficient Timing Element Design Featuring Low Power VLSI Applications”, International Journal of Engineering and Technology, Vol 8, No 4, Aug 2016. (SCOPUS Indexed)
12. Kavitha T **Shiyamala S.** and V.Rajamani “Multipath Routing and Wavelength Assignment Technique in Optical WDM Mesh Networks”, Journal of Optical Communications, Vol.38 , Issue 4, Dec 2017, (SCOPUS Indexed)

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14. **S.Shiyamala**, T. Kavitha, "Real Time Monitoring and Controlling Device for Elderly Personnel with Hypoglycemia", International Journal of Electronics, Electrical and Computational System, Vol. 6, No. 9,2017.
15. T.Kavitha **S.Shiyamala**, , "Routing Protocols of Wireless Sensor Networks and Unmanned Aerial Vehicle", International Journal of Electronics, Electrical and Computational System, Vol. 6, No. 9,2017.
16. Ragul Vasant Nale, **S.Shiyamala**, " A Comparative Study on power Consumption in Smartphone Technology Through Communication Standards", IJCRT, Vol. 5, issue 3, Jan 2018.
17. Sai Suneel A and **Shiyamala S** . " Feasibility Investigation on Cognitive Radio Network Systems: Hearsay Algorithm" International Conference on Universal Computing , Communications and Data Engineering (CCODE2018 ) on Feb 7<sup>th</sup> 2018, SSRN, Tirupati.
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20. **S.Shiyamala**, T. Kavitha, "Design of low delay ring oscillator using CMOS", International Journal of Engineering and Technology. Vol 7, Issue 3, pp 334 - 336, 2018(SCOPUS Indexed)
21. **S.Shiyamala**, T. Kavitha, "A modified MAP decoder architecture for low power consumption and delay using unfolded technique", Journal of Advanced Research in Dynamical and Control Systems, Vol 10, Issue 9, pp 1159 -1166, 2018 (SCOPUS Indexed).
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24. Sai Suneel , **S.Shiyamala**, "Dynamic threshold selection through noise variance for spectrum sensing" International Journal of Engineering and Advanced Technology, Volume: 8,Issue :3, PP 719-724,PP :230-234,FEB 2019. (Scopus Indexed Journal)
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28. R.Kiran Kumar , **S.Shiyamala**, “A 2-D Analytical Modeling of Dual Work Function Metal Gate MOSFET using High-K Gate Dielectric with enhanced RF/Analog Performance for Low Power Applications”, Springer - Silicon , Sep, 2020 (**SCI – E Indexed**)
29. Priyanka halle , **S.Shiyamala**, “AMI and its Wireless Communication Security Aspects with QOS: A Review”, Springer Lecture Series, Volume: 8, Issue :10, PP 282-289, 2021 (Scopus Indexed)
30. Priyanka halle , **S.Shiyamala**, “Trust and Cryptography centered Privileged Routing providing Reliability for WSN considering DoS attack designed for AMI of Smart Grid ”, International Journal of Innovative Technology and Exploring Engineering (IJITEE), Volume: 8, Issue :10, PP 282-289, 2019 ( Non Scopus Indexed) 2021
31. Sai Suneel A and **Shiyamala S** . “PEAK DETECTION BASED ENERGY DETECTION OF A SPECTRUM UNDER RAYLEIGH FADING NOISE ENVIRONMENT” , 2021, (**SCI – E Indexed**)
32. R.Kiran Kumar , **S.Shiyamala**, “”Design and Performance of Charge-Plasma-Based Schottky –FET CMOS Circuit Ring Oscillator for High Density ICs, Springer - Silicon , Aug , 2021 (**SCI – E Indexed**)
33. Nithish Gaikwak and **Shiyamala S** . “Design and Development of Microarchitecture for Dynamic IoT Communication”, International Journal of Engineering Trends and Technology (IJETT), Volume-69 Issue-11, 2021, Scopus Indexed)