assignment 2

February 5, 2024

1. for the fallowing circuit, the correct logic values for the entries X2 and Y2 in the truth table are (PH2019,36)

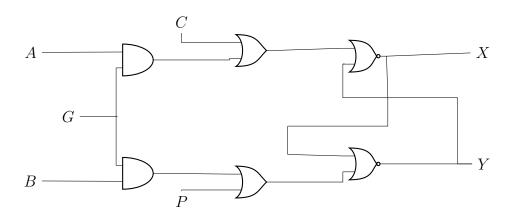


Figure 1: logic diagram

$ \mathbf{G} $	$ \mathbf{A} $	B	$\mid \mathbf{P} \mid$	$ \mathbf{C} $	$\mid \mathbf{X} \mid$	Y
1	0	1	0	0	0	1
0	0	0	1	0	X2	Y2
1	0	0	0	1	0	1

- (a) 1 and 0
- (b) 0 and 0
- (c) 0 and 1
- (d) 1 and 1